

FIG. 1

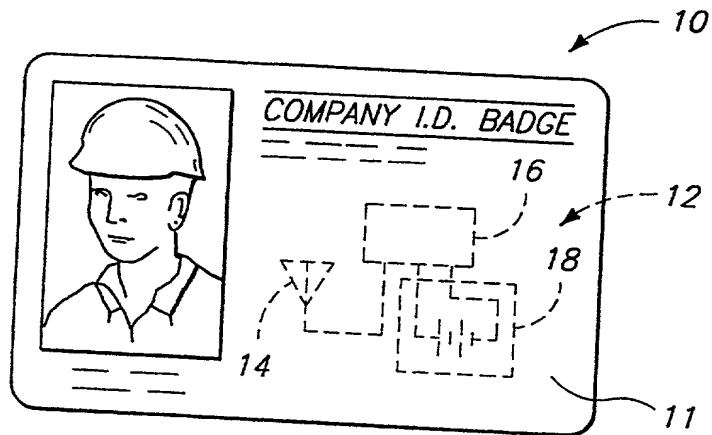
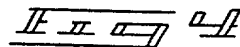
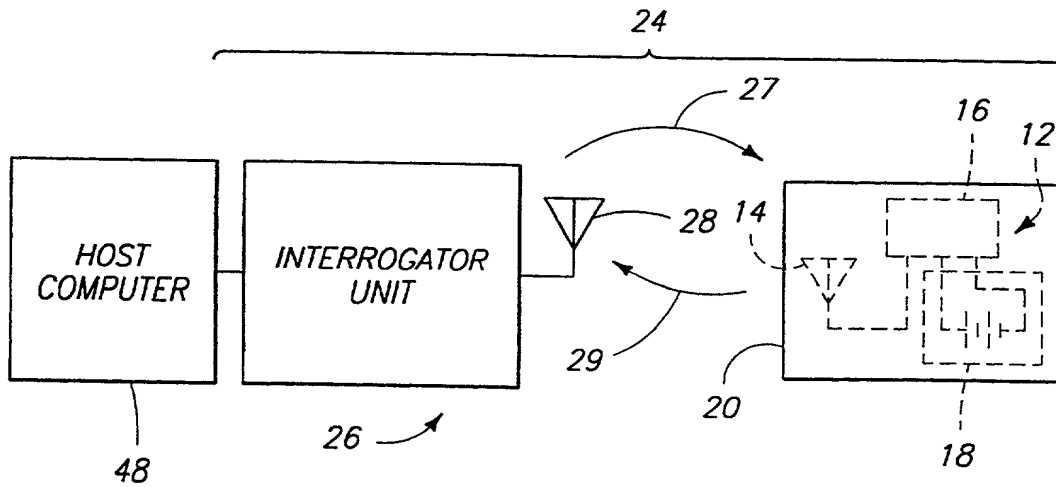
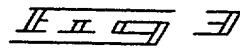
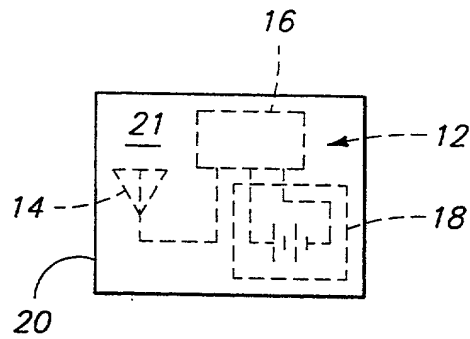


FIG. 2



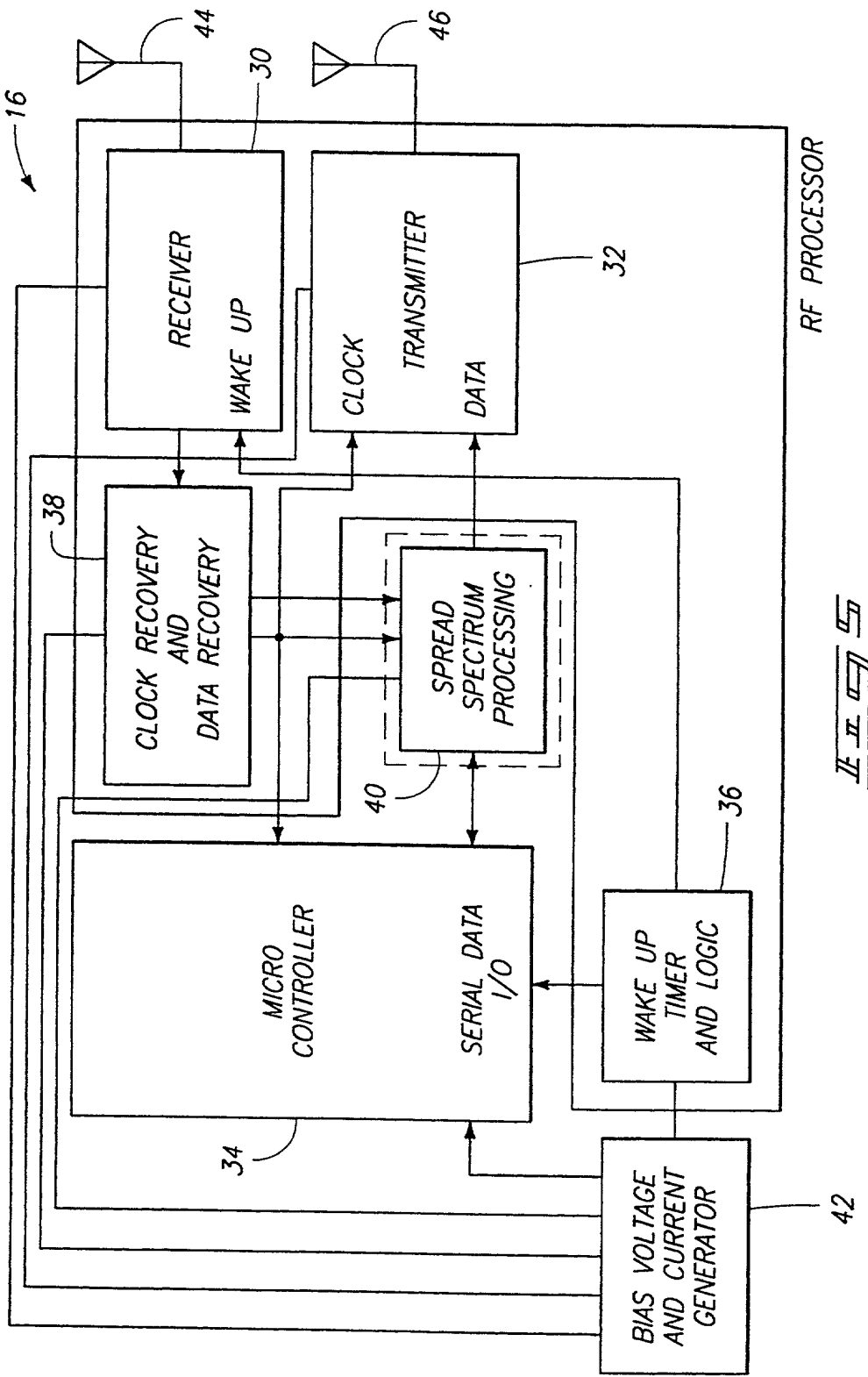


FIG. 1

6AA	6AB	6AC	6AD	6AE	6AF	6AG	6AH	6AI	6AJ	6AK
6BA	6BB	6BC	6BD	6BE	6BF	6BG	6BH	6BI	6BJ	6BK
6CA	6CB	6CC	6CD	6CE	6CF	6CG	6CH	6CI	6CJ	6CK
6DA	6DB	6DC	6DD	6DE	6DF	6DG	6DH	6DI	6DJ	6DK
6EA	6EB	6EC	6ED	6EE	6EF	6EG	6EH	6EI	6EJ	6EK

MI 40-030

6AA 6AB 6AC 6AD 6AE 6AF 6AG 6AH 6AI 6AJ 6AK



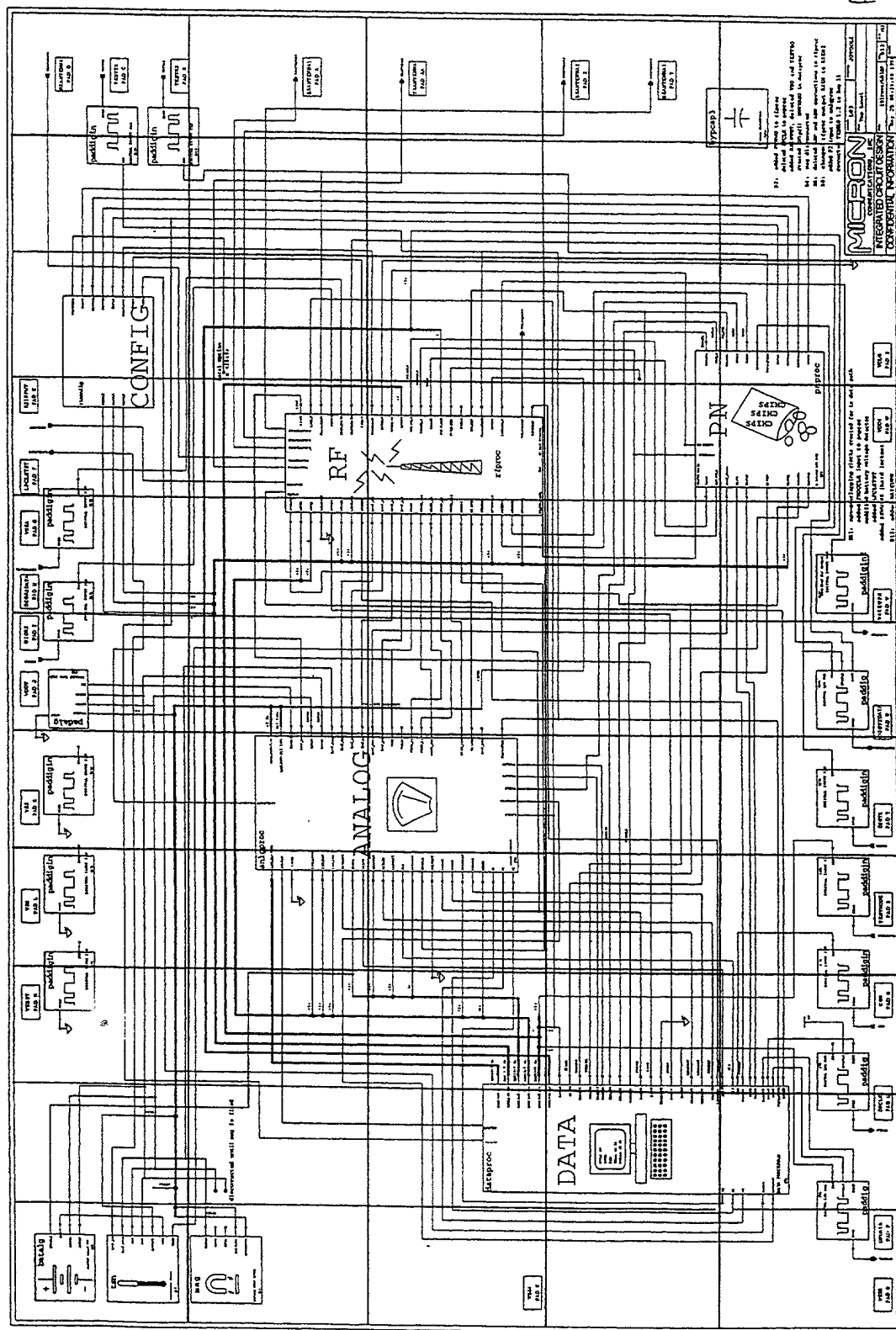


FIG. 6AA-EK

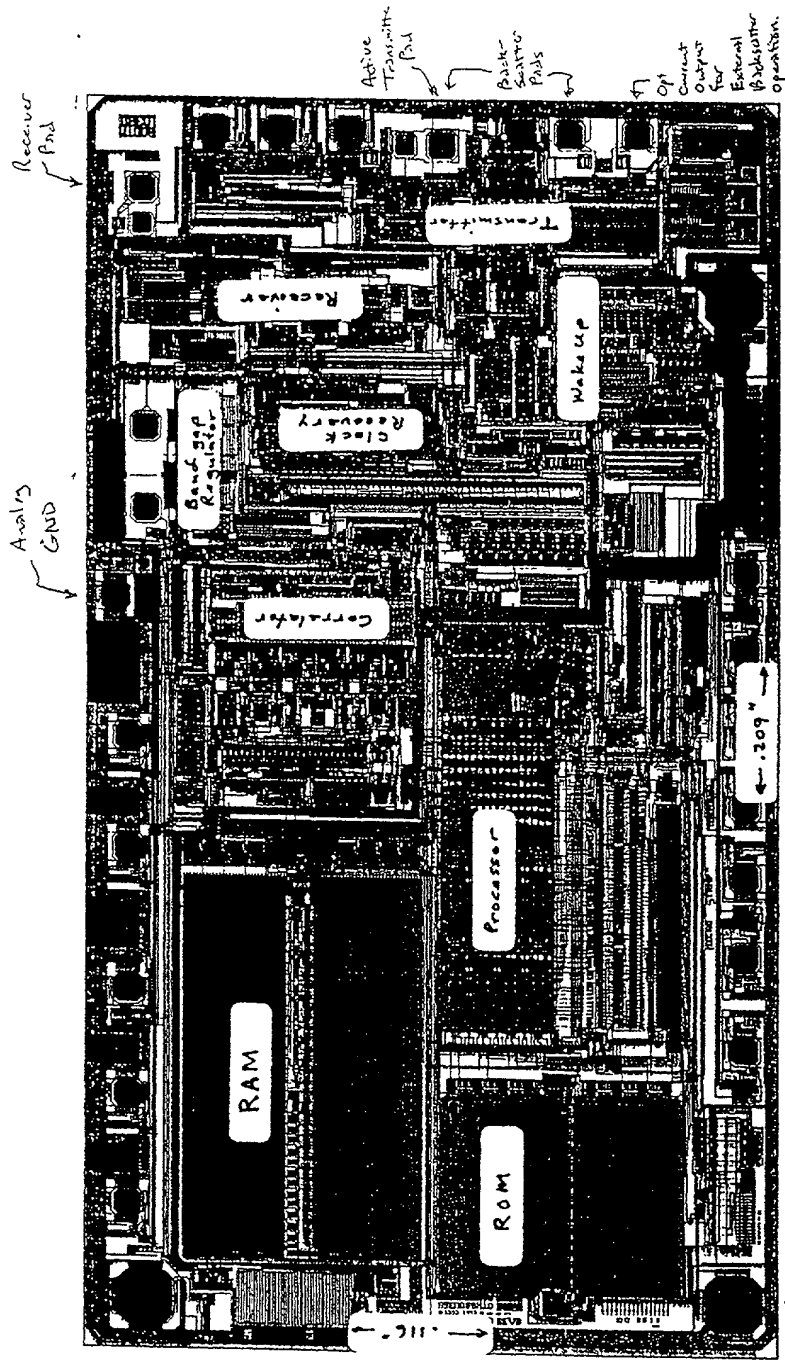
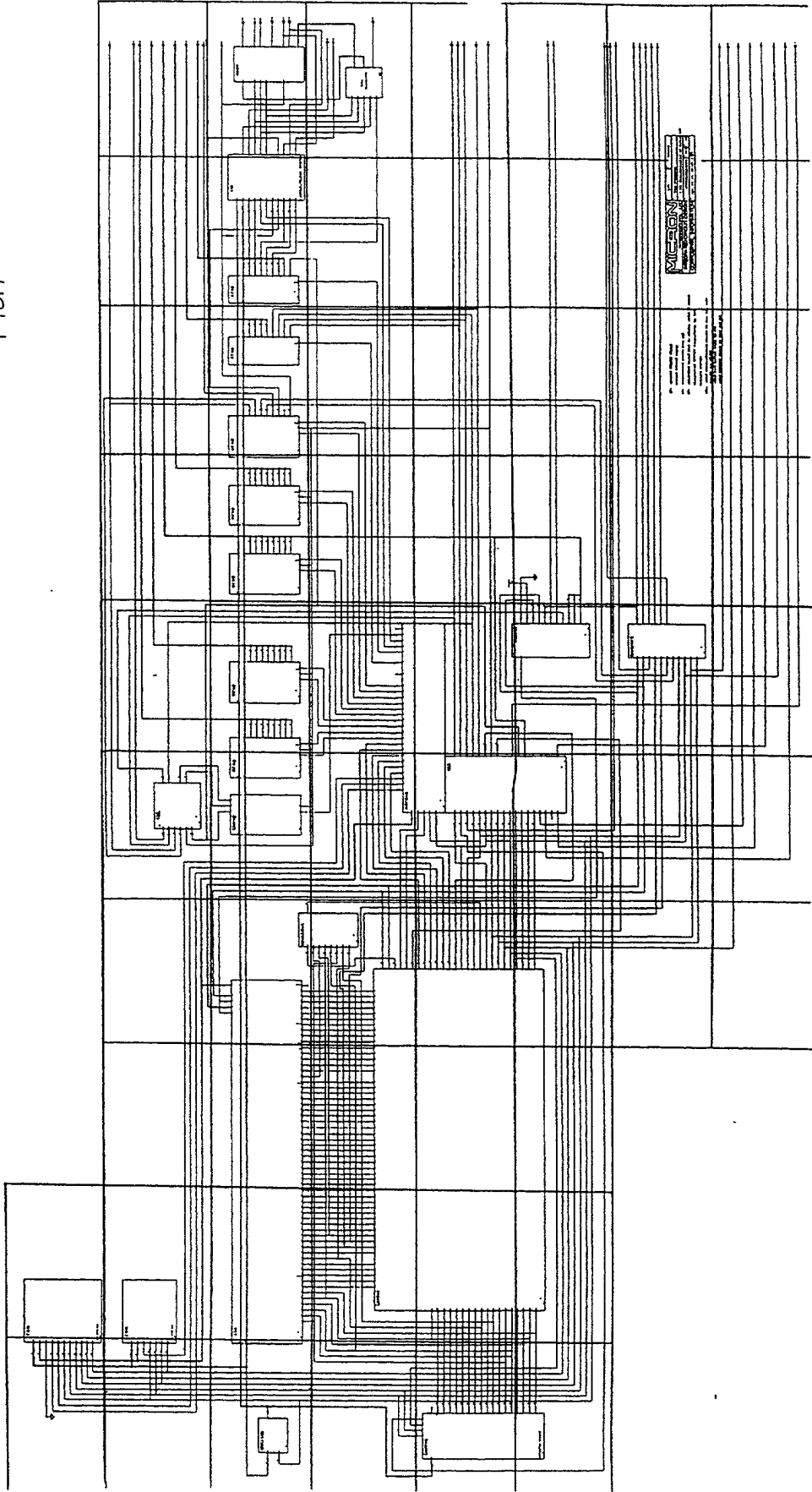


FIG. 6.01

[illegible]
$$\frac{\mathbb{Z} \oplus \mathbb{Z}}{\mathbb{Z} \oplus \mathbb{Z}}$$

FIG. 7



7.01AA	7.01AB
7.01BA	7.01BB



7.0101AA	7.0101AB
7.0101BA	7.0101BB

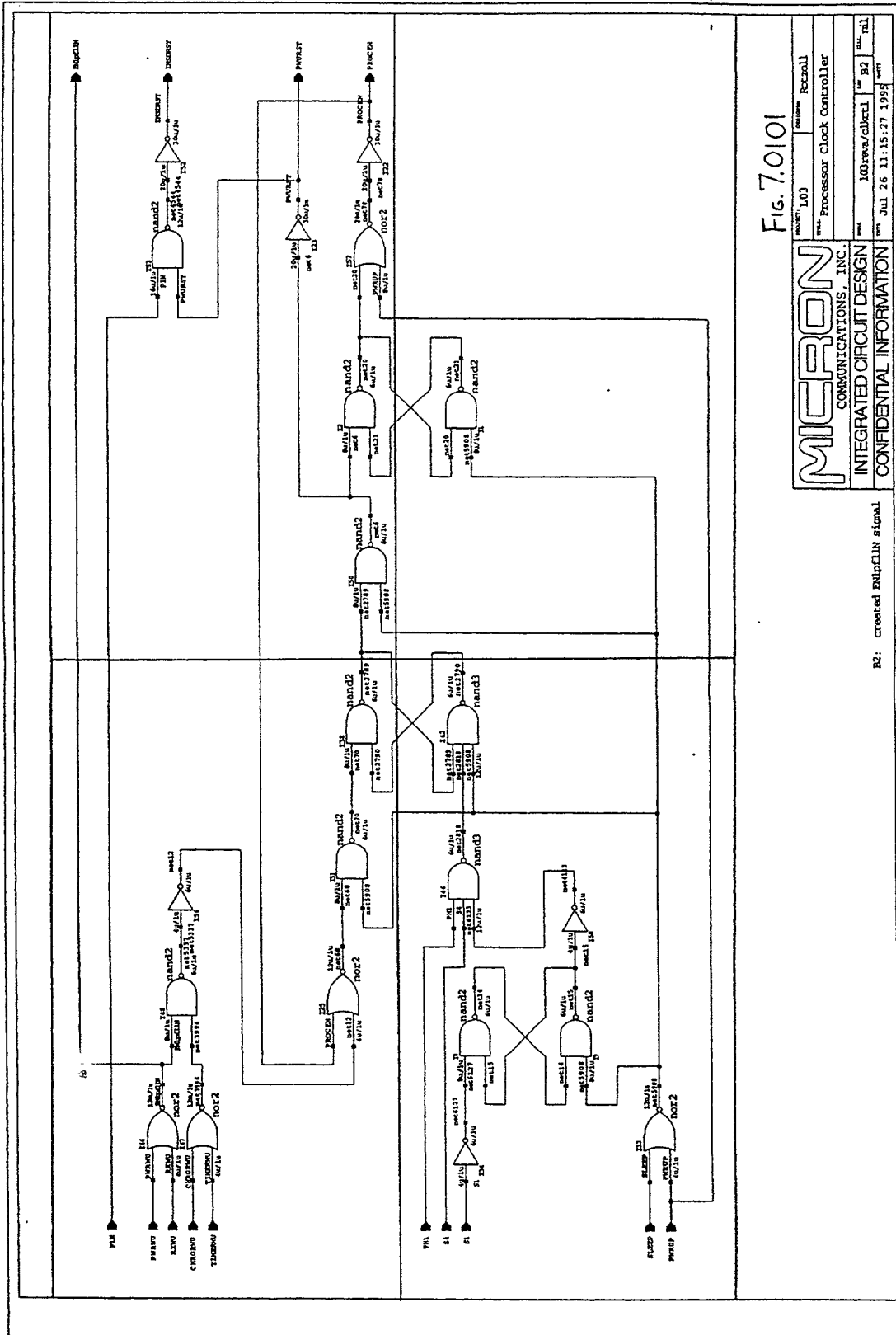


Fig. 7.0101

MICRON		COMMUNICATIONS, INC.	INTEGRATED CIRCUIT DESIGN	CONFIDENTIAL INFORMATION
PROJECT: L03	DESIGN: Recall	NAME: Processor Clock Controller	DATE: Jul 26 11:15:27 1995	REV: B2
				REV: rll

B2: created ENUPCLIN signal



7.0102BA	7.0102BB	7.0102BC	7.0102BD	7.0102BE	7.0102BF	7.0102BG	7.0102BH	7.0102BI	7.0102BJ
7.0102CA	7.0102CB	7.0102CC	7.0102CD	7.0102CE	7.0102CF	7.0102CG	7.0102CH	7.0102CI	7.0102CJ
7.0102DA	7.0102DB	7.0102DC	7.0102DD	7.0102DE	7.0102DF	7.0102DG	7.0102DH	7.0102DI	7.0102DJ

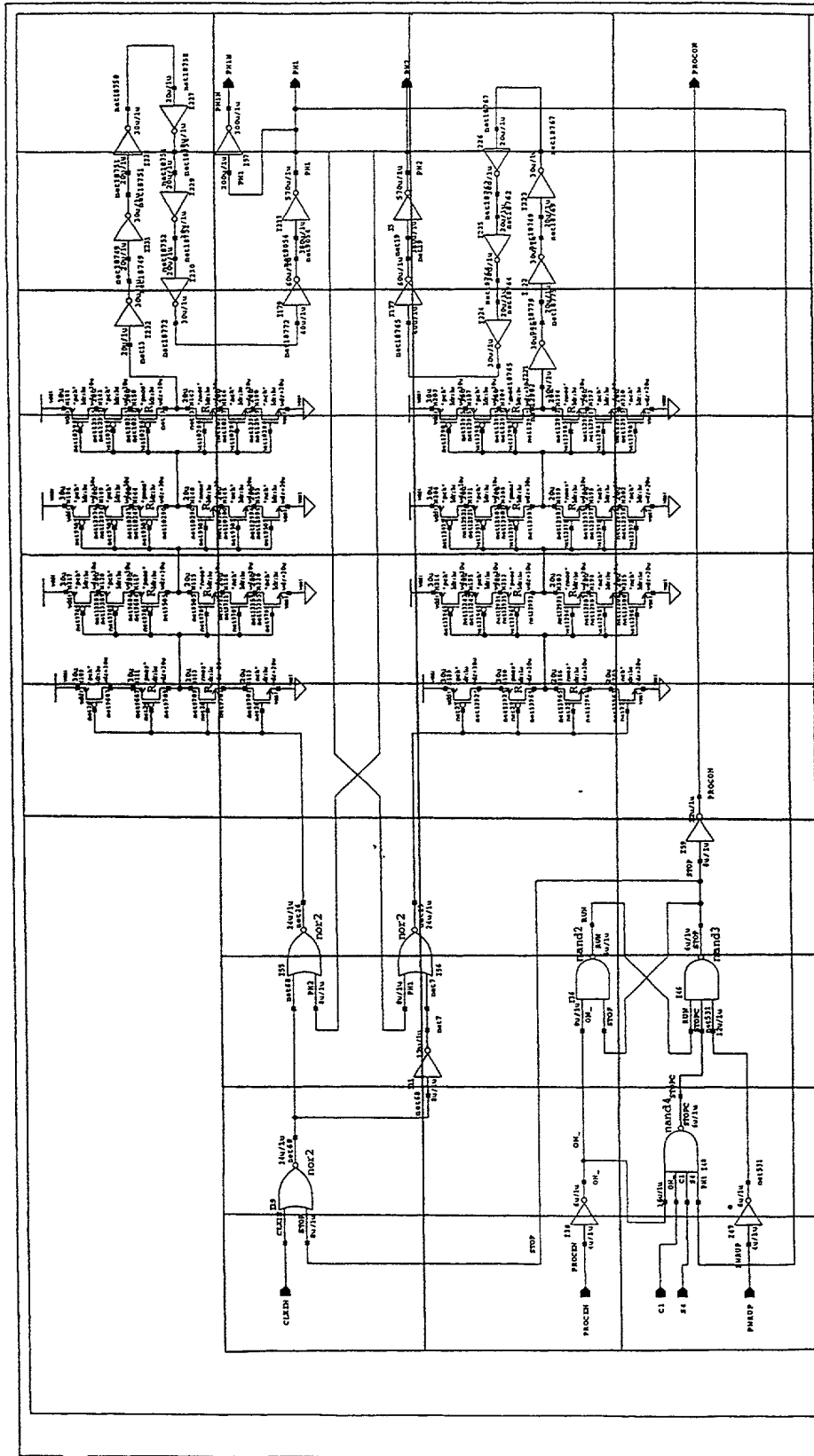


FIG. 7.0102

MICRON	PROJECT	L03	DESIGNER	Rozzoll
	FUNCTION	Processor Phase Generator		
	DESCRIPTION	2-Phase Non-overlapping		
	DATE	10/29/94	BY	B8
INTEGRATED CIRCUIT DESIGN				
CONFIDENTIAL INFORMATION				
Dec 5 17:55:56 1995				

B2: Pin name changes

B8: Added 6 inverters to non-overlap time

Make the number of inverters adjustable on metal

7.0103AA	7.0103AB	7.0103AC	7.0103AD
7.0103BA	7.0103BB	7.0103BC	7.0103BD

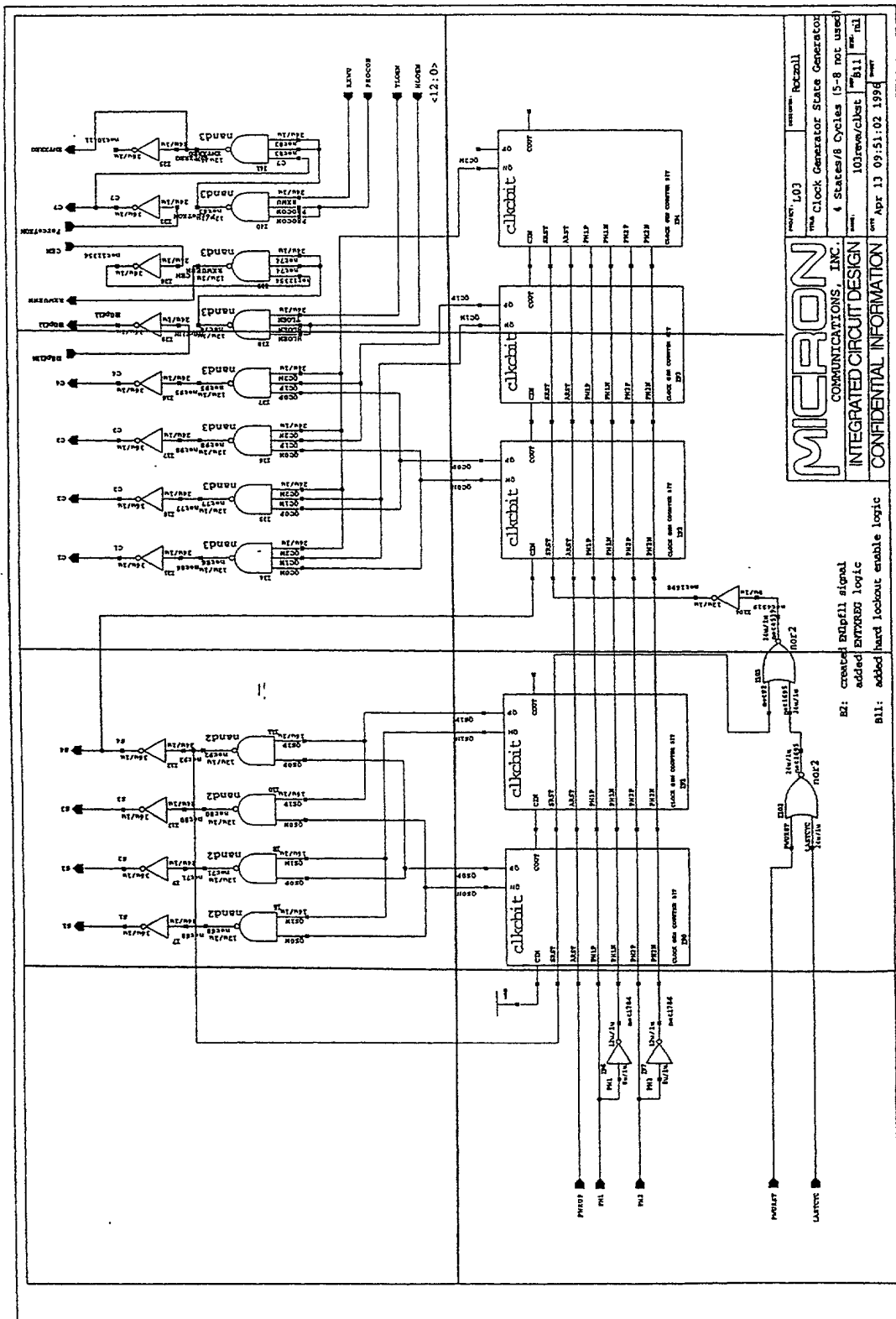


Fig. 7.0103

7.010301AA 7.010301AB

MI40-030

7.010301AA	7.010301AB
7.010301BA	7.010301BB

II II II II III II



7.02AA	7.02AB	7.02AC	7.02AD	7.02AE	7.02AF
7.02BA	7.02BB	7.02BC	7.02BD	7.02BE	7.02BF





7.03AA	7.03AB	7.03AC	7.03AD	7.03AE	7.03AF	7.03AG	7.03AH
7.03BA	7.03BB	7.03BC	7.03BD	7.03BE	7.03BF	7.03BG	7.03BH
7.03CA	7.03CB	7.03CC	7.03CD	7.03CE	7.03CF	7.03CG	7.03CH
7.03DA	7.03DB	7.03DC	7.03DD	7.03DE	7.03DF	7.03DG	7.03DH
7.03EA	7.03EB	7.03EC	7.03ED	7.03EE	7.03EF	7.03EG	7.03EH

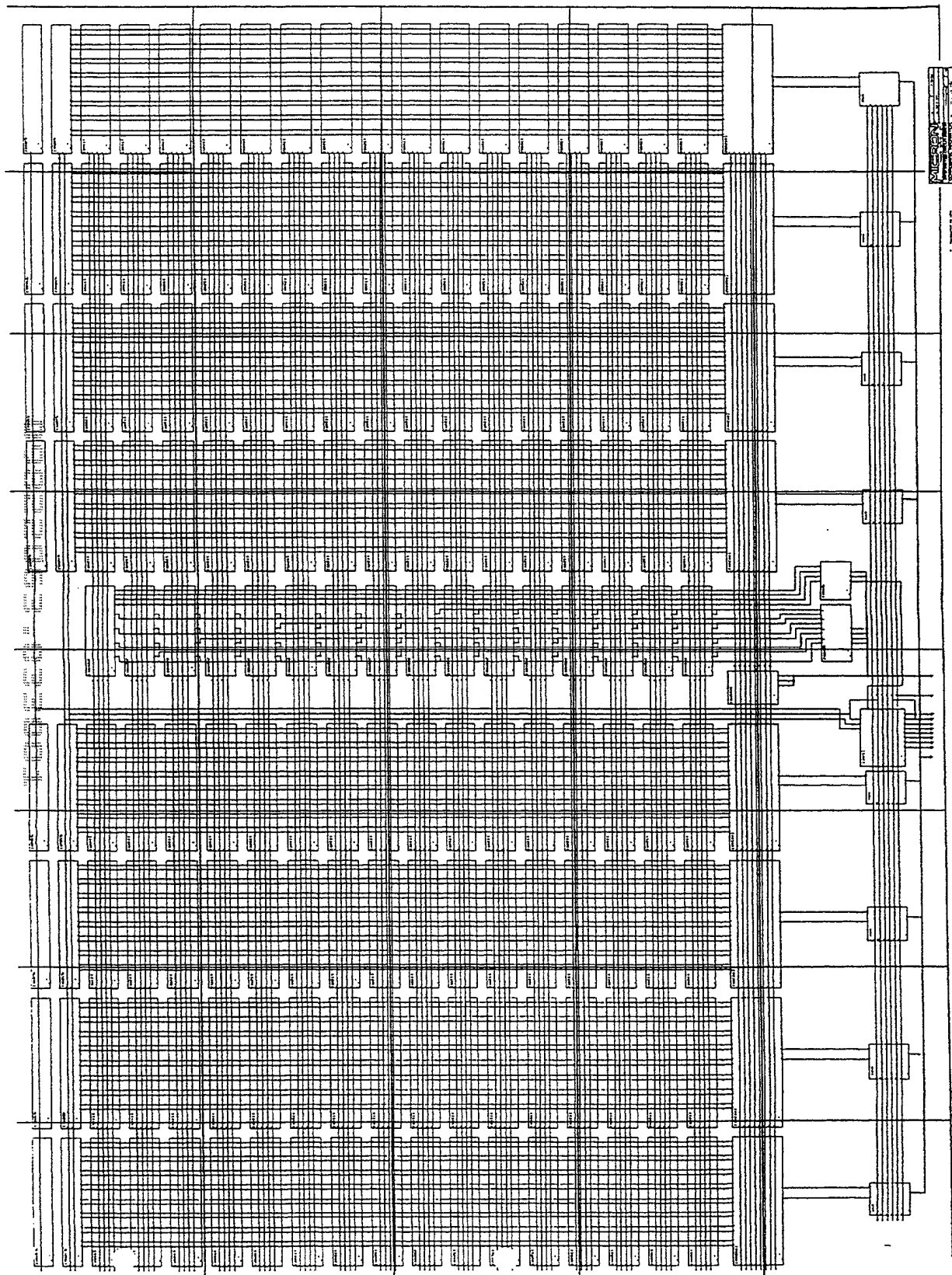


Fig 7.03



Rev 5.1

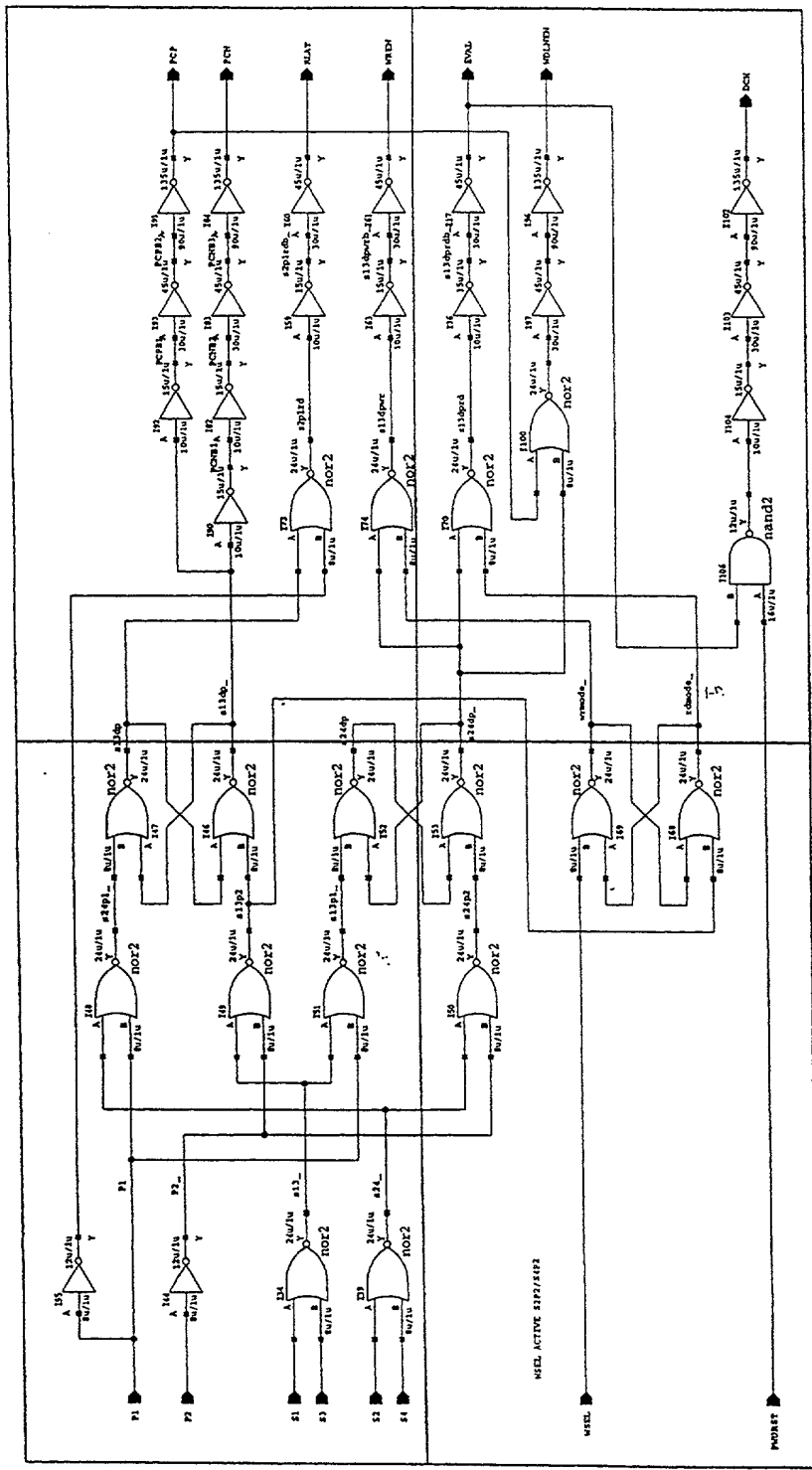


Fig. 7.0301

MICRON	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
PROJECT: L03 A63	DESIGN: REZ001
TITLE: RAM Control	
DATE: 10/26/84/REZ001	REV: 1
DATE: Feb 11 16:47:36 1994	DATE:

ALL INFORMATION CONTAINED HEREIN IS UNCLASSIFIED

7.0302AA	7.0302AB	7.0302AC
----------	----------	----------

END PAGE

CONFIDENTIAL

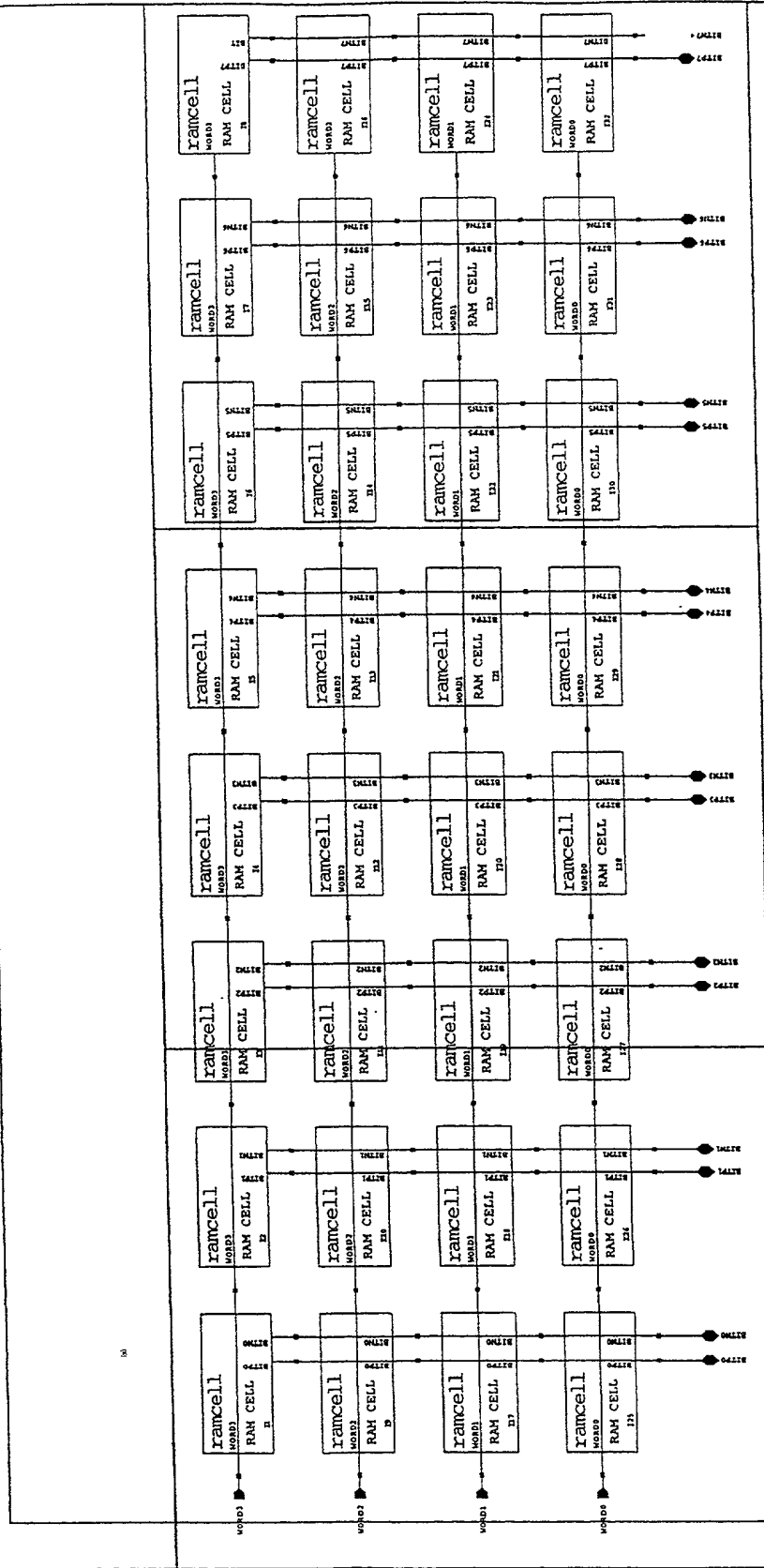
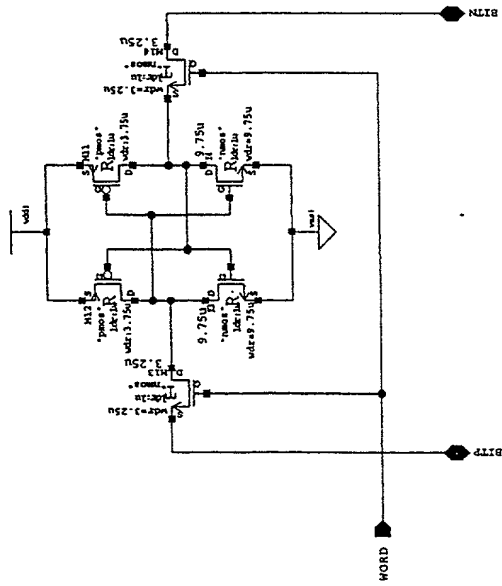


Fig. 7.0302

<b>MICRON</b> COMMUNICATIONS, INC. INTEGRATED CIRCUIT DESIGN	PROJECT: L03	DESIGNER: ROZELL
	TITLE: 8K4 RAM ARRAY	
	DATE: 10/10/84/ram8k4	REV: -
CONFIDENTIAL INFORMATION		DATE: Nov 6 11:34:37 1993



**NICRON**  
COMMUNICATIONS, INC.

## INTEGRATED CIRCUIT DESIGN

**CONFIDENTIAL INFORMATION**

PROJECT: 1.03	DESIGNER: Rotzoll
---------------	-------------------

TIME:	6T RAM Cell
-------	-------------

NAME:	103reva/rancell	REV:	-	STG:	A
-------	-----------------	------	---	------	---

DATE: NOV 6 11:34:48 1993

Fig. 7.030201

7.0303AA	7.0303AB	7.0303AC	7.0303AD
----------	----------	----------	----------

ITEM 7.0303





三

<b>MICRON</b>					
COMMUNICATIONS, INC.					
INTEGRATED CIRCUIT DESIGN					
CONFIDENTIAL INFORMATION					
PAYMENT NO.	103	REVISED	Retzoll		
TOTAL	RAM Precharge				
DATE	103 reva/rampch	NOV -	TIME	min	
Nov 12 02:58:36 1993					
PAGE 1 OF 1					

7.0304AA	7.0304AB	7.0304AC	7.0304AD
----------	----------	----------	----------

IL 11 11 11 11 11 11



DATE: 10/11/93 BY: [signature] FOR: [signature] REV: 1.0

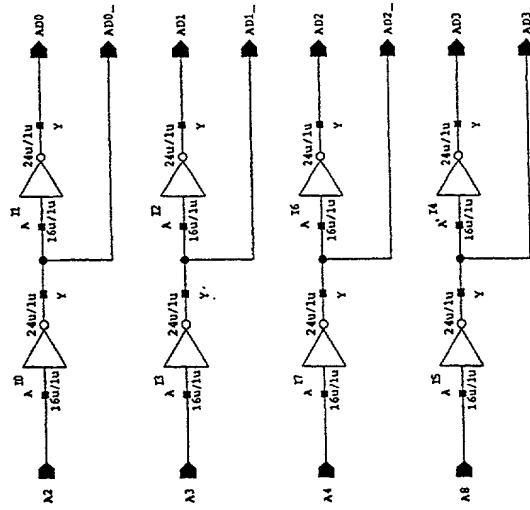


Fig. 7.0305

MICRON		PROJECT: L03	DESIGN: Rotzoll
COMMUNICATIONS, INC.		TITLE: RAM Address Buffer	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/ramadb	REV: -
CONFIDENTIAL INFORMATION		DATE: Sep 29 16:04:01 1993	SHEET: A

MI40-030

7.0306AA

7.0306BA

7.0306



7.0307BB

Итого 7.03007

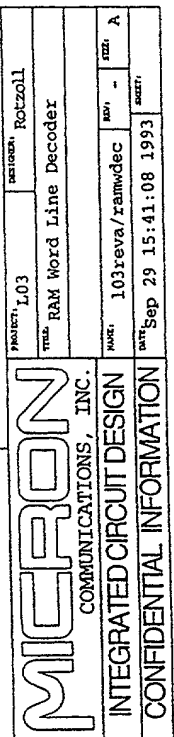


FIG. 7.0307



code 7.0308

7.0308AA	7.0308AB
7.0308BA	7.0308BB

7.0308

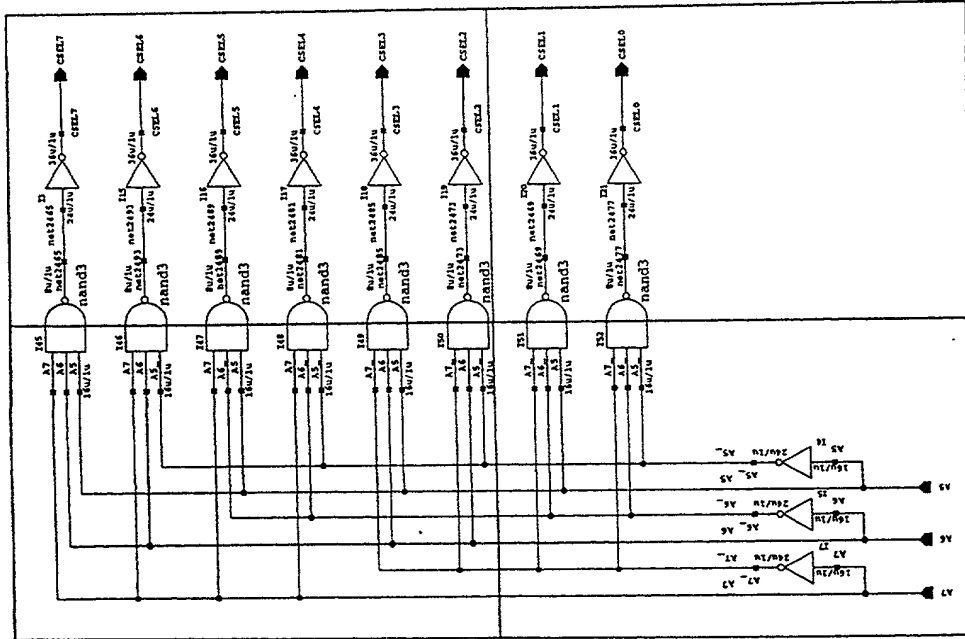


FIG. 7.0308

<b>MICRON</b>		PART NO. L03	REVISION 10/20/91
COMMUNICATIONS, INC.		TITLE: RAM Column Select Decode	
INTEGRATED CIRCUIT DESIGN		PAGE 3 to 8	
CONFIDENTIAL INFORMATION		DATE: 10/23/91	REV: 1
		DATE: Nov 5 17:21:07 1991	REV: 1

7.0309AA	7.0309AB	7.0309AC	7.0309AD	7.0309AE	7.0309AF	7.0309AG
7.0309BA	7.0309BB	7.0309BC	7.0309BD	7.0309BE	7.0309BF	7.0309BG

SECRET

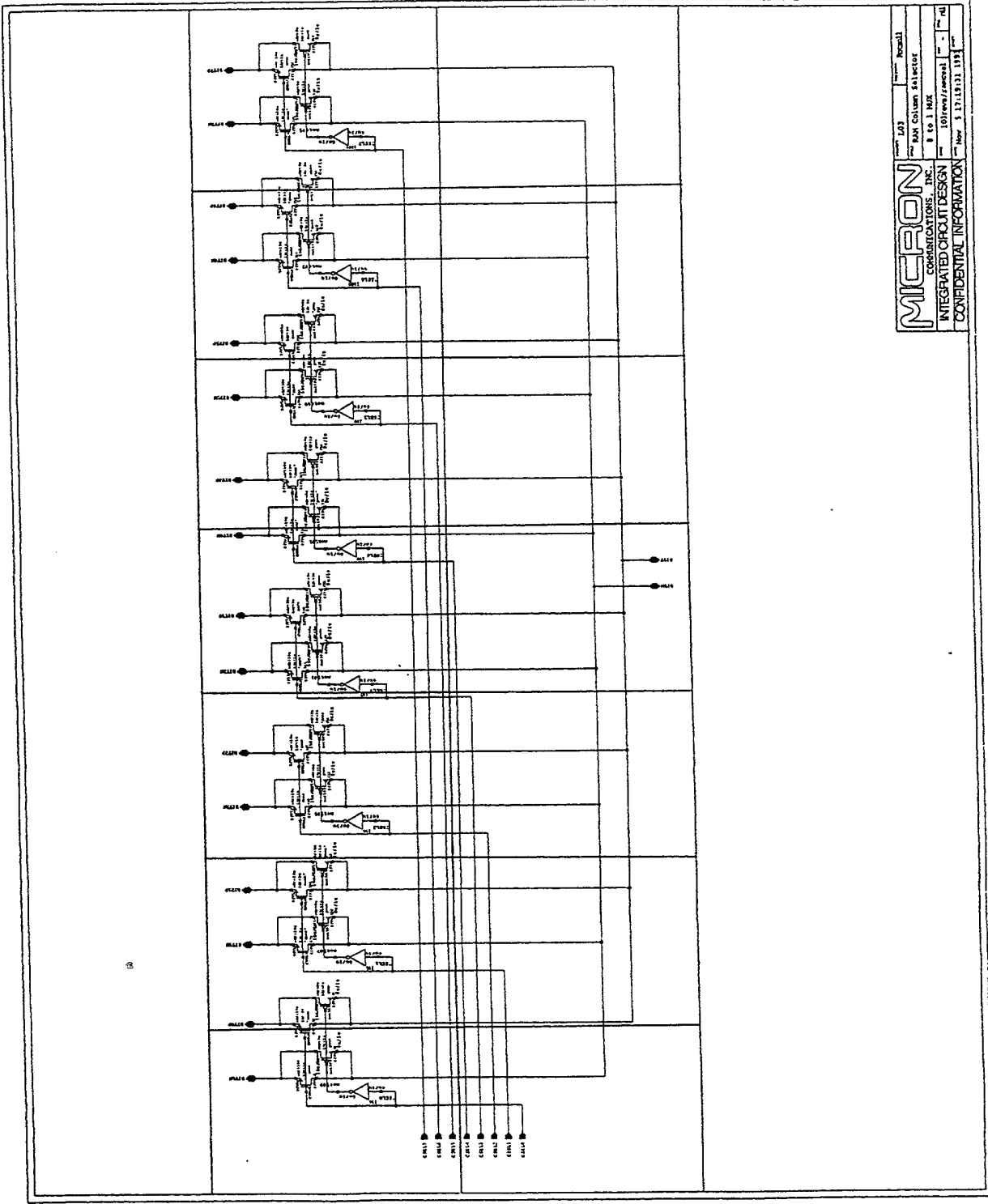


Fig. 7.0309

**MICRON**  
CORPORATION, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

Rev	Date	By	Check
1.0	10/10/77	101000/000001	101000/000001
1.1	11/13/77	101000/000001	101000/000001

7.0310AA	7.0310AB
7.0310BA	7.0310BB

Variable	Mean	SD	Min	Max	Skewness	Kurtosis	Normality
Age	35.2	12.5	18	65	0.15	3.2	0.98
Gender	1.2	0.4	1	2	0.05	3.0	0.99
Education	12.5	2.1	8	16	0.25	3.5	0.97
Income	1500	500	500	3000	0.35	3.8	0.96
Marital Status	1.5	0.5	1	2	0.10	3.1	0.98
Occupation	2.5	1.2	1	5	0.20	3.4	0.97
Health Status	1.8	0.6	1	3	0.12	3.2	0.98
Stress Level	3.2	1.5	1	5	0.30	3.9	0.95
Life Satisfaction	4.5	1.0	3	6	0.18	3.3	0.97
Resilience	2.8	1.1	1	5	0.22	3.6	0.96
Optimism	3.5	1.2	1	5	0.28	3.7	0.94
Emotional Stability	2.5	1.0	1	5	0.15	3.2	0.98
Self-Esteem	3.0	1.1	1	5	0.20	3.5	0.96
Life Purpose	3.8	1.3	1	5	0.32	4.0	0.93
Gratitude	4.2	1.1	3	5	0.10	3.1	0.98
Forgiveness	3.5	1.2	1	5	0.25	3.6	0.95
Empathy	3.0	1.0	1	5	0.18	3.3	0.97
Compassion	3.2	1.1	1	5	0.22	3.4	0.96
Kindness	3.5	1.2	1	5	0.28	3.7	0.94
Generosity	3.0	1.0	1	5	0.15	3.2	0.98
Patience	3.8	1.3	1	5	0.32	4.0	0.93
Humility	3.5	1.2	1	5	0.25	3.6	0.95
Modesty	3.0	1.0	1	5	0.18	3.3	0.97
Meekness	3.2	1.1	1	5	0.22	3.4	0.96
Gentleness	3.5	1.2	1	5	0.28	3.7	0.94
Mildness	3.0	1.0	1	5	0.15	3.2	0.98
Peacefulness	3.8	1.3	1	5	0.32	4.0	0.93
Calming	3.5	1.2	1	5	0.25	3.6	0.95
Tranquility	3.0	1.0	1	5	0.18	3.3	0.97
Serenity	3.2	1.1	1	5	0.22	3.4	0.96
Harmony	3.5	1.2	1	5	0.28	3.7	0.94
Balance	3.0	1.0	1	5	0.15	3.2	0.98
Stability	3.8	1.3	1	5	0.32	4.0	0.93
Consistency	3.5	1.2	1	5	0.25	3.6	0.95
Reliability	3.0	1.0	1	5	0.18	3.3	0.97
Trustworthiness	3.2	1.1	1	5	0.22	3.4	0.96
Integrity	3.5	1.2	1	5	0.28	3.7	0.94
Honesty	3.0	1.0	1	5	0.15	3.2	0.98
Openness	3.8	1.3	1	5	0.32	4.0	0.93
Curiosity	3.5	1.2	1	5	0.25	3.6	0.95
Imagination	3.0	1.0	1	5	0.18	3.3	0.97
Creativity	3.2	1.1	1	5	0.22	3.4	0.96
Innovation	3.5	1.2	1	5	0.28	3.7	0.94
Adaptability	3.0	1.0	1	5	0.15	3.2	0.98
Flexibility	3.8	1.3	1	5	0.32	4.0	0.93
Resilience	3.5	1.2	1	5	0.25	3.6	0.95
Endurance	3.0	1.0	1	5	0.18	3.3	0.97
Persistence	3.2	1.1	1	5	0.22	3.4	0.96
Determination	3.5	1.2	1	5	0.28	3.7	0.94
Commitment	3.0	1.0	1	5	0.15	3.2	0.98
Dedication	3.8	1.3	1	5	0.32	4.0	0.93
Devotion	3.5	1.2	1	5	0.25	3.6	0.95
Loyalty	3.0	1.0	1	5			

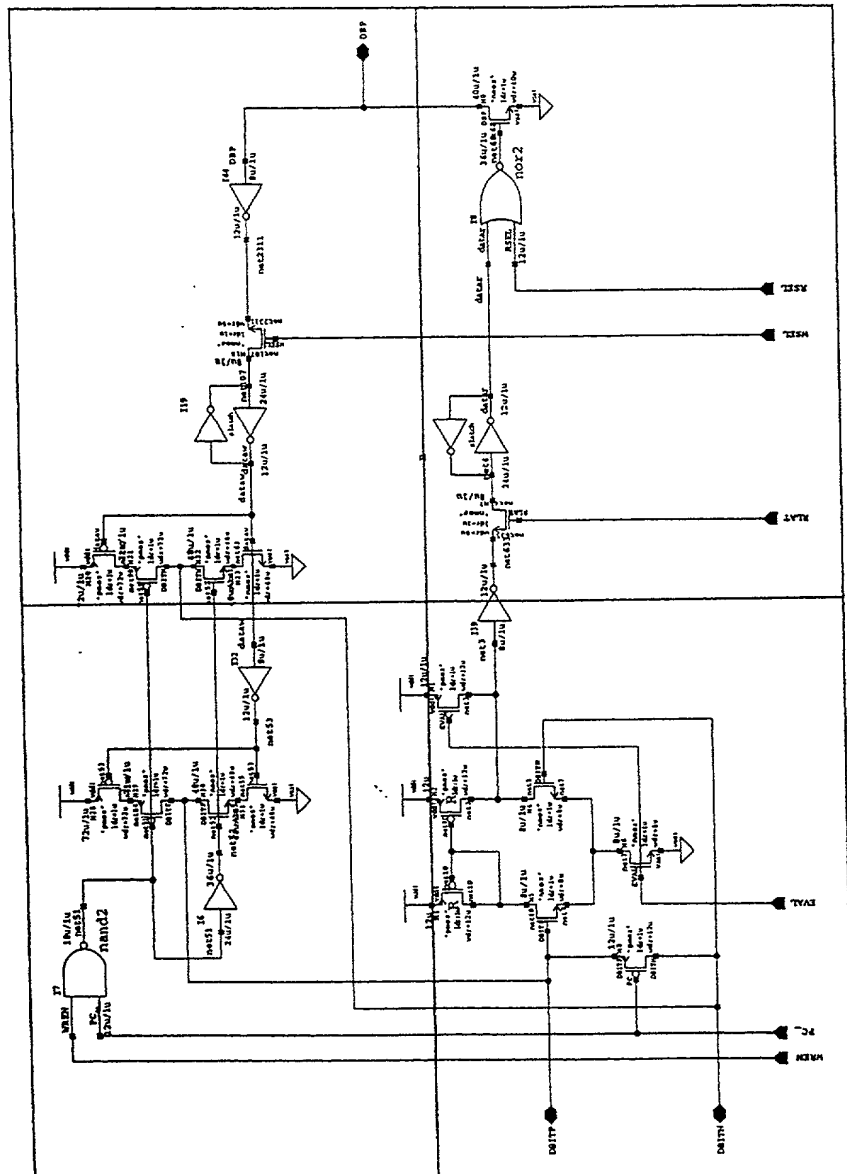
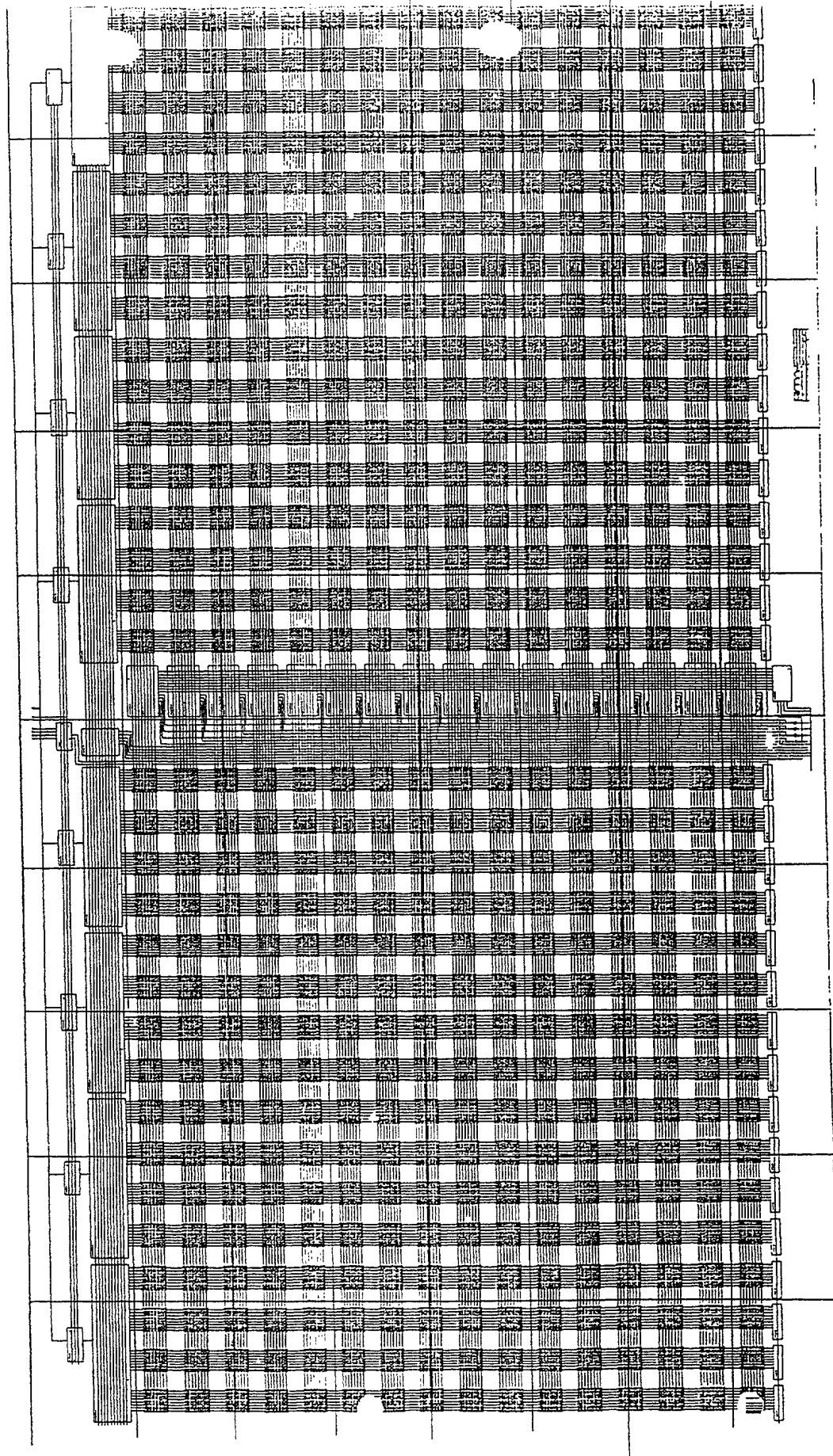


Fig 7.0310

<b>MICRON</b>		1.03		Rotzoll	
COMMUNICATIONS, INC.		THE RAM DATABASE INTERFACE			
INTEGRATED CIRCUIT DESIGN		103revts/ramdb		REV -	
CONFIDENTIAL INFORMATION		DATE		DATE	
		OCT 6 12:08:33 1993			

И. П. И.

FIG. 7.04





7.0401AA	7.0401AB
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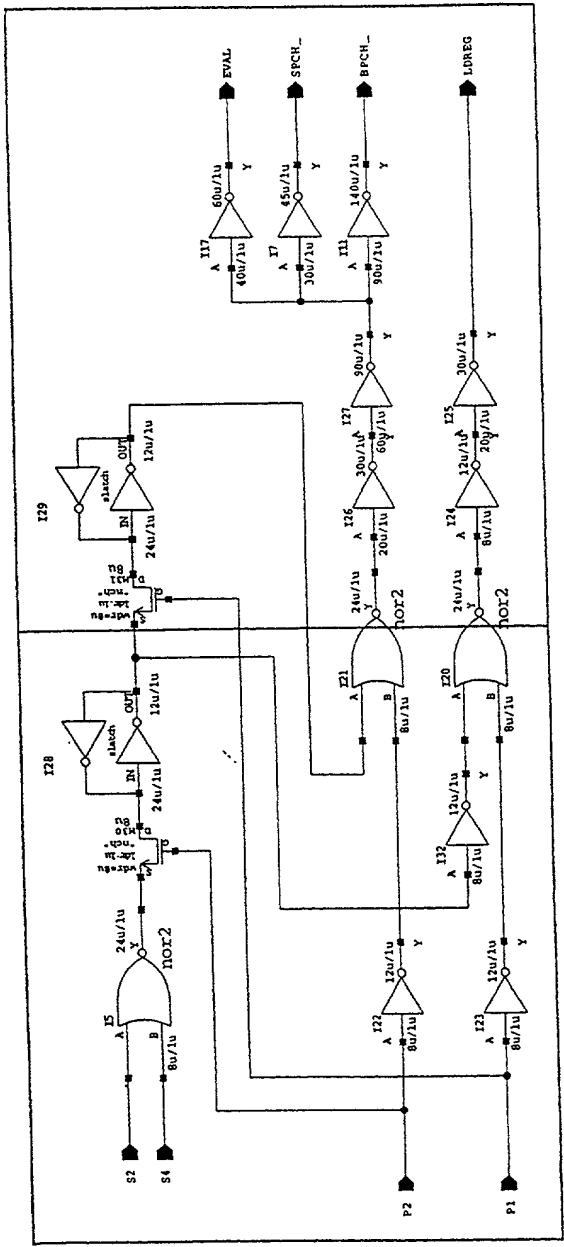


FIG. 7.0401

MICRON		COMMUNICATIONS, INC.		PROJECT: L03		DESIGNER: Rotzoll	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/romctl		REV: -		SHEET: A	
CONFIDENTIAL INFORMATION		DATE: Oct 3 13:16:28 1993		TITLE: ROM Control Logic			

**MICRON**  
 COMMUNICATIONS, INC.  
 INTEGRATED CIRCUIT DESIGN

7.0402AB

7.0402AA

EX 07 7.0402

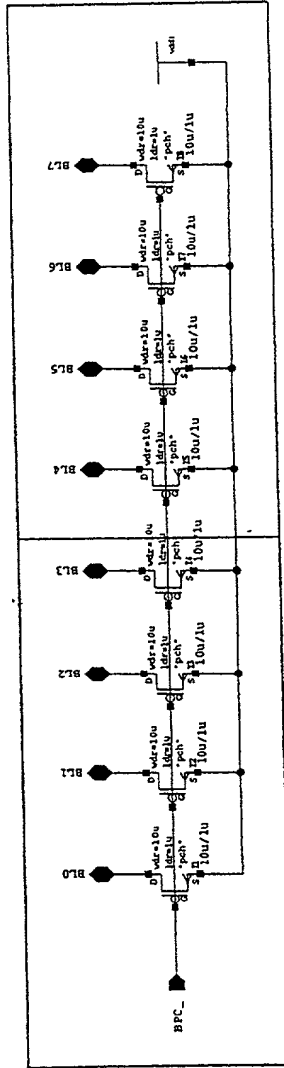


FIG. 7.0402

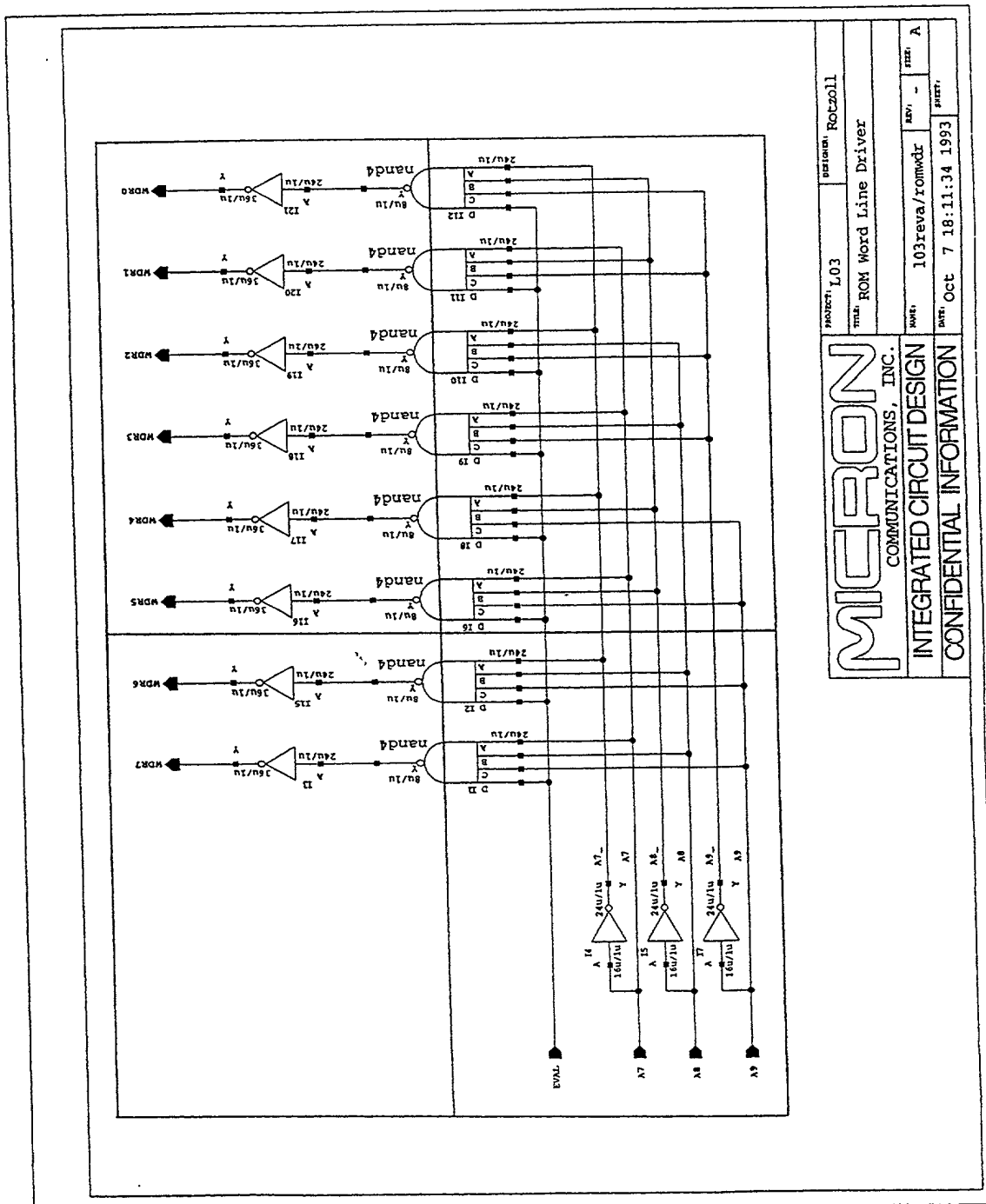
MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: ROM Bit Line Precharge	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/rompch	REV: -
CONFIDENTIAL INFORMATION		DATE: Oct 7 18:09:48 1993	SIZE: A
		SHEET:	

7.0403AA 7.0403AB

7.0403AA	7.0403AB
7.0403BA	7.0403BB

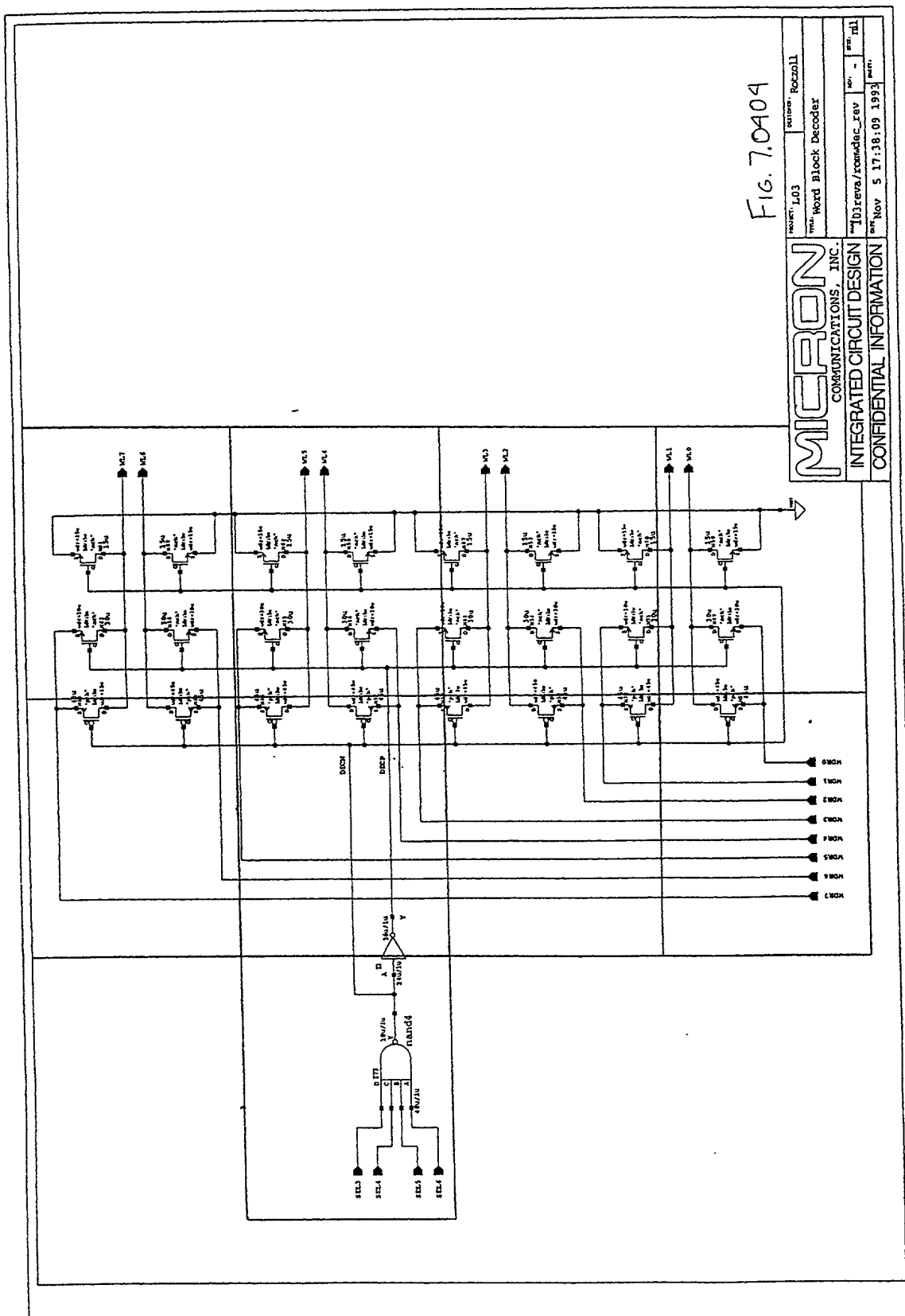
7.0403AA 7.0403AB

Fig. 7.0903



	7.0404AB	7.0404AC
7.0404BA	7.0404BB	7.0404BC
	7.0404CB	7.0404CC
	7.0404DB	7.0404DC

7.0404





7.0405AA

7.0405BA

Fig 7.0405

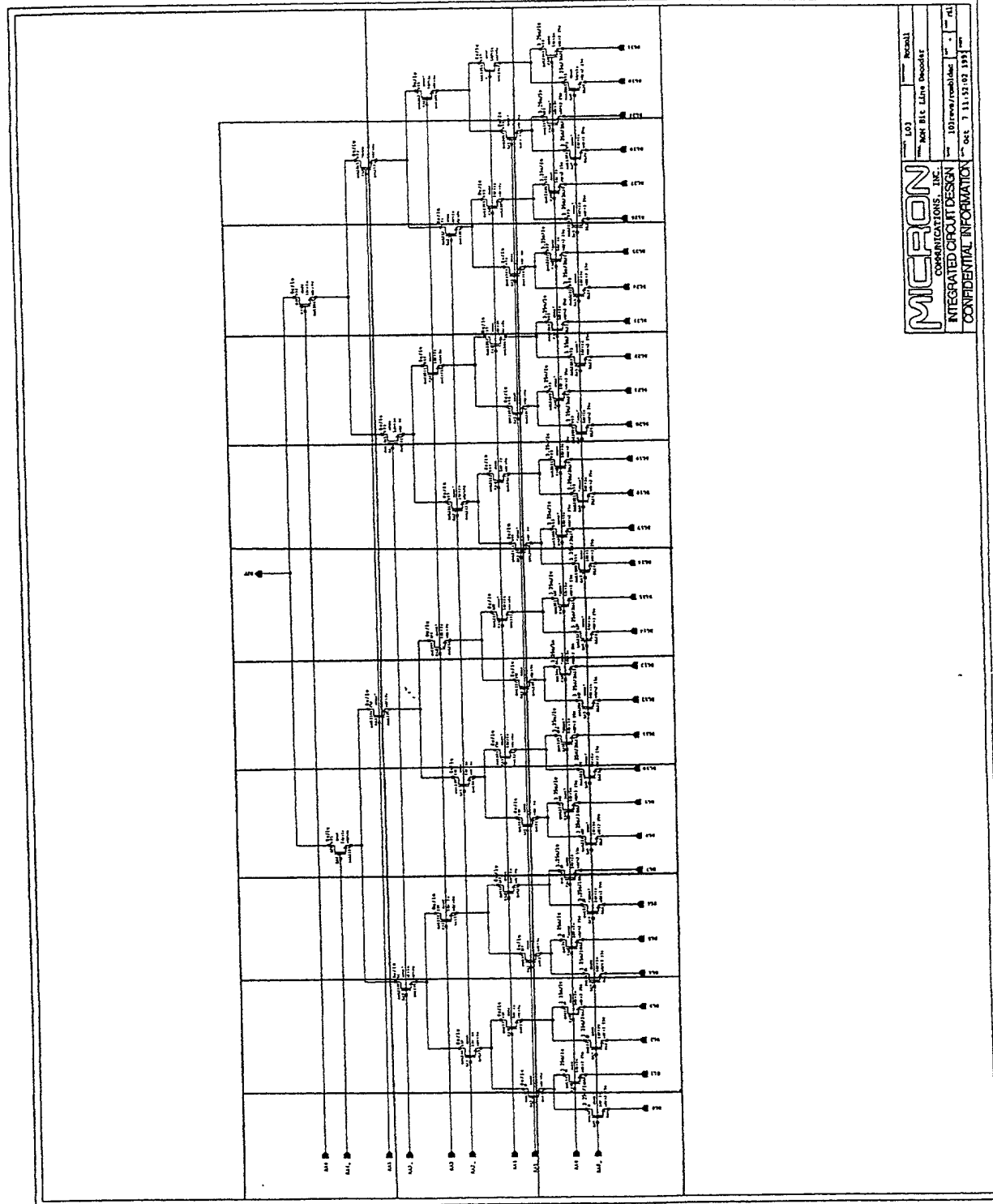


7.0406AA	7.0406AB	7.0406AC	7.0406AD	7.0406AE	7.0406AF	7.0406AG	7.0406AH	7.0406AI	7.0406AJ	
7.0406BA	7.0406BB	7.0406BC	7.0406BD	7.0406BE	7.0406BF	7.0406BG	7.0406BH	7.0406BI	7.0406BJ	7.0406BK
7.0406CA	7.0406CB	7.0406CC	7.0406CD	7.0406CE	7.0406CF	7.0406CG	7.0406CH	7.0406CI	7.0406CJ	7.0406CK

IL 11 000 000 000

6 000 000 000 000

SECRET



<b>MICRON</b>	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
Part No.	101
Rev.	Rev. 1.0
Doc. No.	101-101-101
Doc. Date	Oct. 7 11:52:02 1981

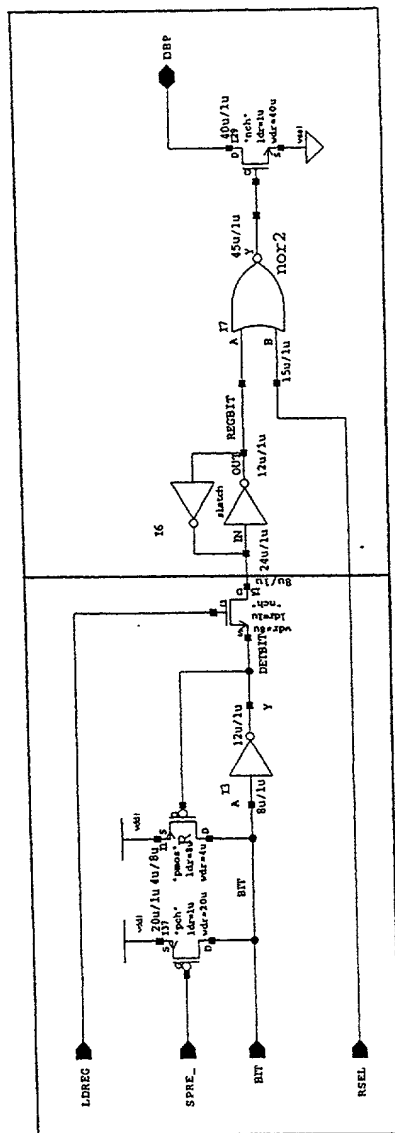
Fig. 7.0406

7.0407AB

7.0407AA

EX 7.0407

FIG. 7.0407



PROJECT: L03		DESIGNER: Rotzoll	
TITLE: ROM Sense Amplifier			
NAME: 103reva/romsns		REV: --	SHEET: A
DATE Oct 7 18:12:58 1993		SHEET: 1	

7.05AA	7.05AB
7.05BA	7.05BB
7.05CA	7.05CB

Fig 7.05

CONFIDENTIAL

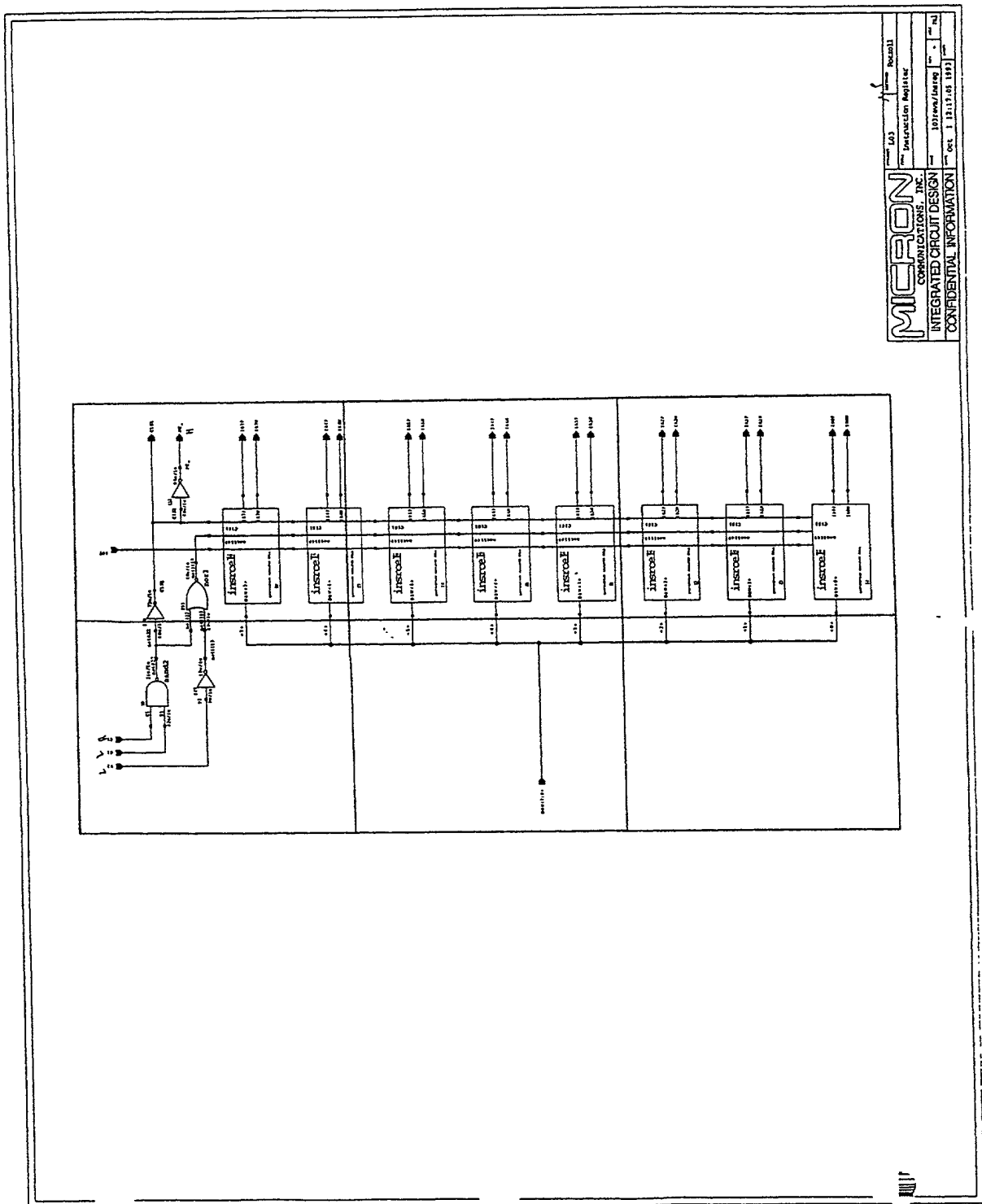


Fig. 7.05

<b>MICRON</b>		Doc 12.17.05.1893
COMMUNICATIONS, INC.		
INTEGRATED CIRCUIT DESIGN		
CONFIDENTIAL INFORMATION		



ALL INFORMATION CONTAINED  
HEREIN IS UNCLASSIFIED

8

7.0501AA	7.0501AB
----------	----------

EX-67 7.05011

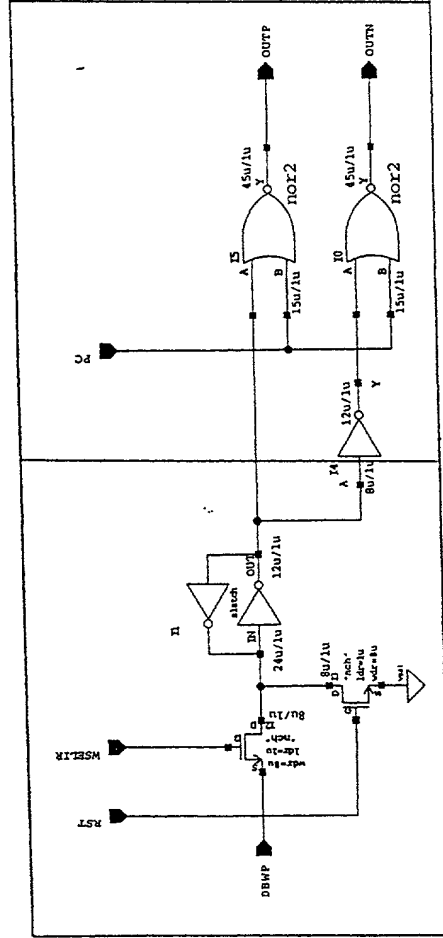


Fig. 7.0501

PROJECT: L03	DESIGNER: Rotzoll
TITLE: Instruction Register Cell	
NAME: 103reva/insrcel	REV: -
DATE: Oct 5 20:12:49 1993	SIZE: A
CONFIDENTIAL INFORMATION	

7.06AA	7.06AB	7.06AC	7.06AD	7.06AE	7.06AF	7.06AG	7.06AH	7.06AI	7.06AJ	7.06AK	7.06AL	7.06AM	7.06AN
	7.06BB	7.06BC	7.06BD	7.06BE	7.06BF	7.06BG	7.06BH	7.06BI	7.06BJ	7.06BK	7.06BL	7.06BM	7.06BN
7.06CA	7.06CB	7.06CC	7.06CD	7.06CE	7.06CF	7.06CG	7.06CH	7.06CI	7.06CJ	7.06CK	7.06CL	7.06CM	7.06CN

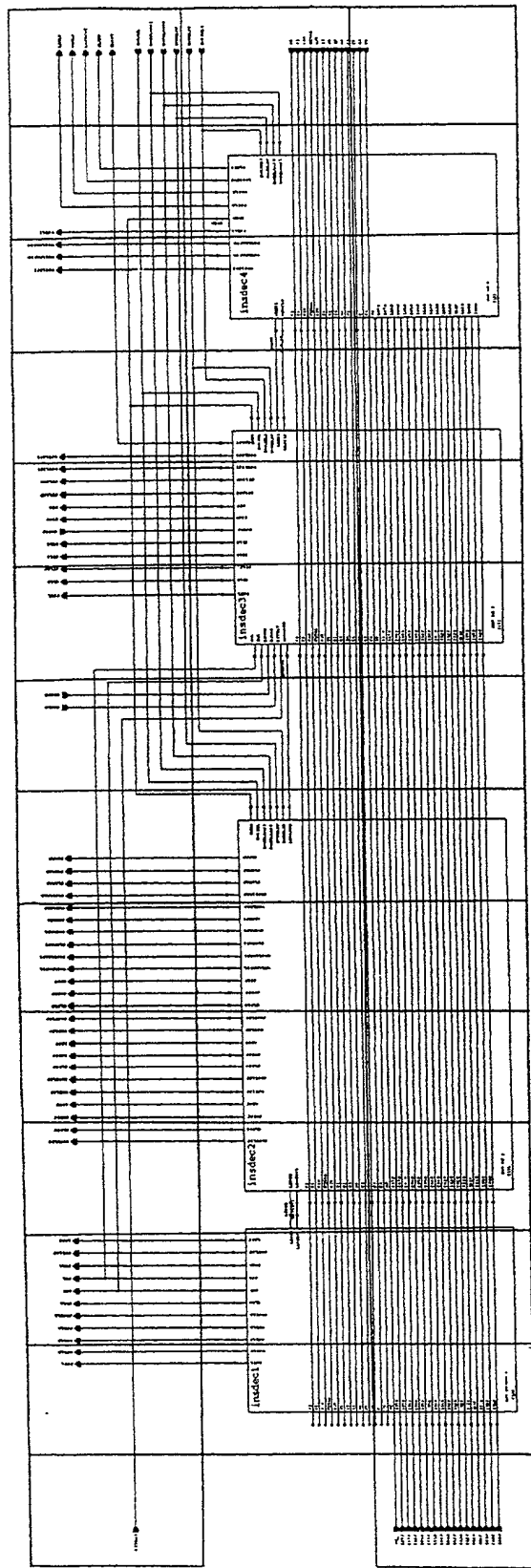


Fig. 7.06

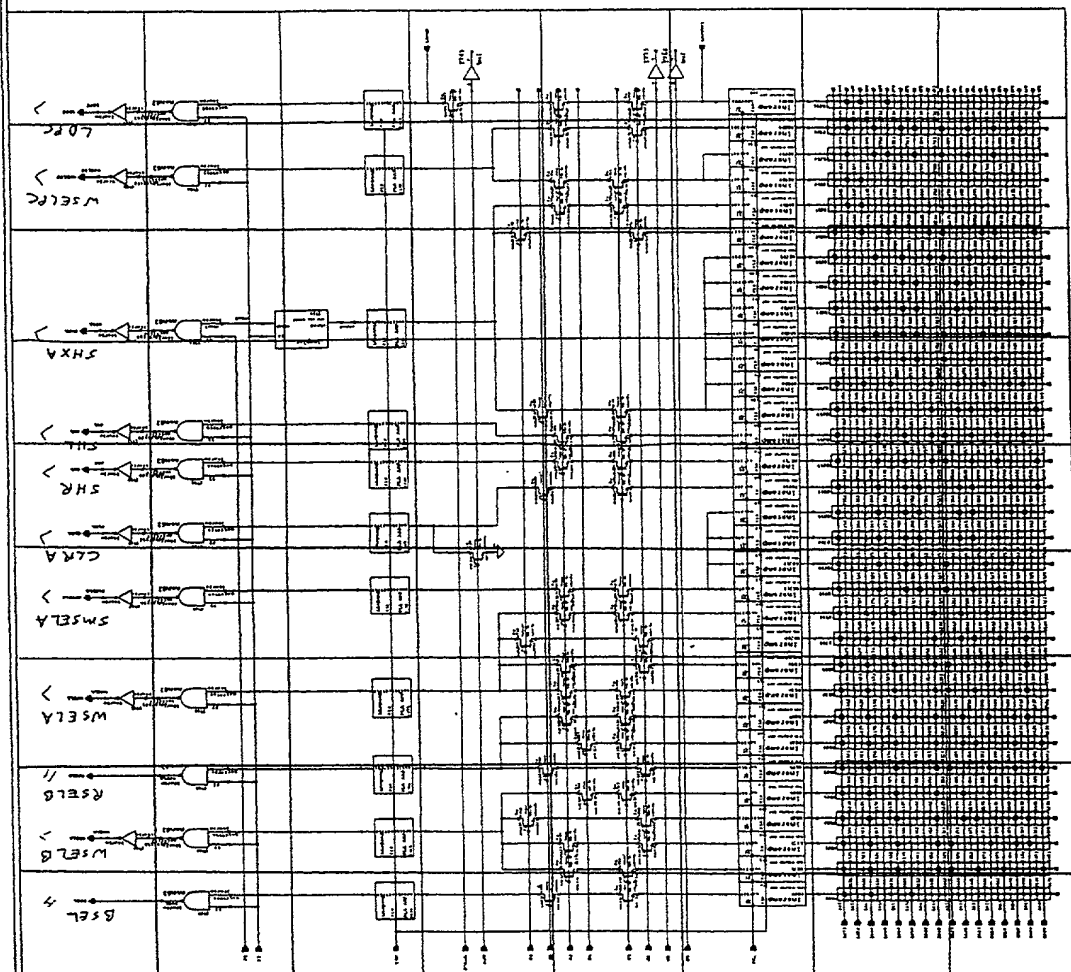
U1

7.0601AA	7.0601AB	7.0601AC	7.0601AD	7.0601AE	7.0601AF	7.0601AG	7.0601AH	7.0601AI
7.0601BA	7.0601BB	7.0601BC	7.0601BD	7.0601BE	7.0601BF	7.0601BG	7.0601BH	7.0601BI
7.0601CA	7.0601CB	7.0601CC	7.0601CD	7.0601CE	7.0601CF	7.0601CG	7.0601CH	7.0601CI
7.0601DA	7.0601DB	7.0601DC	7.0601DD	7.0601DE	7.0601DF	7.0601DG	7.0601DH	7.0601DI
7.0601EA	7.0601EB	7.0601EC	7.0601ED	7.0601EE	7.0601EF	7.0601EG	7.0601EH	7.0601EI
7.0601FA	7.0601FB	7.0601FC	7.0601FD	7.0601FE	7.0601FF	7.0601FG	7.0601FH	7.0601FI
7.0601GA	7.0601GB	7.0601GC	7.0601GD	7.0601GE	7.0601GF	7.0601GG	7.0601GH	7.0601GI
7.0601HA	7.0601HB	7.0601HC	7.0601HD	7.0601HE	7.0601HF	7.0601HG	7.0601HH	7.0601HI

II II II 7.0601

7.0601AA 7.0601AB 7.0601AC 7.0601AD 7.0601AE 7.0601AF 7.0601AG 7.0601AH 7.0601AI  
7.0601BA 7.0601BB 7.0601BC 7.0601BD 7.0601BE 7.0601BF 7.0601BG 7.0601BH 7.0601BI  
7.0601CA 7.0601CB 7.0601CC 7.0601CD 7.0601CE 7.0601CF 7.0601CG 7.0601CH 7.0601CI  
7.0601DA 7.0601DB 7.0601DC 7.0601DD 7.0601DE 7.0601DF 7.0601DG 7.0601DH 7.0601DI  
7.0601EA 7.0601EB 7.0601EC 7.0601ED 7.0601EE 7.0601EF 7.0601EG 7.0601EH 7.0601EI  
7.0601FA 7.0601FB 7.0601FC 7.0601FD 7.0601FE 7.0601FF 7.0601FG 7.0601FH 7.0601FI  
7.0601GA 7.0601GB 7.0601GC 7.0601GD 7.0601GE 7.0601GF 7.0601GG 7.0601GH 7.0601GI  
7.0601HA 7.0601HB 7.0601HC 7.0601HD 7.0601HE 7.0601HF 7.0601HG 7.0601HH 7.0601HI

FIG. 7.0601



**MICRON**

INTEGRATED CIRCUIT DESIGN  
CONFOUNDING THE  
CONFIDENTIAL INFORMATION

Name \_\_\_\_\_ Title \_\_\_\_\_  
Company \_\_\_\_\_  
Address \_\_\_\_\_  
City \_\_\_\_\_ State \_\_\_\_\_ Zip \_\_\_\_\_  
Phone ( ) \_\_\_\_\_ Fax ( ) \_\_\_\_\_  
E-mail \_\_\_\_\_

Registration Number \_\_\_\_\_  
Expiration Date \_\_\_\_\_

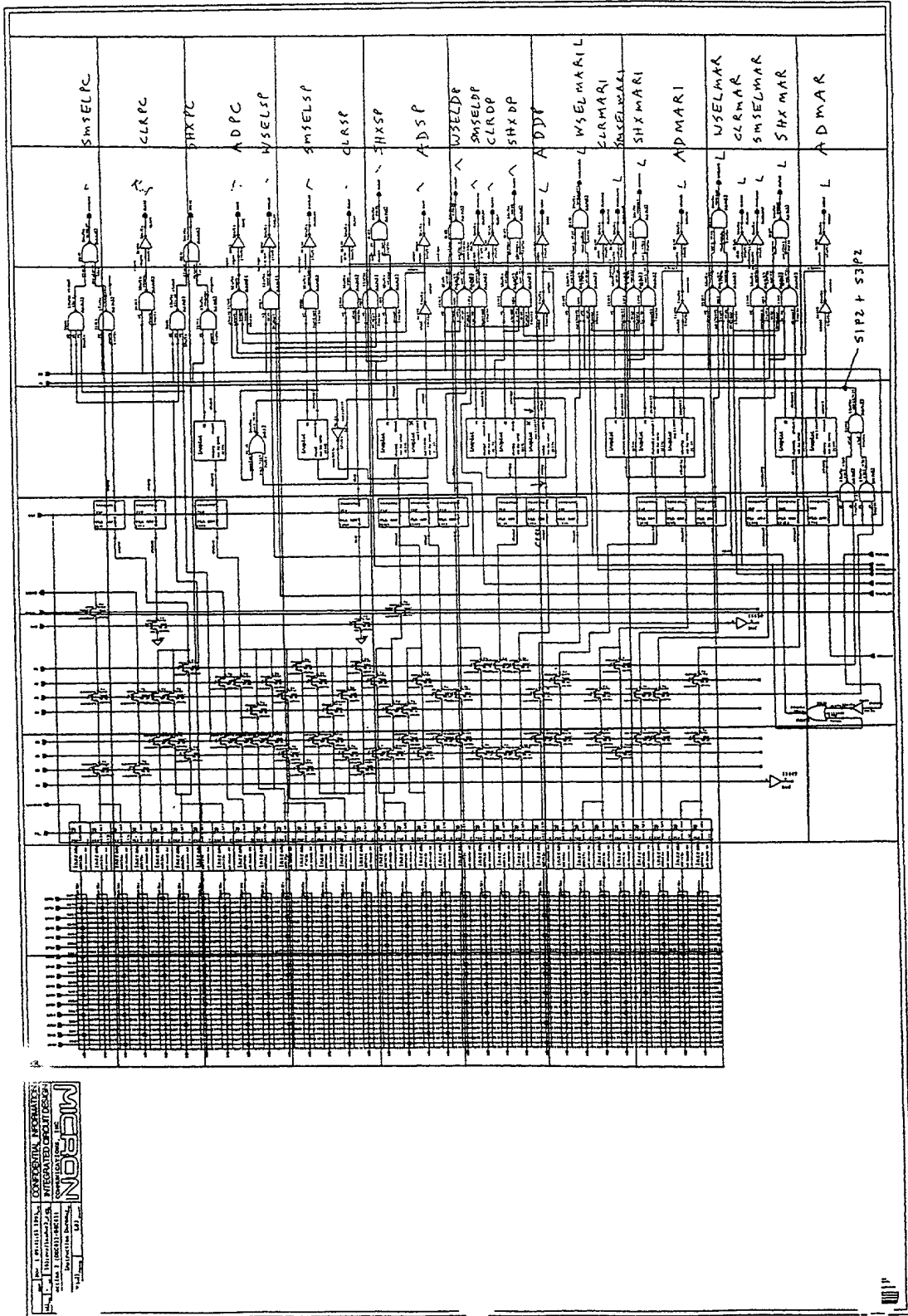
Section 1 (REG-011)

Date \_\_\_\_\_ Time \_\_\_\_\_

7.0602AA	7.0602AB	7.0602AC	7.0602AD	7.0602AE	7.0602AF	7.0602AG	7.0602AH
7.0602BA	7.0602BB	7.0602BC	7.0602BD	7.0602BE	7.0602BF	7.0602BG	7.0602BH
7.0602CA	7.0602CB	7.0602CC	7.0602CD	7.0602CE	7.0602CF	7.0602CG	7.0602CH
7.0602DA	7.0602DB	7.0602DC	7.0602DD	7.0602DE	7.0602DF	7.0602DG	7.0602DH
7.0602EA	7.0602EB	7.0602EC	7.0602ED	7.0602EE	7.0602EF	7.0602EG	7.0602EH
7.0602FA	7.0602FB	7.0602FC	7.0602FD	7.0602FE	7.0602FF	7.0602FG	7.0602FH
7.0602GA	7.0602GB	7.0602GC	7.0602GD	7.0602GE	7.0602GF	7.0602GG	7.0602GH
7.0602HA	7.0602HB	7.0602HC	7.0602HD	7.0602HE	7.0602HF	7.0602HG	7.0602HH
		7.0602IC	7.0602ID	7.0602IE	7.0602IF	7.0602IG	7.0602IH
		7.0602JC	7.0602JD	7.0602JE	7.0602JF	7.0602JG	7.0602JH

Fig 7.0602

650602



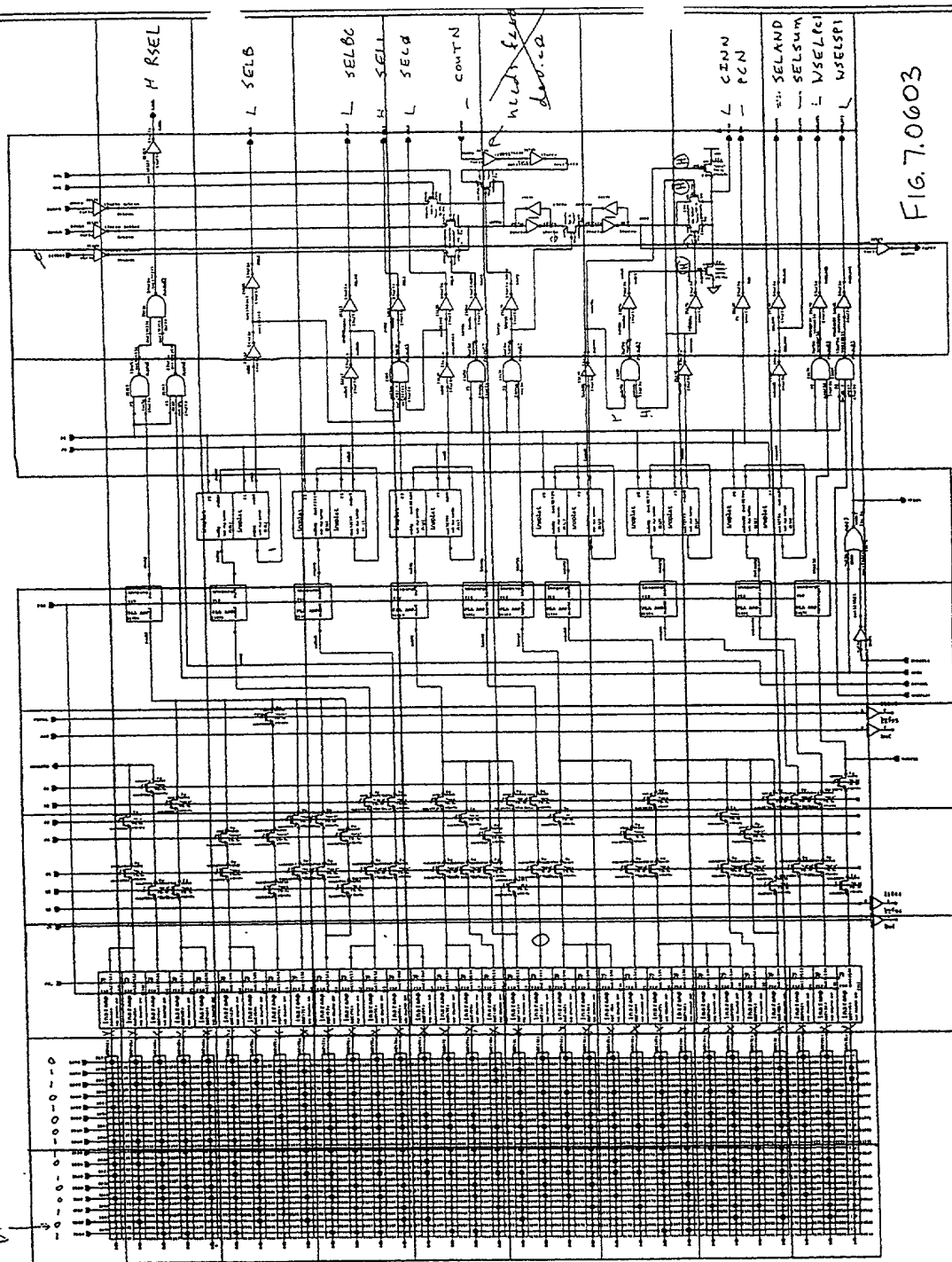
- ADPC is the  
one of the  
ADSP, ADPC, ADMAR  
ADMAR

FIG. 7.0602



7.0603AA	7.0603AB	7.0603AC	7.0603AD	7.0603AE	7.0603AF	7.0603AH	7.0603AI	7.0603AJ
7.0603BA	7.0603BB	7.0603BC	7.0603BD	7.0603BE	7.0603BF	7.0603BH	7.0603BI	7.0603BJ
7.0603CA	7.0603CB	7.0603CC	7.0603CD	7.0603CE	7.0603CF	7.0603CH	7.0603CI	7.0603CJ
7.0603DA	7.0603DB	7.0603DC	7.0603DD	7.0603DE	7.0603DF	7.0603DH	7.0603DI	7.0603DJ
7.0603EA	7.0603EB	7.0603EC	7.0603ED	7.0603EE	7.0603EF	7.0603EH	7.0603EI	7.0603EJ
7.0603FA	7.0603FB	7.0603FC	7.0603FD	7.0603FE	7.0603FF	7.0603FH	7.0603FI	7.0603FJ
7.0603GA	7.0603GB	7.0603GC	7.0603GD	7.0603GE	7.0603GF	7.0603GH	7.0603GI	7.0603GJ
7.0603HA	7.0603HB	7.0603HC	7.0603HD	7.0603HE	7.0603HF	7.0603HH	7.0603HI	7.0603HJ
7.0603IA	7.0603IB	7.0603IC	7.0603ID	7.0603IE	7.0603IF	7.0603IH	7.0603II	7.0603IJ
		7.0603JC	7.0603JD	7.0603JE	7.0603JF	7.0603JG	7.0603JI	
								7.0603BK


 Министерство образования и науки Республики Беларусь

[illegible]

7.0604AA	7.0604AB	7.0604AC	7.0604AD	7.0604AE	7.0604AF	7.0604AG	7.0604AH	7.0604AI
7.0604BA	7.0604BB	7.0604BC	7.0604BD	7.0604BE	7.0604BF	7.0604BG	7.0604BH	7.0604BI
7.0604CA	7.0604CB	7.0604CC	7.0604CD	7.0604CE	7.0604CF	7.0604CG	7.0604CH	7.0604CI
7.0604DA	7.0604DB	7.0604DC	7.0604DD	7.0604DE	7.0604DF	7.0604DG	7.0604DH	7.0604DI
7.0604EA	7.0604EB	7.0604EC	7.0604ED	7.0604EE	7.0604EF	7.0604EG	7.0604EH	7.0604EI
7.0604FA	7.0604FB	7.0604FC	7.0604FD	7.0604FE	7.0604FF	7.0604FG	7.0604FH	7.0604FI
7.0604GA	7.0604GB	7.0604GC	7.0604GD	7.0604GE	7.0604GF	7.0604GG	7.0604GH	7.0604GI
7.0604HA	7.0604HB	7.0604HC	7.0604HD	7.0604HE	7.0604HF	7.0604HG		
7.0604IA	7.0604IB	7.0604IC	7.0604ID	7.0604IE	7.0604IF	7.0604IG		
7.0604JA	7.0604JB	7.0604JC	7.0604JD	7.0604JE	7.0604JF	7.0604JG	7.0604JH	7.0604JI

7.0604 "A" series

00101030

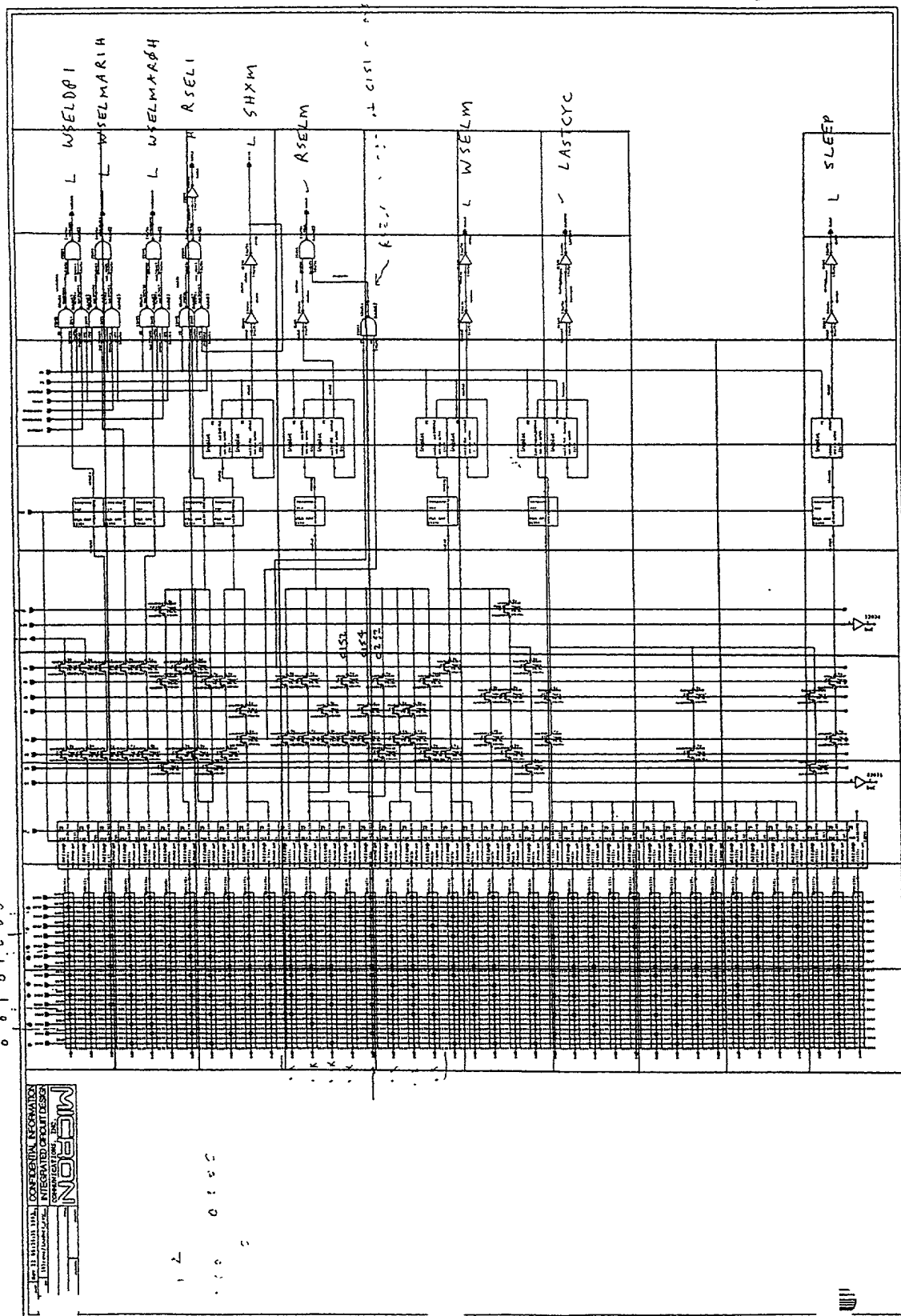


Fig. 7.06



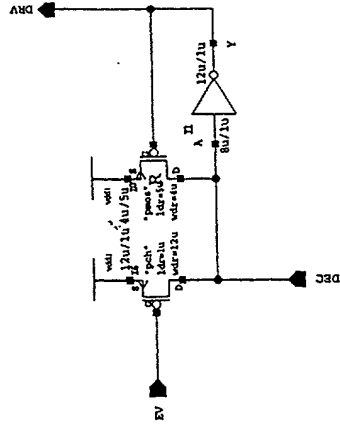


Fig. 7.060402

MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: Instruction Decoder PLA Amp	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/inspamp	REV: -
CONFIDENTIAL INFORMATION		DATE: Oct 6 12:21:34 1993	SIZE: A

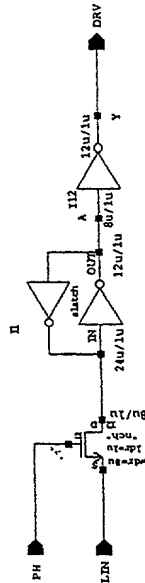


FIG. 7.060403

PROJECT: L03		DESIGNER: Rotzoll	
TITLE: Instruction Decoder PLA Latch			
NAME: 103reva/insplat		REV: -	SHEET: A
DATE: Sep 29 16:10:56 1993			

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

CONFIDENTIAL

7.07AA	7.07AB
7.07BA	7.07BB

ILLUSTRATION



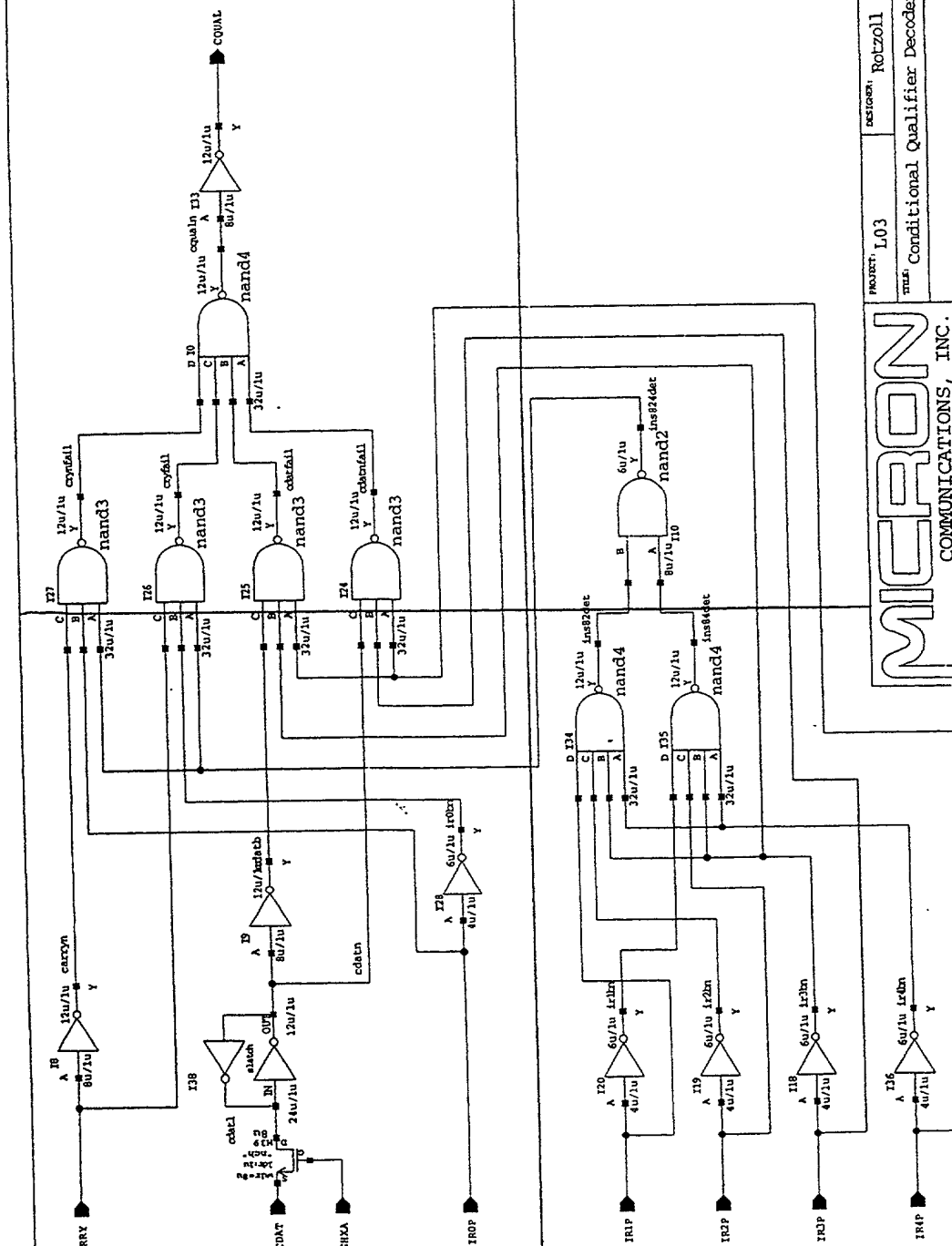


FIG. 7.07

<b>MICRON</b>		PROJECT: L03		DESIGNER: Retzoll	
CONDITIONAL QUALIFIER DESIGN					
TITLE: Conditional Qualifier Decoder					
NAME: 103reva/cqualdec		REV: -		SHEET: A	
DATE: Nov 17 20:09:12 1993					

**MICRON**  
 COMMUNICATIONS, INC.  
 INTEGRATED CIRCUIT DESIGN  
 CONFIDENTIAL INFORMATION

7.08AA

7.08BA

7.08CA

7.08 II

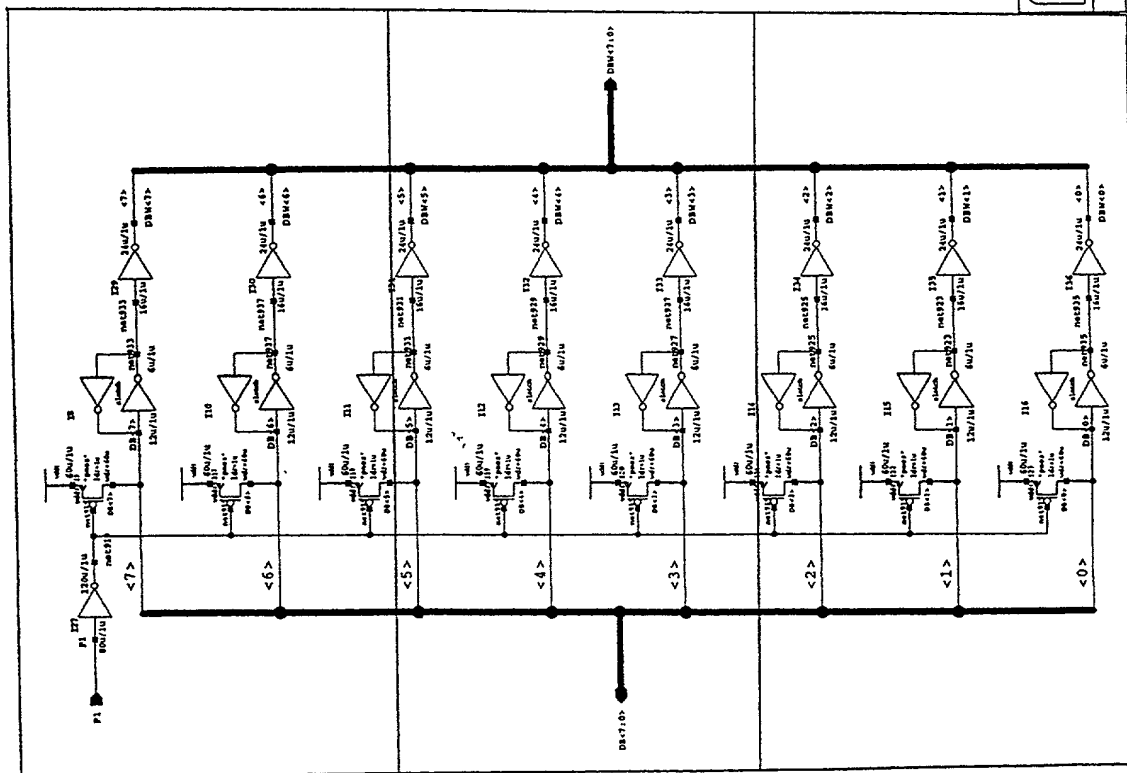


FIG. 7.08

# 21CIPROZ

**INTEGRATED CIRCUIT DESIGN**

**CONFIDENTIAL INFORMATION**

1. The first step is to identify the problem or question that needs to be answered. This involves understanding the context and the specific requirements of the task.

Parameter	Value	Unit	Parameter	Value	Unit
Age	1.0	yr	Age	1.0	yr
Weight	70.0	kg	Weight	70.0	kg
Height	1.75	m	Height	1.75	m
Sex	Male		Sex	Male	
Smoking	Non-smoker		Smoking	Non-smoker	
Alcohol	Non-drinker		Alcohol	Non-drinker	
Exercise	None		Exercise	None	
Medication	None		Medication	None	
Family History	None		Family History	None	
Genotype	AA		Genotype	AA	
Phenotype	Normal		Phenotype	Normal	
Proteinuria	None		Proteinuria	None	
Blood Pressure	120/80	mmHg	Blood Pressure	120/80	mmHg
Serum Creatinine	1.0	mg/dL	Serum Creatinine	1.0	mg/dL
Estimated GFR	120	ml/min/1.73m <sup>2</sup>	Estimated GFR	120	ml/min/1.73m <sup>2</sup>
Urine Protein	0.1	g/day	Urine Protein	0.1	g/day
Urine Albumin	0.1	mg/day	Urine Albumin	0.1	mg/day
Urine Creatinine	0.1	mg/day	Urine Creatinine	0.1	mg/day
Urine pH	5.0		Urine pH	5.0	
Urine Specific Gravity	1.020		Urine Specific Gravity	1.020	
Urine Osmolality	900	mOsm/kg	Urine Osmolality	900	mOsm/kg
Urine Sodium	100	mmol/day	Urine Sodium	100	mmol/day
Urine Potassium	40	mmol/day	Urine Potassium	40	mmol/day
Urine Calcium	10	mmol/day	Urine Calcium	10	mmol/day
Urine Magnesium	5	mmol/day	Urine Magnesium	5	mmol/day
Urine Phosphorus	5	mmol/day	Urine Phosphorus	5	mmol/day
Urine Sulfate	5	mmol/day	Urine Sulfate	5	mmol/day
Urine Chloride	5	mmol/day	Urine Chloride	5	mmol/day
Urine Bicarbonate	5	mmol/day	Urine Bicarbonate	5	mmol/day
Urine Nitrogen	5	g/day	Urine Nitrogen	5	g/day
Urine Carbon	5	g/day	Urine Carbon	5	g/day
Urine Hydrogen	5	g/day	Urine Hydrogen	5	g/day
Urine Oxygen	5	g/day	Urine Oxygen	5	g/day
Urine Fluorine	5	g/day	Urine Fluorine	5	g/day
Urine Iodine	5	g/day	Urine Iodine	5	g/day
Urine Bromine	5	g/day	Urine Bromine	5	g/day
Urine Zinc	5	g/day	Urine Zinc	5	g/day
Urine Selenium	5	g/day	Urine Selenium	5	g/day
Urine Manganese	5	g/day	Urine Manganese	5	g/day
Urine Cobalt	5	g/day	Urine Cobalt	5	g/day
Urine Nickel	5	g/day	Urine Nickel	5	g/day
Urine Silicon	5	g/day	Urine Silicon	5	g/day
Urine Boron	5	g/day	Urine Boron	5	g/day
Urine Vanadium	5	g/day	Urine Vanadium	5	g/day
Urine Chromium	5	g/day	Urine Chromium	5	g/day
Urine Molybdenum	5	g/day	Urine Molybdenum	5	g/day
Urine Tin	5	g/day	Urine Tin	5	g/day
Urine Lead	5	g/day	Urine Lead	5	g/day
Urine Cadmium	5	g/day	Urine Cadmium	5	g/day
Urine Mercury	5	g/day	Urine Mercury	5	g/day
Urine Arsenic	5	g/day	Urine Arsenic	5	g/day
Urine Antimony	5	g/day	Urine Antimony	5	g/day
Urine Tellurium	5	g/day	Urine Tellurium	5	g/day
Urine Barium	5	g/day	Urine Barium	5	g/day
Urine Strontium	5	g/day	Urine Strontium	5	g/day
Urine Zirconium	5	g/day	Urine Zirconium	5	g/day
Urine Niobium	5	g/day	Urine Niobium	5	g/day
Urine Molybdenum	5	g/day	Urine Molybdenum	5	g/day
Urine Ruthenium	5	g/day	Urine Ruthenium	5	g/day
Urine Rhodium	5	g/day	Urine Rhodium	5	g/day
Urine Palladium	5	g/day	Urine Palladium	5	g/day
Urine Silver	5	g/day	Urine Silver	5	g/day
Urine Cadmium	5	g/day	Urine Cadmium	5	g/day
Urine Barium	5	g/day	Urine Barium	5	g/day
Urine Strontium	5	g/day	Urine Strontium	5	g/day
Urine Zirconium	5	g/day	Urine Zirconium	5	g/day
Urine Niobium	5	g/day	Urine Niobium	5	g/day
Urine Molybdenum	5	g/day	Urine Molybdenum	5	g/day
Urine Ruthenium	5	g/day	Urine Ruthenium	5	g/day
Urine Rhodium	5	g/day	Urine Rhodium	5	g/day
Urine Palladium	5	g/day	Urine Pall		

EST. 1977

60620 6306200

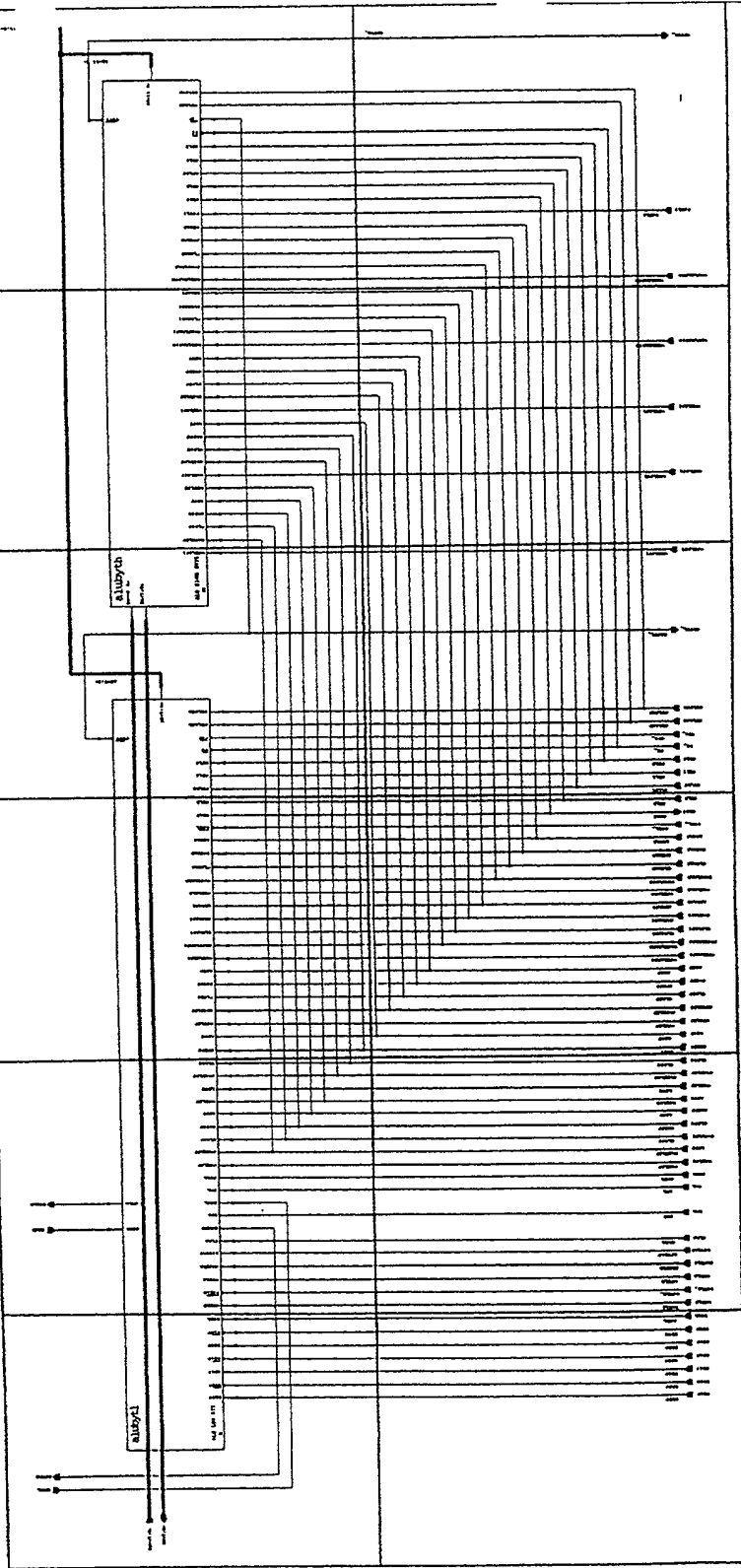


Fig. 7.09

<b>MEARON</b>	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
DATE: 10/11/77	BY: [Signature]
DESIGNED BY: [Signature]	CHECKED BY: [Signature]
APPROVED BY: [Signature]	REVIEWED BY: [Signature]

7.0901AA 7.0901AB 7.0901AC 7.0901AD 7.0901AE

7.0901AA	7.0901AB	7.0901AC	7.0901AD	7.0901AE
7.0901BA	7.0901BB	7.0901BC	7.0901BD	7.0901BE
7.0901CA	7.0901CB	7.0901CC	7.0901CD	7.0901CE

7.0901

SECRET

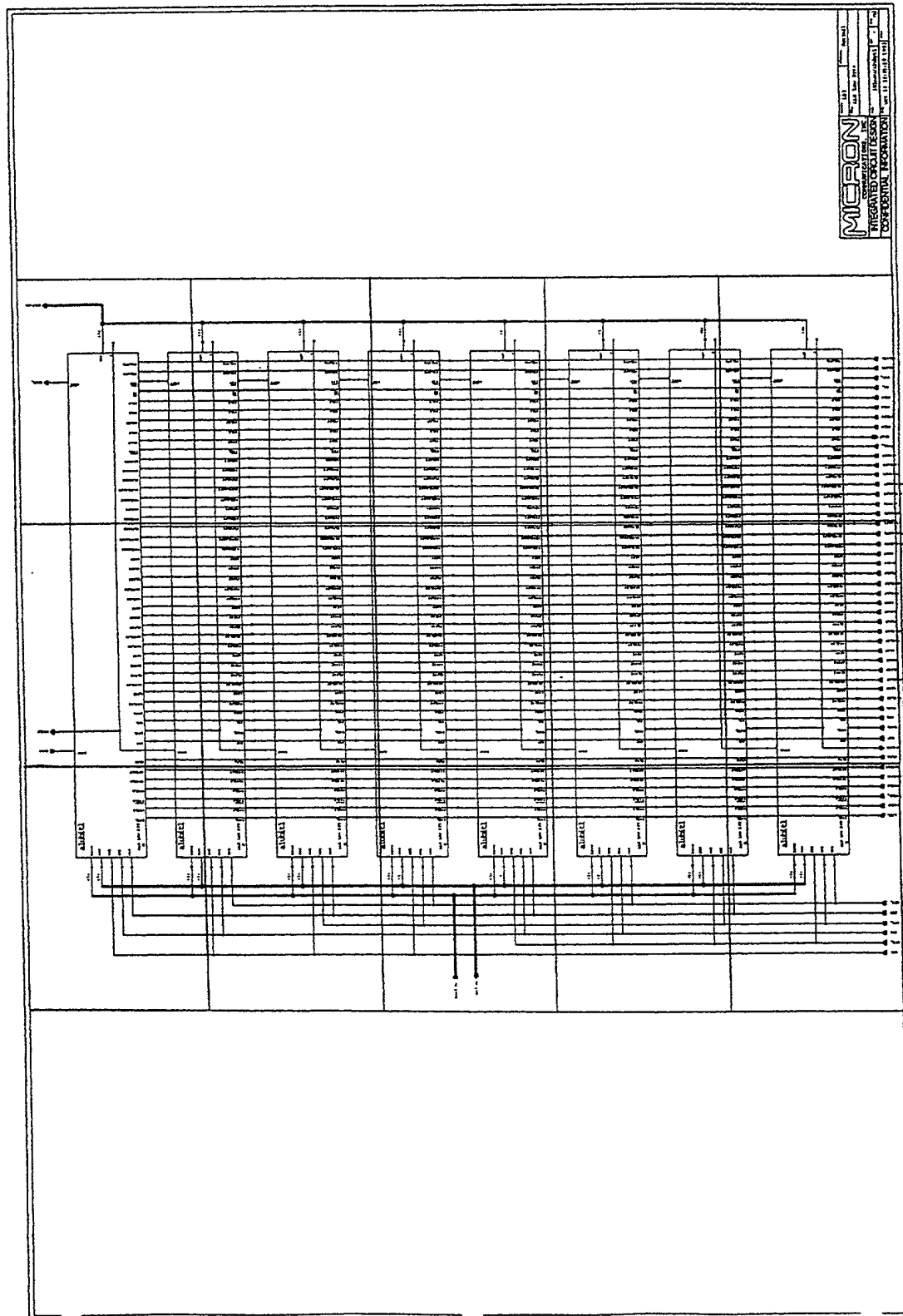
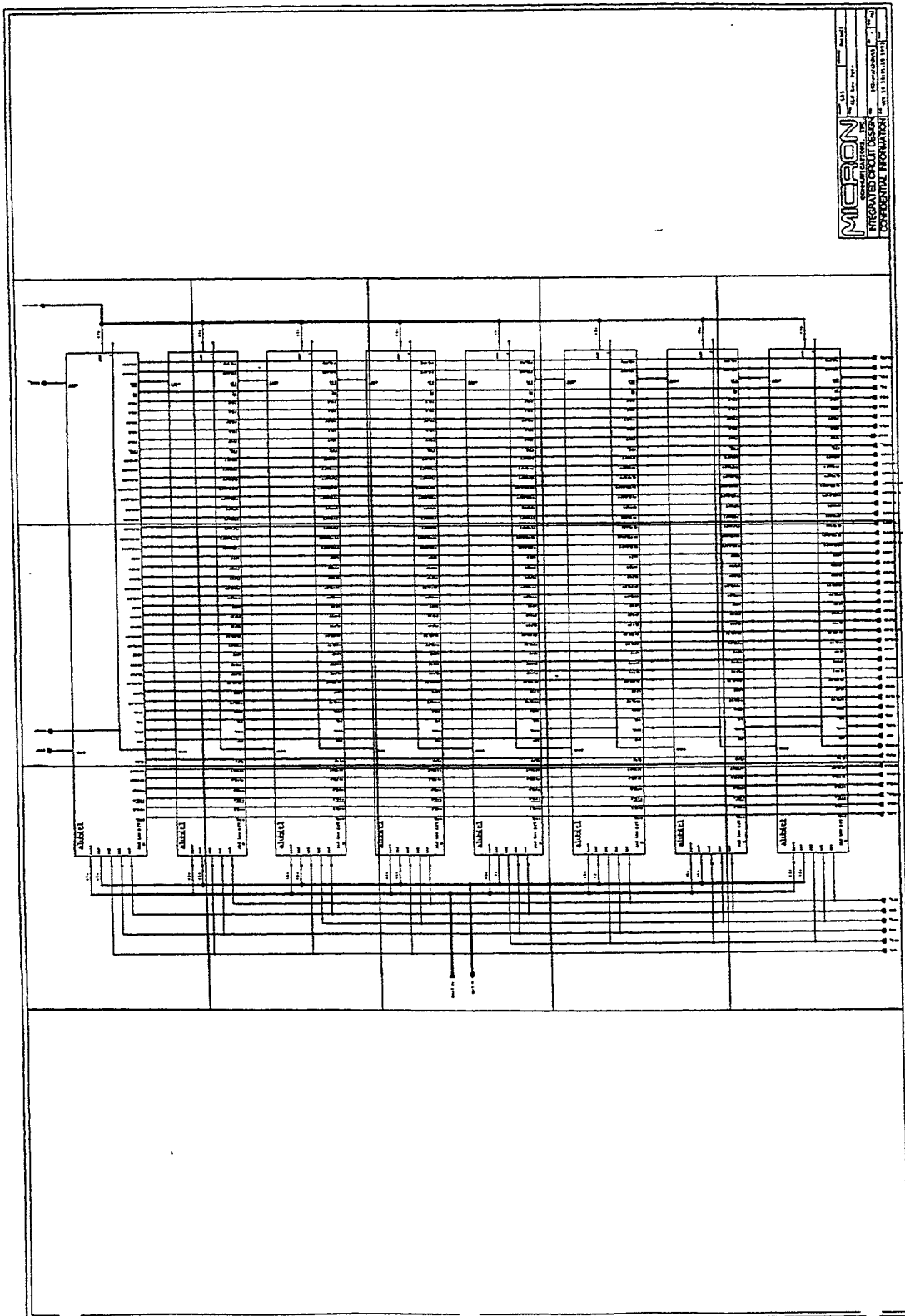


FIG. 7.0901

**MICRON**  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION  
Rev. 1.1 (10-01-1991)

SECRET  
NO FORN DISSEM  
NO UNCLASSIFIED DISSEM

FIG. 7.0901



**MICRON**  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION  
DATE: 11.10.1981



II II II      II. IIII II



Parameter	Unit	Value	Standard Error	95% CI	P-value
Intercept		1.00	0.00	1.00	0.00
Age	Year	0.02	0.01	-0.01, 0.05	0.15
Gender					
Male		0.01	0.02	-0.03, 0.05	0.78
Female		-0.01	0.02	-0.05, 0.03	0.82
Education	Year	0.01	0.01	-0.01, 0.03	0.45
Income	Year	0.01	0.01	-0.01, 0.03	0.45
Health					
Good		0.01	0.02	-0.03, 0.05	0.78
Fair		-0.01	0.02	-0.05, 0.03	0.82
Poor		0.01	0.02	-0.03, 0.05	0.78
Occupation					
Manager		0.01	0.02	-0.03, 0.05	0.78
Professional		-0.01	0.02	-0.05, 0.03	0.82
Service		0.01	0.02	-0.03, 0.05	0.78
Unemployed		-0.01	0.02	-0.05, 0.03	0.82
Retired		0.01	0.02	-0.03, 0.05	0.78
Homemaker		-0.01	0.02	-0.05, 0.03	0.82
Student		0.01	0.02	-0.03, 0.05	0.78
Military		-0.01	0.02	-0.05, 0.03	0.82
Other		0.01	0.02	-0.03, 0.05	0.78
Marital Status					
Married		0.01	0.02	-0.03, 0.05	0.78
Single		-0.01	0.02	-0.05, 0.03	0.82
Divorced		0.01	0.02	-0.03, 0.05	0.78
Widowed		-0.01	0.02	-0.05, 0.03	0.82
Never Married		0.01	0.02	-0.03, 0.05	0.78
Religion					
Christian		0.01	0.02	-0.03, 0.05	0.78
Jewish		-0.01	0.02	-0.05, 0.03	0.82
Muslim		0.01	0.02	-0.03, 0.05	0.78
Hindu		-0.01	0.02	-0.05, 0.03	0.82
Buddhist		0.01	0.02	-0.03, 0.05	0.78
Other		-0.01	0.02	-0.05, 0.03	0.82
Political Affiliation					
Democrat		0.01	0.02	-0.03, 0.05	0.78
Republican		-0.01	0.02	-0.05, 0.03	0.82
Independent		0.01	0.02	-0.03, 0.05	0.78
Other		-0.01	0.02	-0.05, 0.03	0.82
Region					
North		0.01	0.02	-0.03, 0.05	0.78
South		-0.01	0.02	-0.05, 0.03	0.82
West		0.01	0.02	-0.03, 0.05	0.78
Midwest		-0.01	0.02	-0.05, 0.03	0.82
Other		0.01	0.02	-0.03, 0.05	0.78
Time	Year	0.01	0.01	-0.01, 0.03	0.45
Time Squared	Year	-0.01	0.01	-0.03, 0.01	0.15
Time Cubed	Year	0.01	0.01	-0.01, 0.03	0.45
Time Squared Cubed	Year	-0.01	0.01	-0.03, 0.01	0.15
Time Squared Cubed Squared	Year	0.01	0.01	-0.01, 0.03	0.45
Time Squared Cubed Cubed	Year	-0.01	0.01	-0.03, 0.01	0.15
Time Squared Cubed Squared Cubed	Year	0.01	0.01	-0.01, 0.03	0.45
Time Squared Cubed Squared Cubed Squared	Year	-0.01	0.01	-0.03, 0.01	0.15
Time Squared Cubed Squared Cubed Squared Cubed	Year	0.01	0.01	-0.01, 0.03	0.45
Time Squared Cubed Squared Cubed Squared Cubed Squared	Year	-0.01	0.01	-0.03, 0.01	0.15
Time Squared Cubed Squared Cubed Squared Cubed Squared Cubed	Year	0.01	0.01	-0.01, 0.03	0.45
Time Squared Cubed Squared Cubed Squared Cubed Squared Cubed Squared	Year	-0.01	0.01	-0.03, 0.01	0.15
Time Squared Cubed Squared Cubed Squared Cubed Squared Cubed Squared Cubed	Year	0.01	0.01	-0.01, 0.03	0.45
Time Squared Cubed Squared Cubed Squared Cubed Squared Cubed Squared Cubed Squared	Year	-0.01	0.01	-0.03, 0.01	0.15
Time Squared Cubed Squared Cubed Squared Cubed Squared Cubed Squared Cubed Squared Cubed	Year	0.01	0.01	-0.01, 0.03	0.45
Time Squared Cubed Squared Cubed Squared Cubed Squared Cubed Squared Cub					

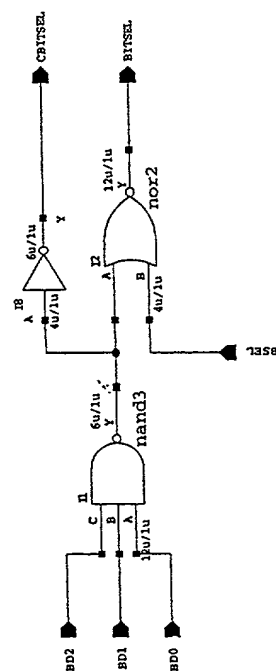


FIG. 7. 09010101

MICRON		PROJECT: L03		DESIGNER: Rotzoll	
COMMUNICATIONS, INC.		TITLE: ALU Bit Decoder Cell			
		NAME: 103reva/alubdec		REV: -	
				EVAL: A	
INTEGRATED CIRCUIT DESIGN		DATE: Sep 29 16:07:43 1993		SHEET: 1	
CONFIDENTIAL INFORMATION					

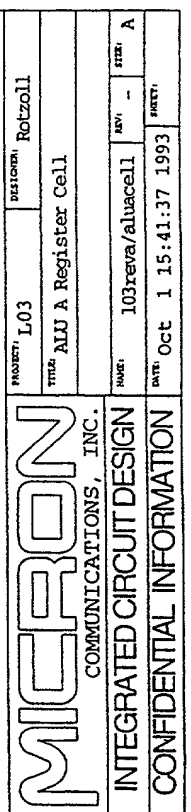


ALL INFORMATION CONTAINED  
HEREIN IS UNCLASSIFIED

7.09010103AA	7.09010103AB
--------------	--------------

EX-107 7.09010103

FIG. 7.09010103



PROJECT: L03	DESIGNER: Rotzoll
--------------	-------------------

[illegible]

ZOOBITE COMMUNICATIONS, INC.

## INTEGRATED CIRCUIT DESIGN

**CONFIDENTIAL INFORMATION**

NAME:	REV:	SIZE:
103reva/aluacell	--	A

DATE: Oct 1 15:41:37 1993

Size	A
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**SHEET:**

	1990	1991	1992	1993	1994	1995	1996	1997	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035	2036	2037	2038	2039	2040	2041	2042	2043	2044	2045	2046	2047	2048	2049	2050	2051	2052	2053	2054	2055	2056	2057	2058	2059	2060	2061	2062	2063	2064	2065	2066	2067	2068	2069	2070	2071	2072	2073	2074	2075	2076	2077	2078	2079	2080	2081	2082	2083	2084	2085	2086	2087	2088	2089	2090	2091	2092	2093	2094	2095	2096	2097	2098	2099	2100	2101	2102	2103	2104	2105	2106	2107	2108	2109	2110	2111	2112	2113	2114	2115	2116	2117	2118	2119	2120	2121	2122	2123	2124	2125	2126	2127	2128	2129	2130	2131	2132	2133	2134	2135	2136	2137	2138	2139	2140	2141	2142	2143	2144	2145	2146	2147	2148	2149	2150	2151	2152	2153	2154	2155	2156	2157	2158	2159	2160	2161	2162	2163	2164	2165	2166	2167	2168	2169	2170	2171	2172	2173	2174	2175	2176	2177	2178	2179	2180	2181	2182	2183	2184	2185	2186	2187	2188	2189	2190	2191	2192	2193	2194	2195	2196	2197	2198	2199	2200	2201	2202	2203	2204	2205	2206	2207	2208	2209	2210	2211	2212	2213	2214	2215	2216	2217	2218	2219	2220	2221	2222	2223	2224	2225	2226	2227	2228	2229	2230	2231	2232	2233	2234	2235	2236	2237	2238	2239	2240	2241	2242	2243	2244	2245	2246	2247	2248	2249	2250	2251	2252	2253	2254	2255	2256	2257	2258	2259	2260	2261	2262	2263	2264	2265	2266	2267	2268	2269	2270	2271	2272	2273	2274	2275	2276	2277	2278	2279	2280	2281	2282	2283	2284	2285	2286	2287	2288	2289	2290	2291	2292	2293	2294	2295	2296	2297	2298	2299	2300	2301	2302	2303	2304	2305	2306	2307	2308	2309	2310	2311	2312	2313	2314	2315	2316	2317	2318	2319	2320	2321	2322	2323	2324	2325	2326	2327	2328	2329	2330	2331	2332	2333	2334	2335	2336	2337	2338	2339	2340	2341	2342	2343	2344	2345	2346	2347	2348	2349	2350	2351	2352	2353	2354	2355	2356	2357	2358	2359	2360	2361	2362	2363	2364	2365	2366	2367	2368	2369	2370	2371	2372	2373	2374	2375	2376	2377	2378	2379	2380	2381	2382	2383	2384	2385	2386	2387	2388	2389	2390	2391	2392	2393	2394	2395	2396	2397	2398	2399	2400	2401	2402	2403	2404	2405	2406	2407	2408	2409	2410	2411	2412	2413	2414	2415	2416	2417	2418	2419	2420	2421	2422	2423	2424	2425	2426	2427	2428	2429	2430	2431	2432	2433	2434	2435	2436	2437	2438	2439	2440	2441	2442	2
--	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	---

7.09010104AB

FOOTNOTES

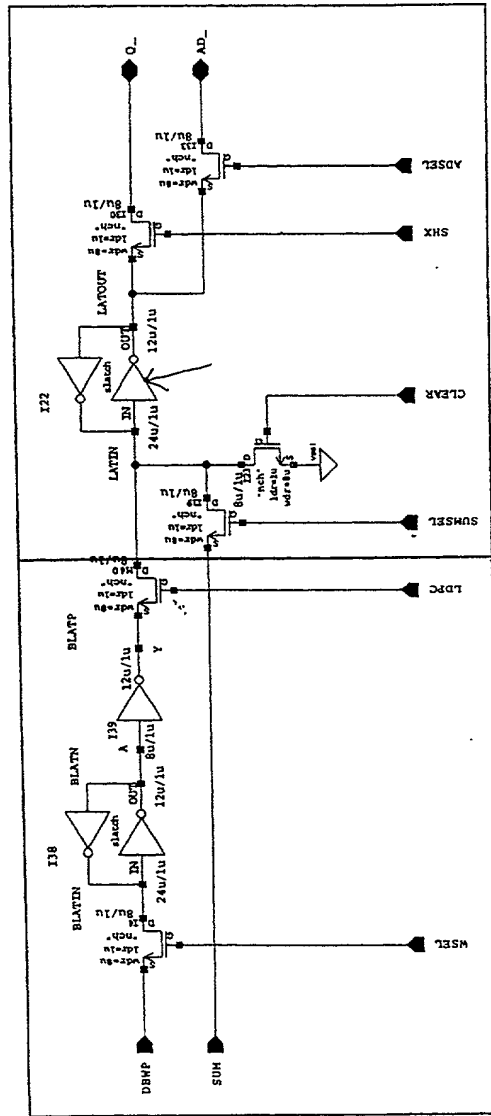
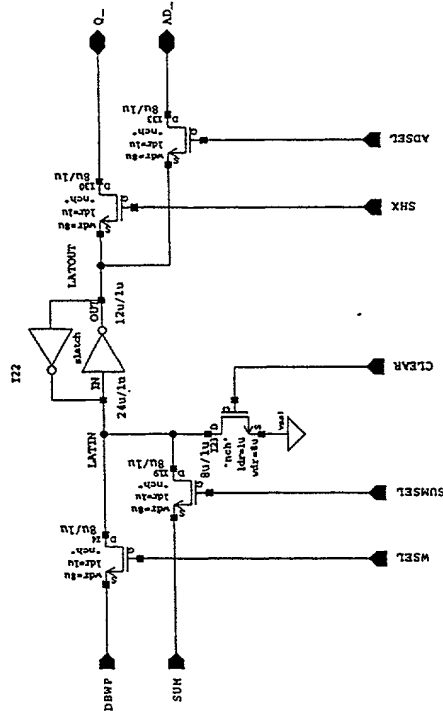


FIG. 7.09010104

PROJECT: L03		DESIGNER: Rotzoll	
TITLE: ALU Register Cell			
NAME: 103reva/alupc		REV: -	SIZE: A
DATE: Oct 1 15:45:48 1993		SHEET: 1	

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION





<b>MICRON</b>		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: ALU Register Cell	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/alurcell	REV: -
CONFIDENTIAL INFORMATION		DATE: Oct 1 15:51:03 1993	SHEET: A

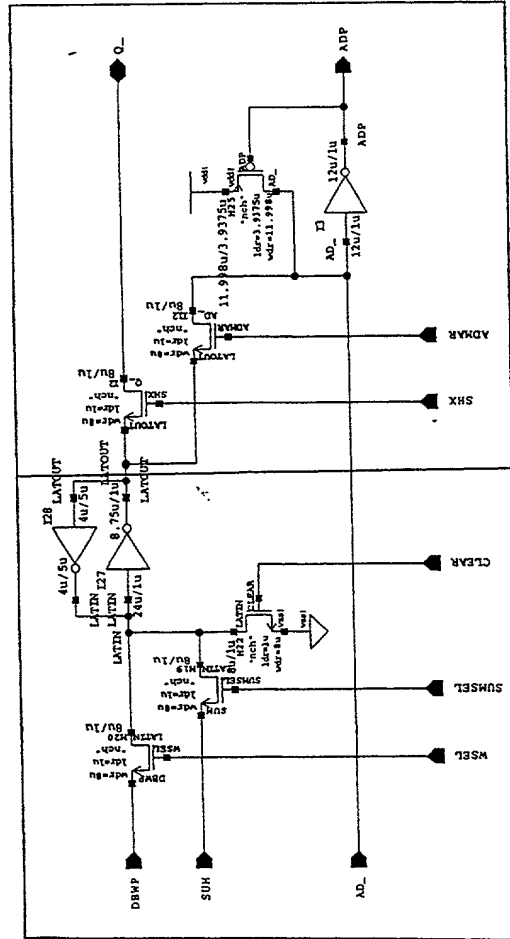
FIG. 7.09010105

ALL INFORMATION CONTAINED  
HEREIN IS UNCLASSIFIED

7.09010106AA	7.09010106AB
--------------	--------------

EX-107 11.0000000000000000

FIG. 7.09010106



MICRON		PROJECT: L03		DESIGNER: JOTOOLE	
COMMUNICATIONS, INC.		TITLE: ALU Memory Address Register			
		NAME: 103reva/alumar		REV: B8	EDES: A
		DATE: Jan 4 10:27:28 1996		DSSTY:	
INTEGRATED CIRCUIT DESIGN					
CONFIDENTIAL INFORMATION					

B8: added pch feedback device



7.09010108AA	7.09010108AB	7.09010108AC
7.09010108BA	7.09010108BB	7.09010108BC

II II III III III III III III

SECRET

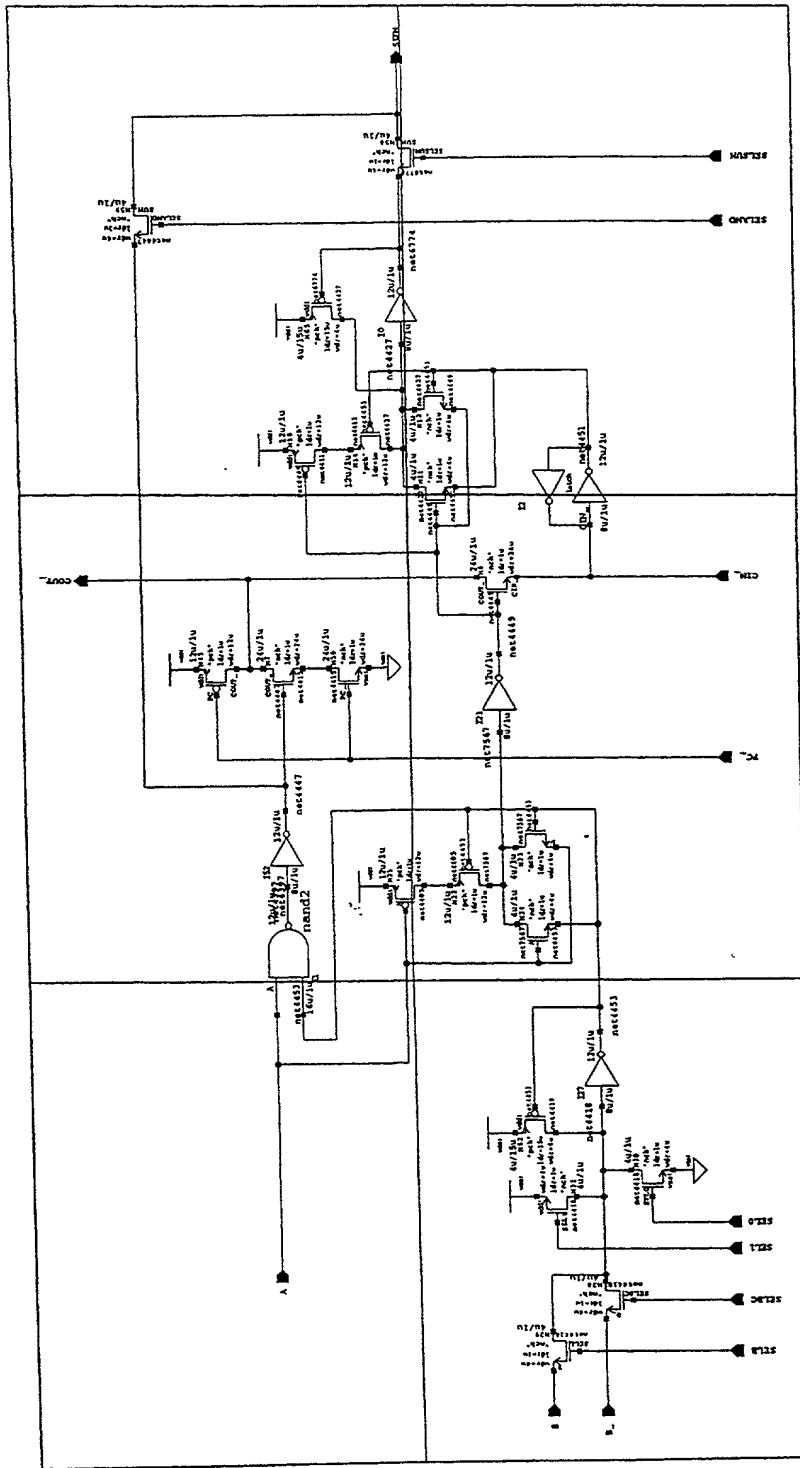


Fig. 7.09010108

B5: move feedback device from I21 to I27

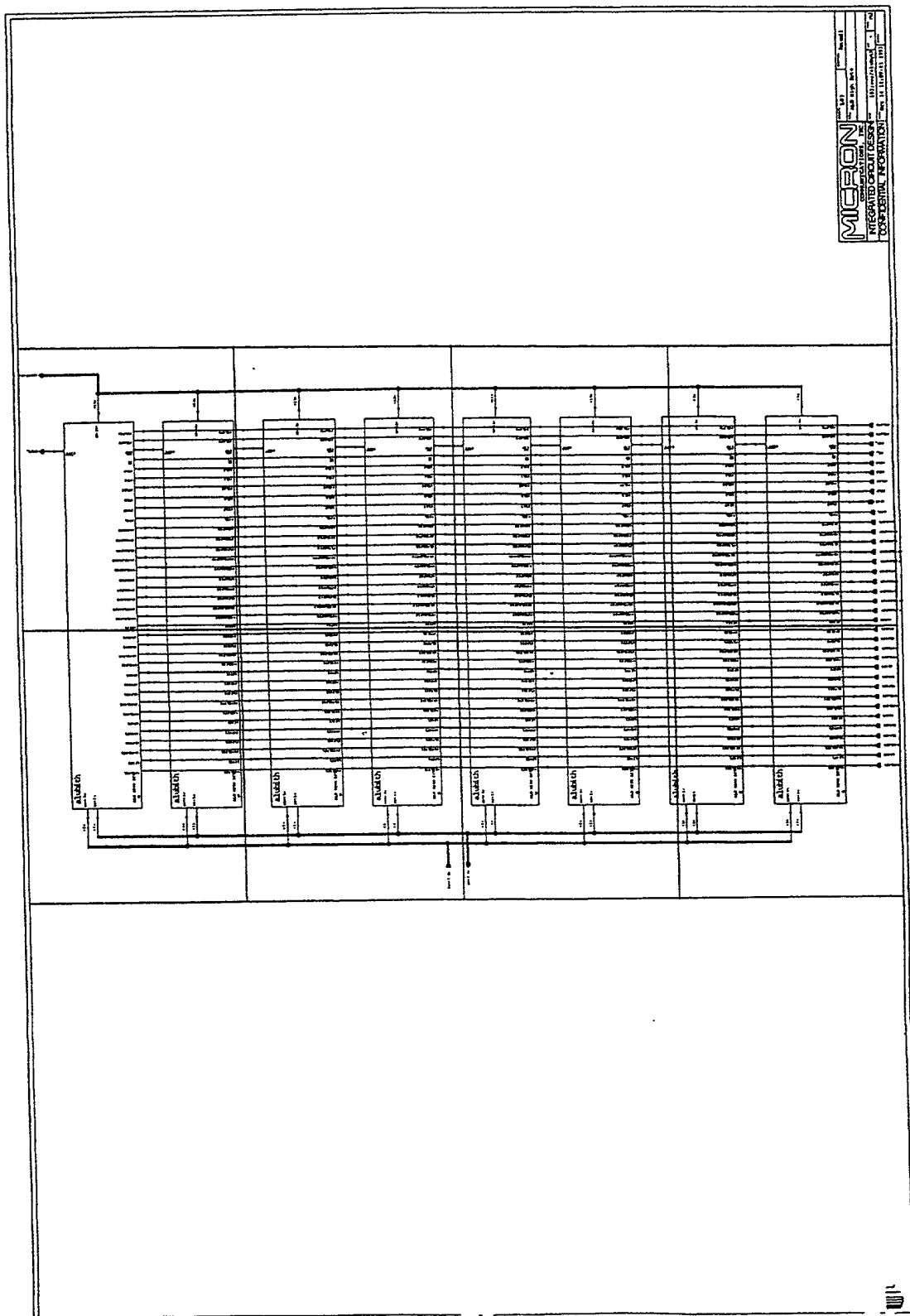
MICRON		PROJECT L03	SECTION J07000LE
COMMUNICATIONS, INC.		THE ALJ Address	
INTEGRATED CIRCUIT DESIGN		part 103reva/aluadd	REV B5
CONFIDENTIAL INFORMATION		part Sep 16 15:48:21 1995	FILE

7.0902AA 7.0902AB 7.0902AC 7.0902AD

7.0902AA	7.0902AB	7.0902AC	7.0902AD
7.0902BA	7.0902BB	7.0902BC	7.0902BD

7.0902

Fig. 7.0902







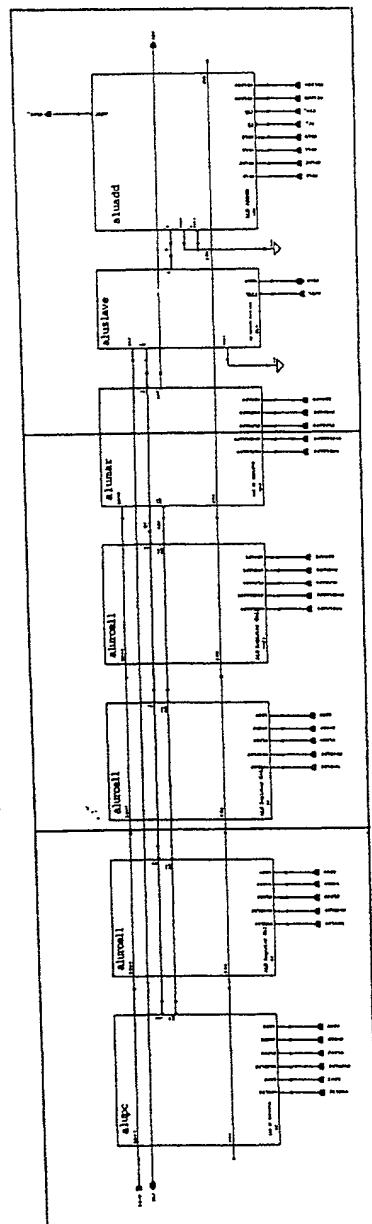
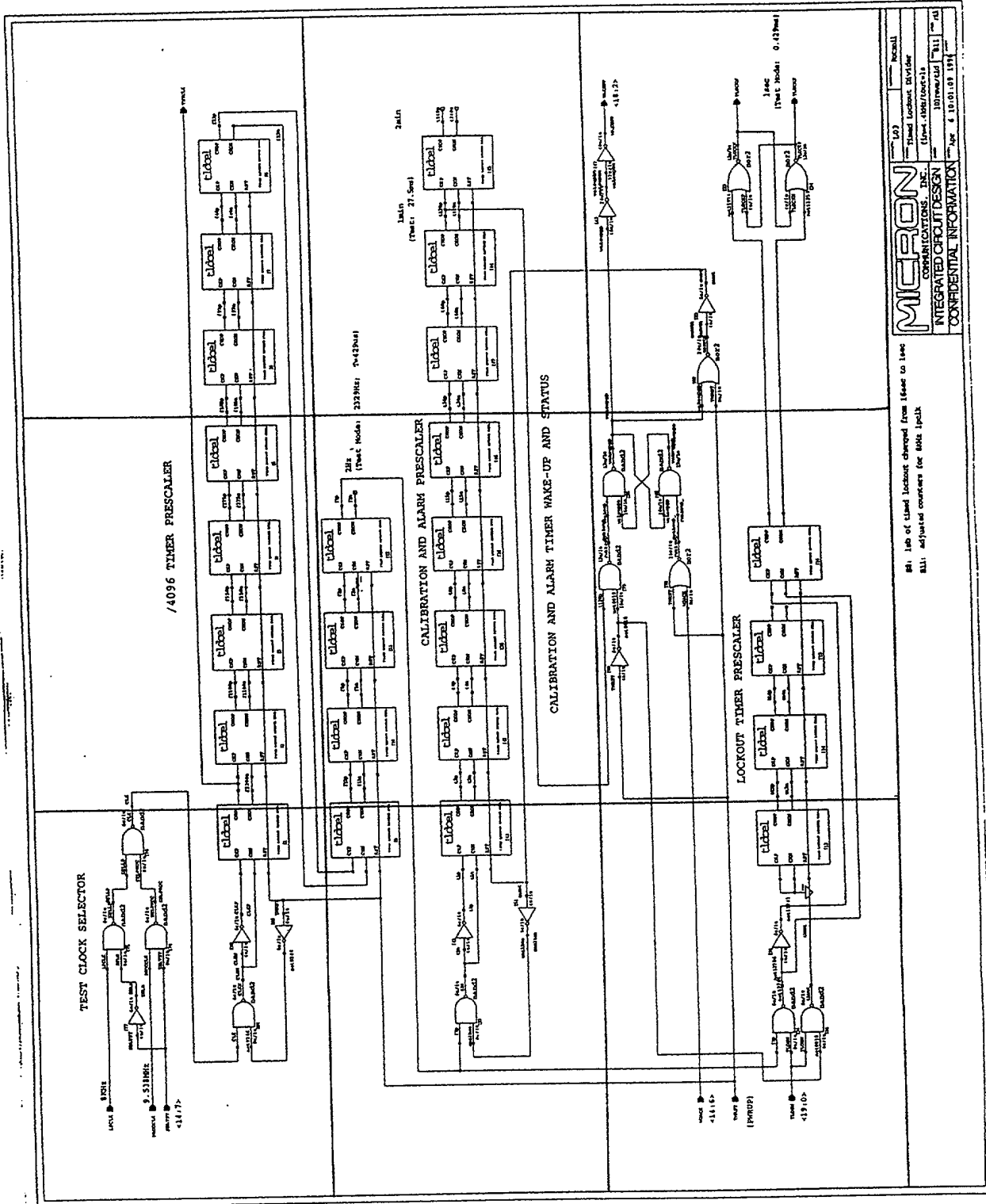


FIG. 7.090201

ALL INFORMATION CONTAINED HEREIN IS UNCLASSIFIED

7.10AA	7.10AB	7.10AC
7.10BA	7.10BB	7.10BC
7.10CA	7.10CB	7.10CC

EX-111

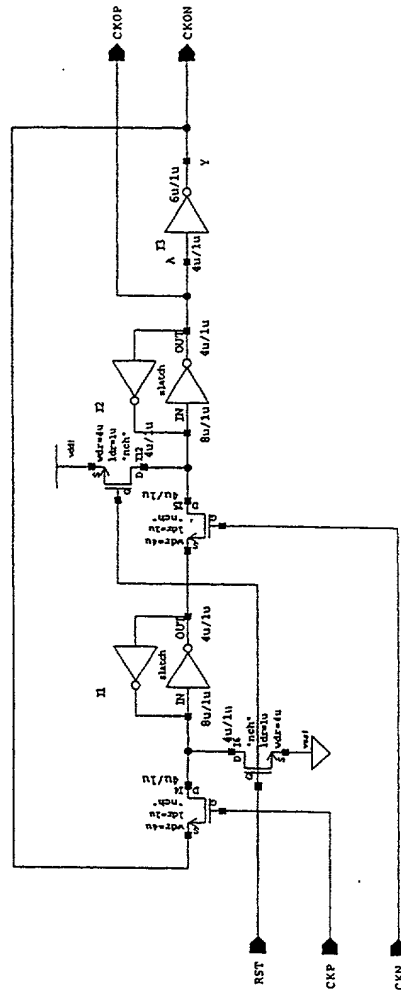


**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

84: 1st of time lockout charged from time to time  
85: adjusted counters for 1000 Hz

86: 1st of time lockout charged from time to time  
87: adjusted counters for 1000 Hz

FIG. 7.10



12/29/92

MICRON		PROJECT	L03	DESIGNER	Rotzoll
COMMUNICATIONS, INC.		TITLE Timed Lockout Divider Cell			
INTEGRATED CIRCUIT DESIGN					
CONFIDENTIAL INFORMATION		NAME	103reva/Edcel		REV
		DATE	Sep 22 15:26:56 1994		SHEET
				SIZE	A

ALL INFORMATION CONTAINED  
HEREIN IS UNCLASSIFIED

7.11AA	7.11AB
--------	--------

EX-107 7.11

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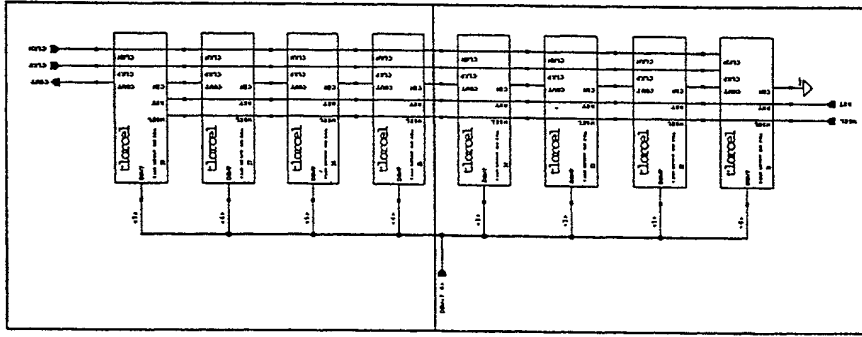


FIG. 7.11

<b>MICRON</b> TECHNOLOGY, INC. INTEGRATED CIRCUIT DESIGN CONFIDENTIAL INFORMATION	Doc 1.03	Rev	Rev
	Ther Lockout Amplifier		
	10000/10000	10000/10000	10000/10000
	Oct 1 11 05 17 199		

MI40-030

7.1101AA	7.1101AB	7.1101AC
----------	----------	----------

MI40-030



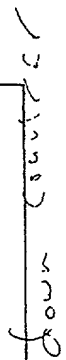


FIG. 7.1101

ALL INFORMATION CONTAINED  
HEREIN IS UNCLASSIFIED

8

7.12AA	7.12AB	7.12AC
--------	--------	--------

U.S. 112

CONFIDENTIAL INFORMATION

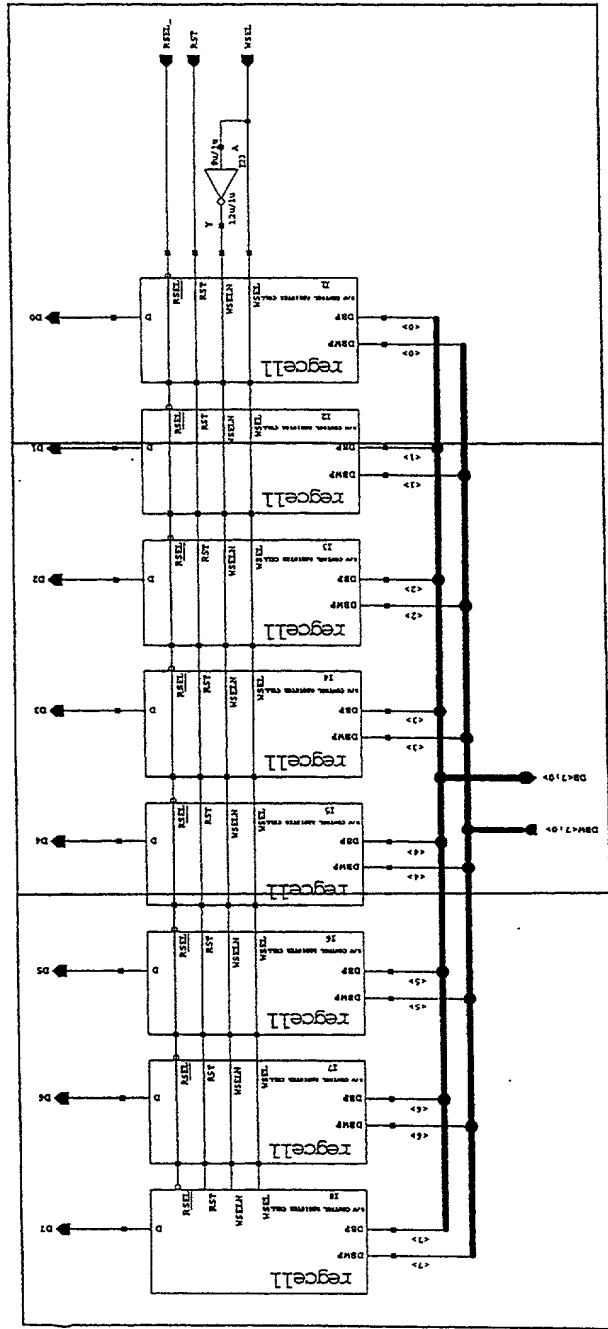


Fig. 7.12

<b>MICRON</b>		PROPERTY: L03		REVISED: ROTCOLL	
COMMUNICATIONS, INC.		TITLE: R/W Control Register			
INTEGRATED CIRCUIT DESIGN		NAME: 101reva/orig		REV: 101 r01	
CONFIDENTIAL INFORMATION		DATE: Nov 12 09:44:40 1993		PAGE: 1	

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION



7.13AA

7.13BA

7.13

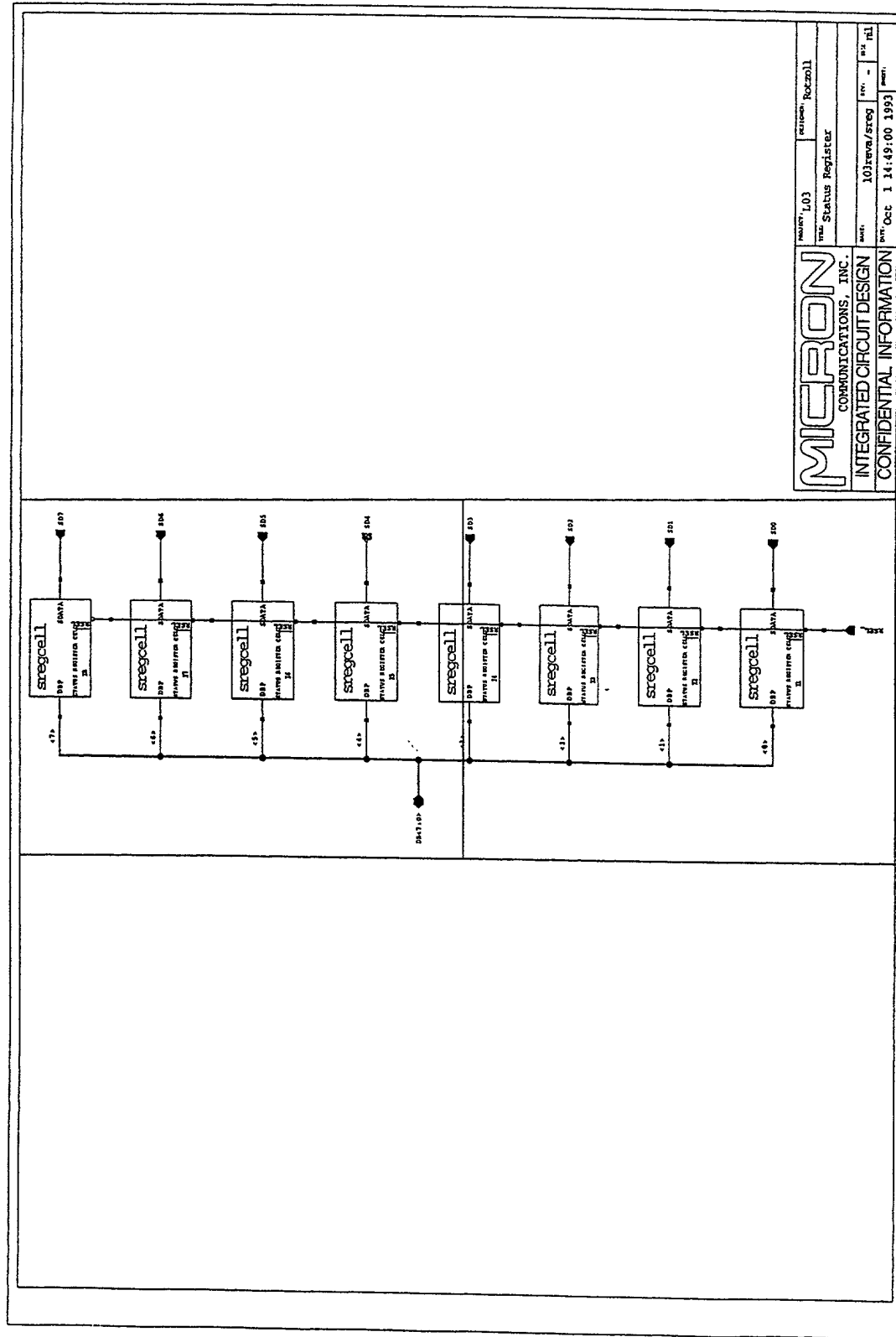
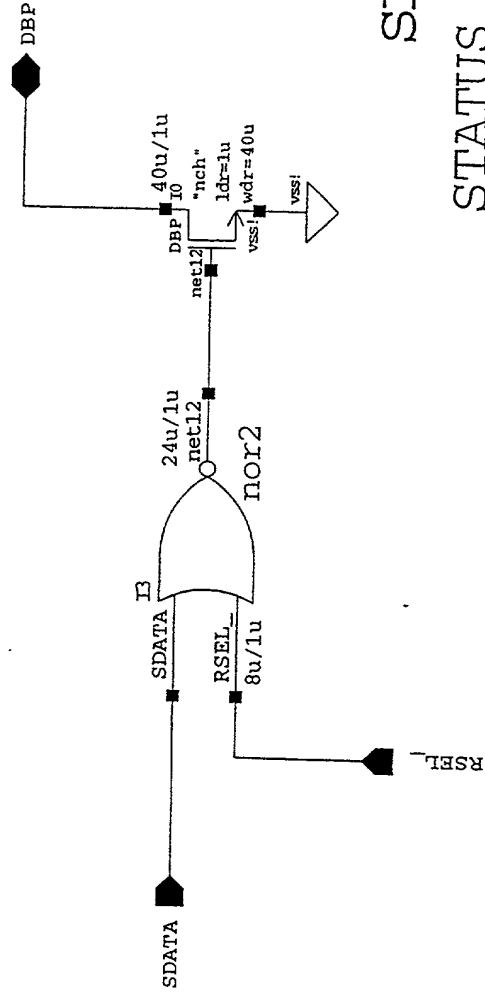


FIG. 7.13

[illegible]

```
sregcell
STATUS REGISTER CELL
```

R. Rotzoll

12/8/92

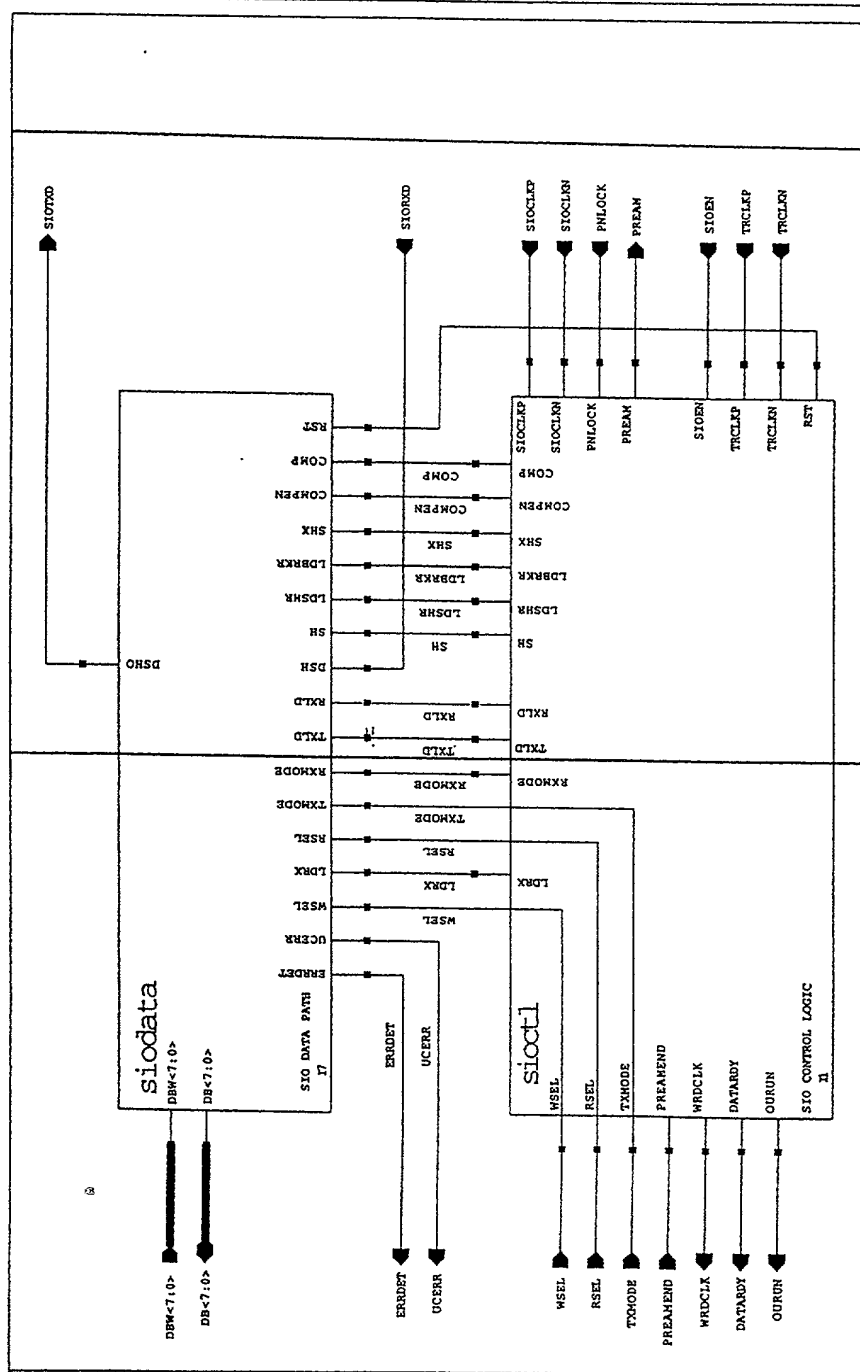
FIG. 7.1301

ALL INFORMATION CONTAINED  
HEREIN IS UNCLASSIFIED

7.14AA	7.14AB
--------	--------

EX-107 7024





B8: deleted BRKREN  
B11: added non-overlapping clocks

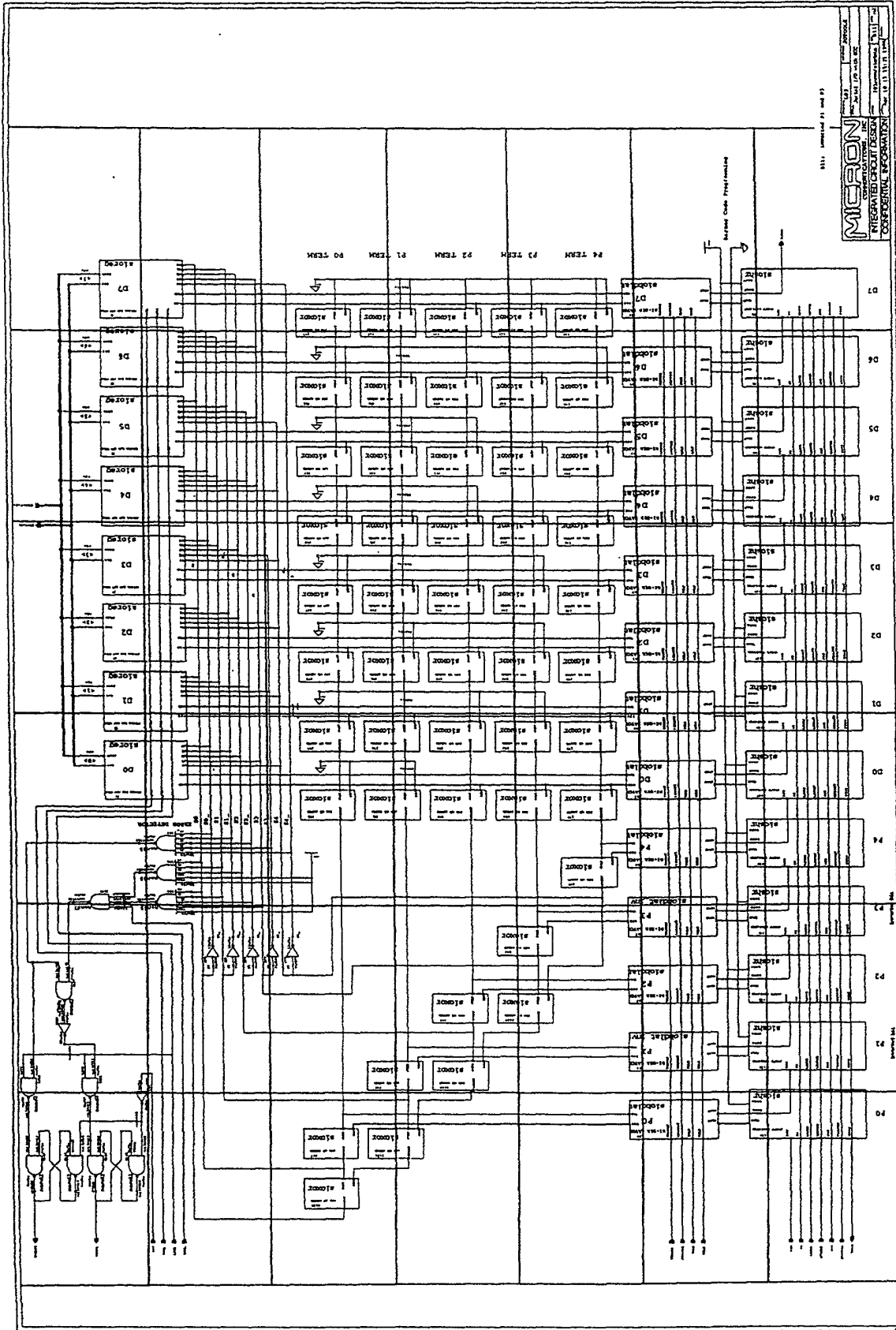
**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

"SECRET" ESD 2000

7.1401AA	7.1401AB	7.1401AC	7.1401AD	7.1401AE	7.1401AF
7.1401BA	7.1401BB	7.1401BC	7.1401BD	7.1401BE	7.1401BF
7.1401CA	7.1401CB	7.1401CC	7.1401CD	7.1401CE	7.1401CF
7.1401DA	7.1401DB	7.1401DC	7.1401DD	7.1401DE	7.1401DF
7.1401EA	7.1401EB	7.1401EC	7.1401ED	7.1401EE	7.1401EF
7.1401FA	7.1401FB	7.1401FC	7.1401FD	7.1401FE	7.1401FF
7.1401GA	7.1401GB	7.1401GC	7.1401GD	7.1401GE	7.1401GF

SECRET No. 2-4 0000

Fig. 7, 1401



ALL INFORMATION CONTAINED  
HEREIN IS UNCLASSIFIED

7.140101AA	7.140101AB
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EX-107 11-11-11



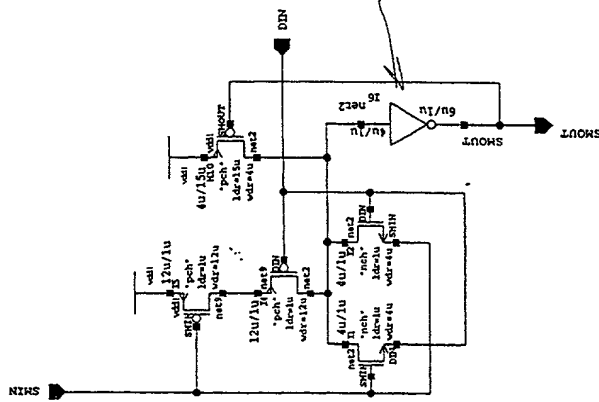


Fig. 7.140102

MICRON COMMUNICATIONS, INC.		PROJECT	L03	DESIGNER	JOTOOLE
		TITLE	SIO XOR		
		NAME	103:eva/sioxor		
		DATE	Sep 1 18:07:22 1994	REV	B1
					SHEET
INTEGRATED CIRCUIT DESIGN		A			
CONFIDENTIAL INFORMATION					

7.140103AB

7.140103AA

EXHIBIT 7.140103

7.140103AB 7.140103AA

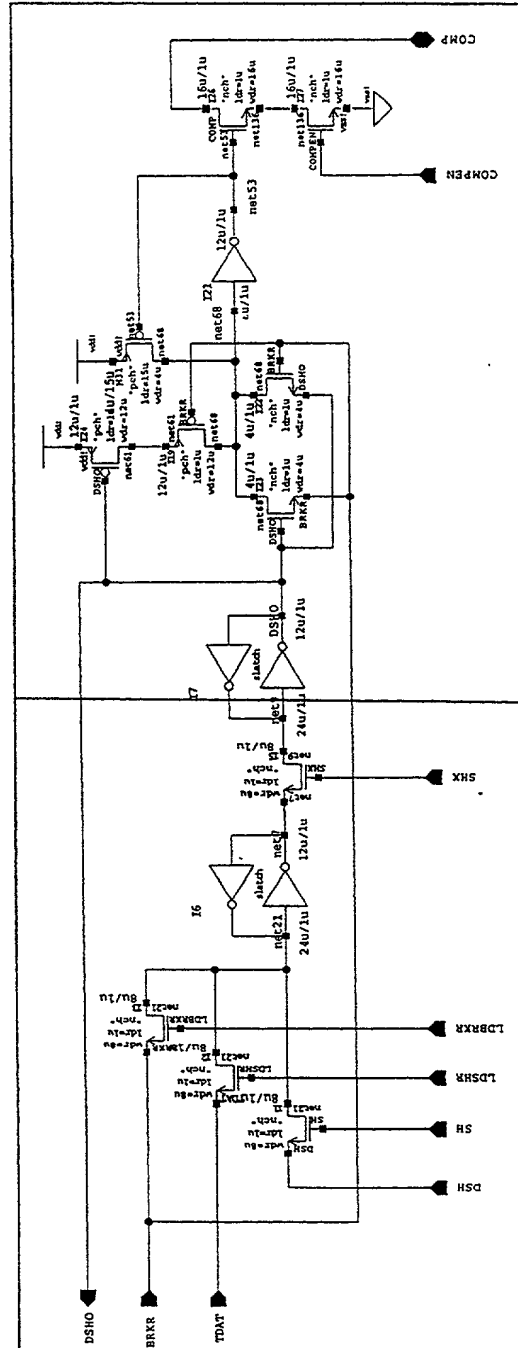




7.140104AA

Π. Π. Ι. Ι. Ι. Ι. Ι.

7.140104



7.140104

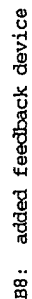
Fig. 7.140104

MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: SIO Shift Register	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/sioshr	REV: B1
CONFIDENTIAL INFORMATION		DATE: Sep 2 08:06:26 1994	SHEET: A

7.140105AB

7.140105AA

SECRET 7.140105



MICRON		PROJECT: L03	SECTION: JOTOOLE
		TITLE: SIO Bidirectional Latch	
COMMUNICATIONS, INC.			
INTEGRATED CIRCUIT DESIGN	NAME: 103reva/sioBidLat	REV: B8	SIZE: A
CONFIDENTIAL INFORMATION	DATE: Jan 8 11:04:57 1996	SHEET:	

7.1402BA	7.1402BB	7.1402AC	7.1402AD	7.1402AE	7.1402AF	7.1402AG	7.1402AH	7.1402AI
		7.1402BC	7.1402BD	7.1402BE	7.1402BF	7.1402BG	7.1402BH	7.1402BI
7.1402CA	7.1402CB	7.1402CC	7.1402CD	7.1402CE	7.1402CF	7.1402CG	7.1402CH	7.1402CI
		7.1402DC	7.1402DD	7.1402DE	7.1402DF	7.1402DG	7.1402DH	7.1402DI
7.1402EA	7.1402EB	7.1402EC	7.1402ED	7.1402EE	7.1402EF	7.1402EG	7.1402EH	7.1402EI



7.140201AB

7.140201AA

II II II II II II II II II II

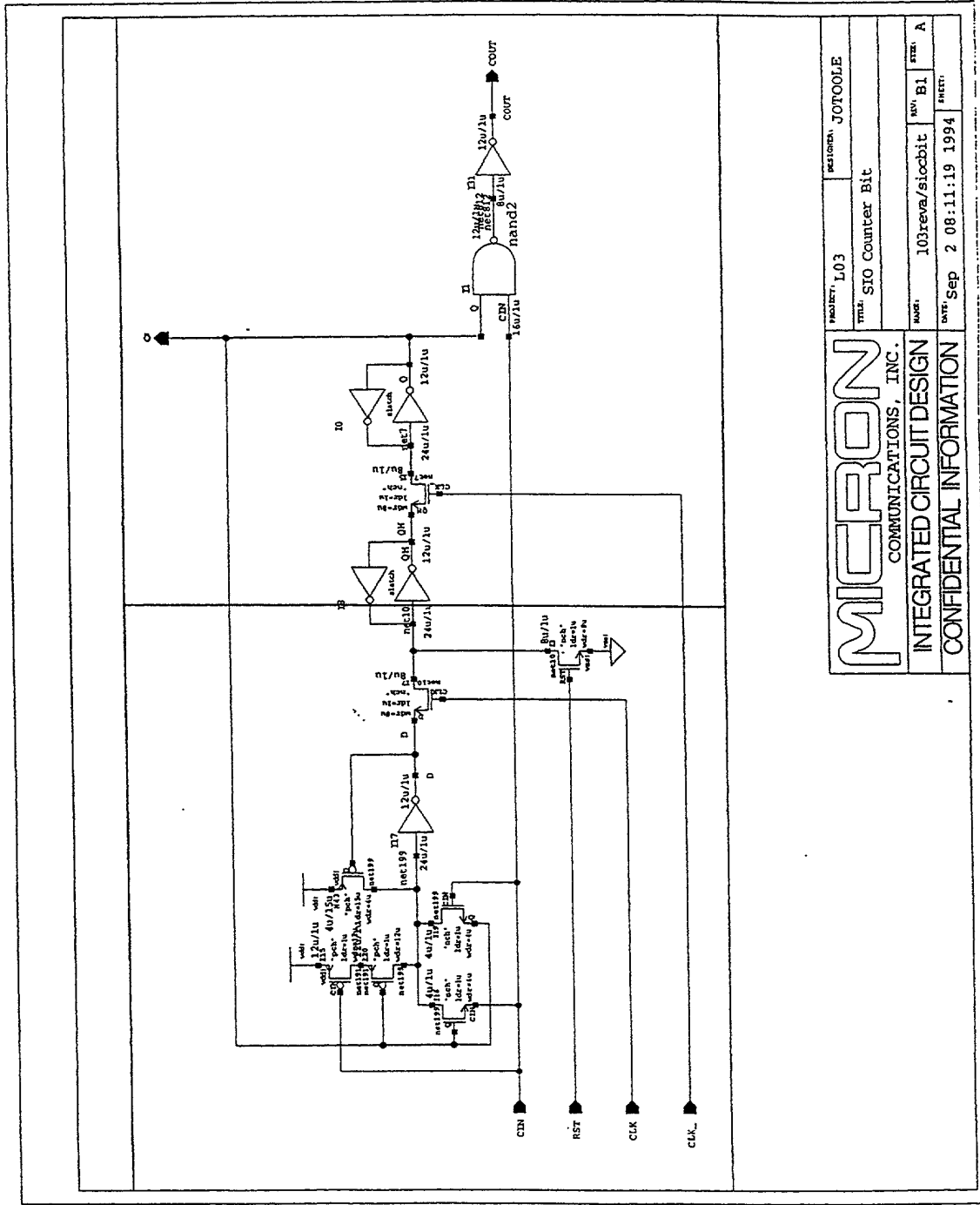


Fig. 7.140

PROJECT: L03		DESIGNER: JOTCOOLE	
TITLE: SIO Counter Bit			
PART: 103reva/siocbit		REV: B1	REV: A
DATE: Sep 2 08:11:19 1994		HEET: 1	



7.15AA 7.15AB 7.15AC 7.15AD

7.15AA	7.15AB	7.15AC	7.15AD
7.15BA	7.15BB	7.15BC	7.15BD
7.15CA	7.15CB	7.15CC	7.15CD
7.15DA	7.15DB	7.15DC	7.15DD
7.15EA	7.15EB	7.15EC	

7.15



7.1501AA

7.1501BA

7.1501CA

Fig 7.1501

2025 RELEASE UNDER E.O. 14176



7.1502AA

7.1502BA

7.1502CA

Fig 7.1502

2025 RELEASE UNDER E.O. 14176

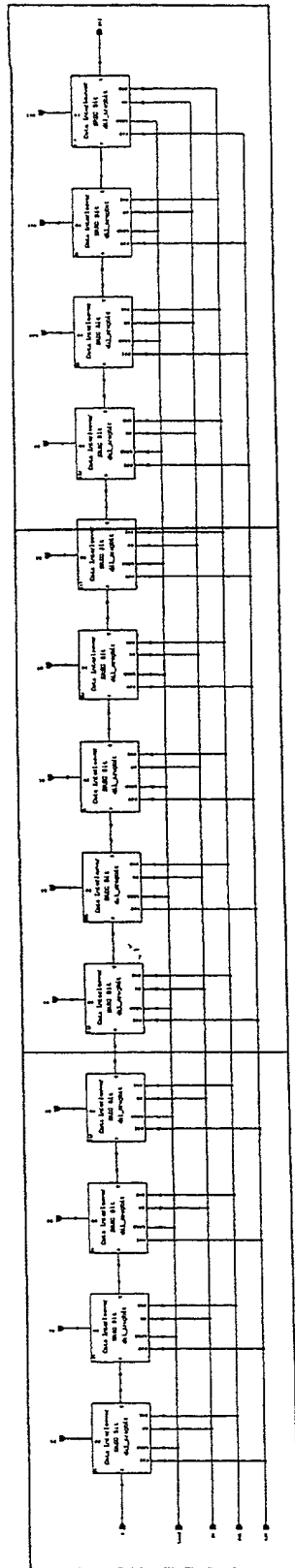


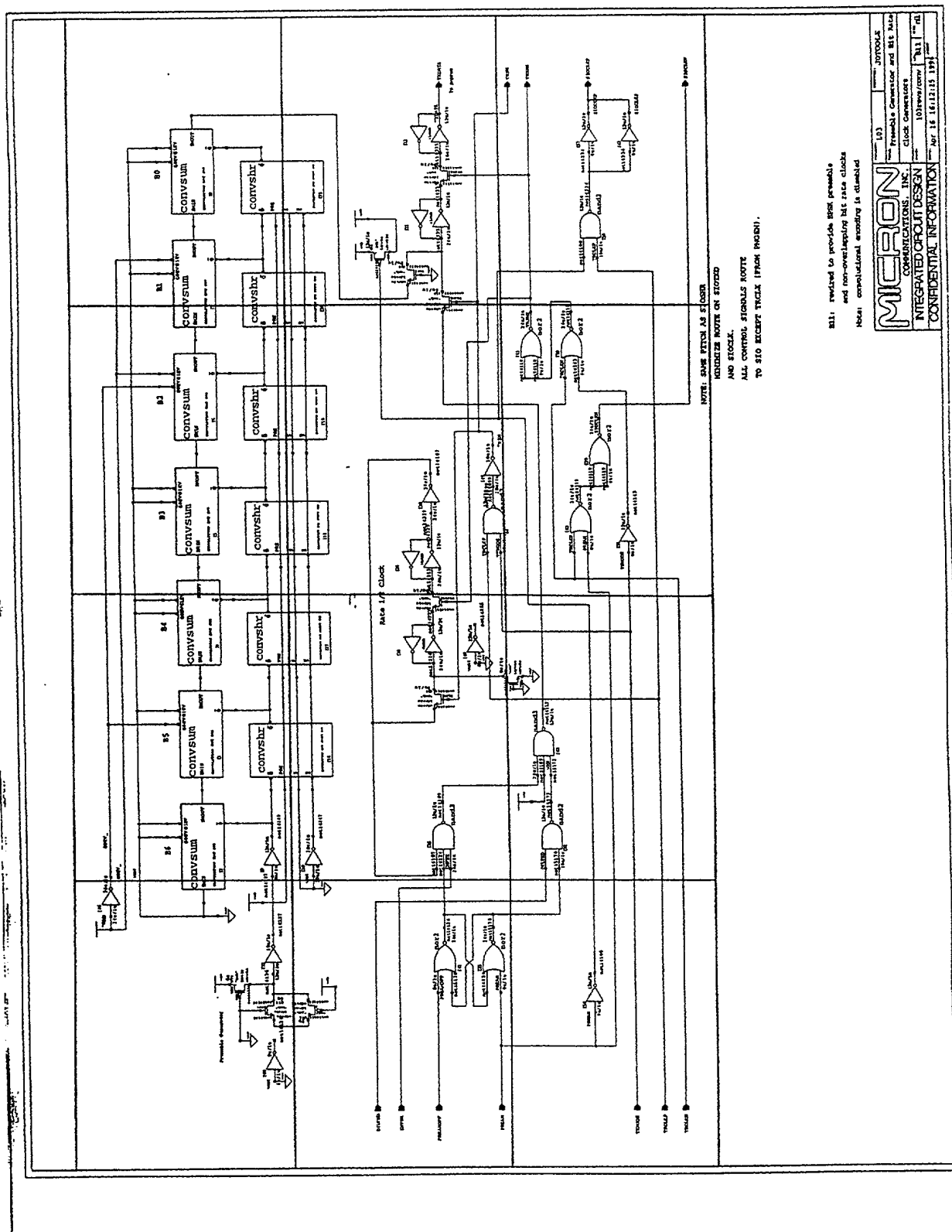
Fig. 7.1502



Ex II      II. II



Fig. 7.16





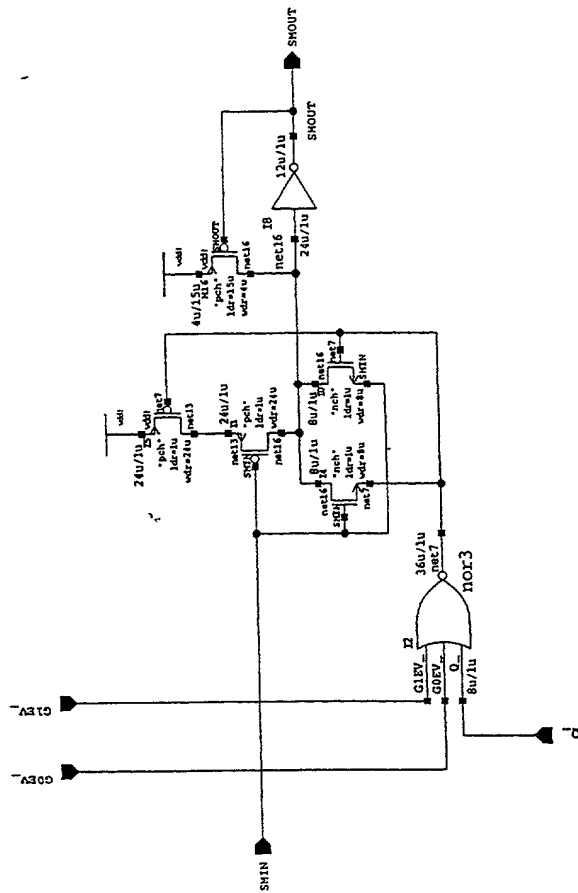


Fig. 7.1602

MICRON		PROJECT: L03		DESIGNER: JOTOOLE	
		TITLE: Convolutional Encoder Summer			
COMMUNICATIONS, INC.		NAME: 103reva/convsum		REV B1	SIZE A
INTEGRATED CIRCUIT DESIGN		DATE: Sep 2 10:32:17 1994			
CONFIDENTIAL INFORMATION					

**MICRON**  
COMMUNICATIONS, INC.

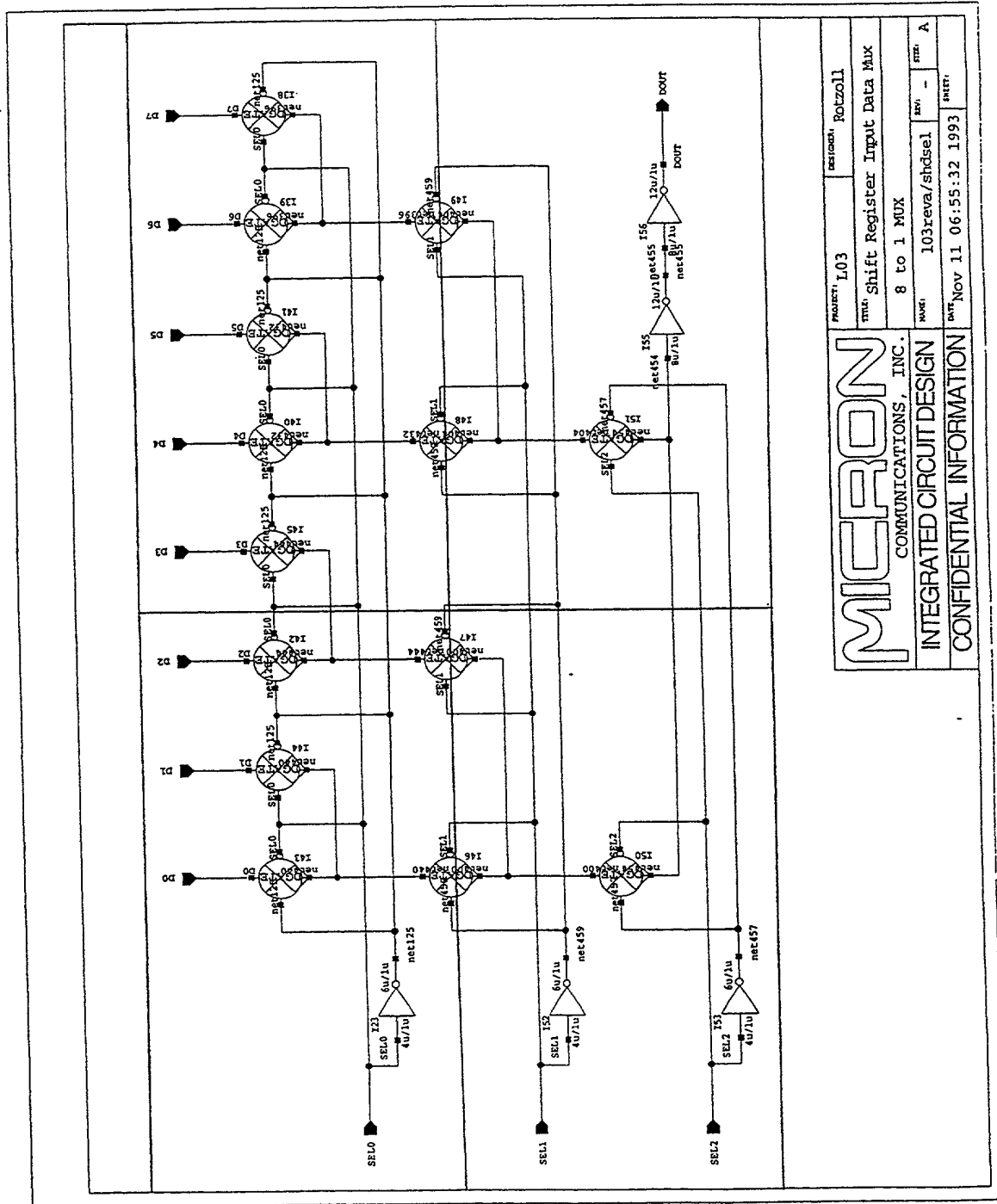
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

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7.17AA	7.17AB
7.17BA	7.17BB

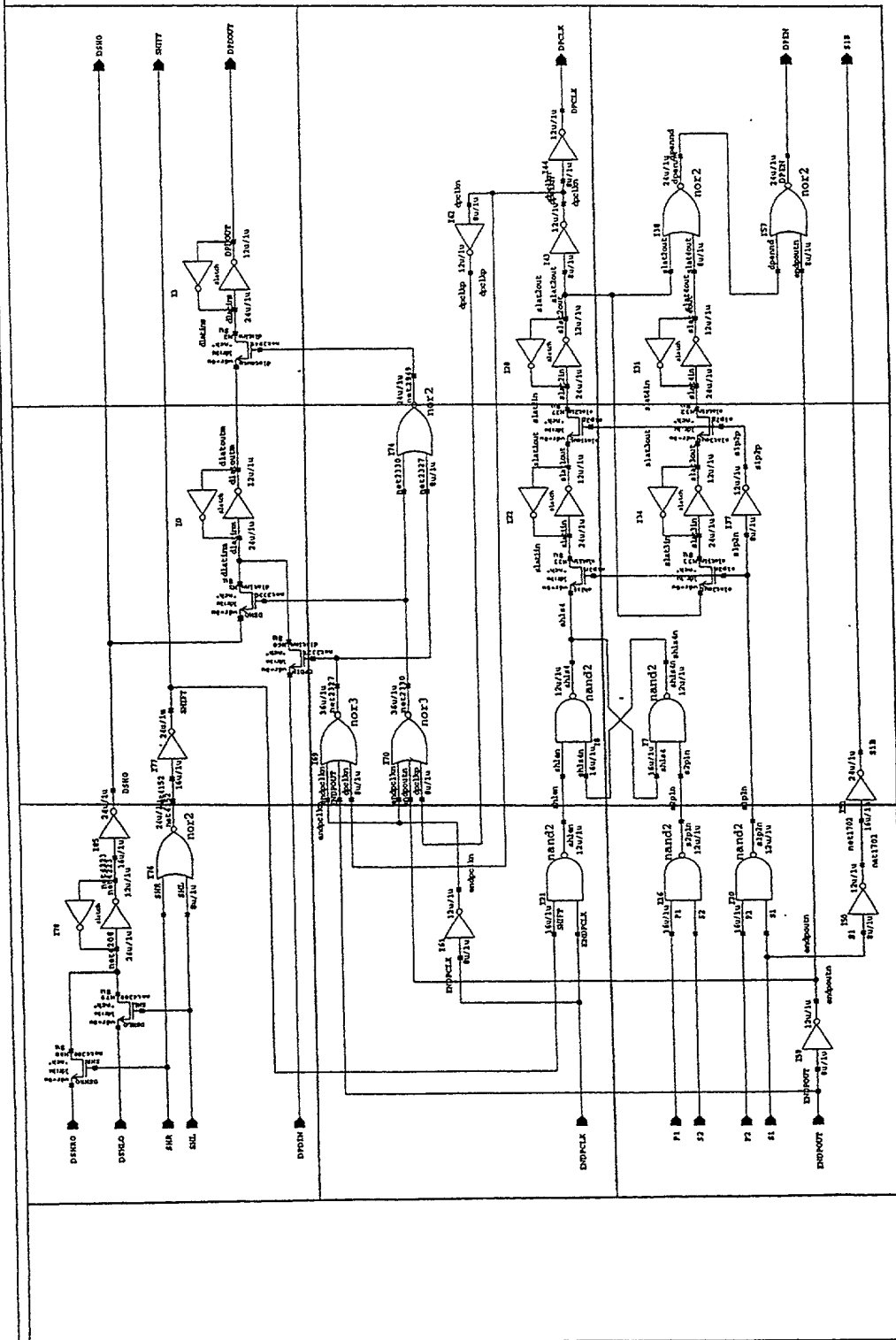
EX-117

Fig. 7.17



PROJECT L03		DESIGNER: Retzoll
Title: Shift Register Input Data Mux		
8 to 1 MUX		
MODE: 103reva/shdrel	REV: -	SER: A
DATE: Nov 11 06:55:32 1993		
CONFIDENTIAL INFORMATION		

7.18AA	7.18AB	7.18AC
7.18BA	7.18BB	7.18BC
7.18CA	7.18CB	7.18CC



8CB





8.01AA 8.01AB 8.01AC 8.01AD 8.01AE

8.01AA	8.01AB	8.01AC	8.01AD	8.01AE
8.01BA	8.01BB	8.01BC	8.01BD	8.01BE
8.01CA	8.01CB	8.01CC	8.01CD	8.01CE
8.01DA	8.01DB	8.01DC	8.01DD	8.01DE

8.01 8.01 8.01 8.01 8.01



ALL INFORMATION CONTAINED  
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8.0101AA	8.0101AB
8.0101BA	8.0101BB
8.0101CA	8.0101CB

EX-107 88.000000

SECRET

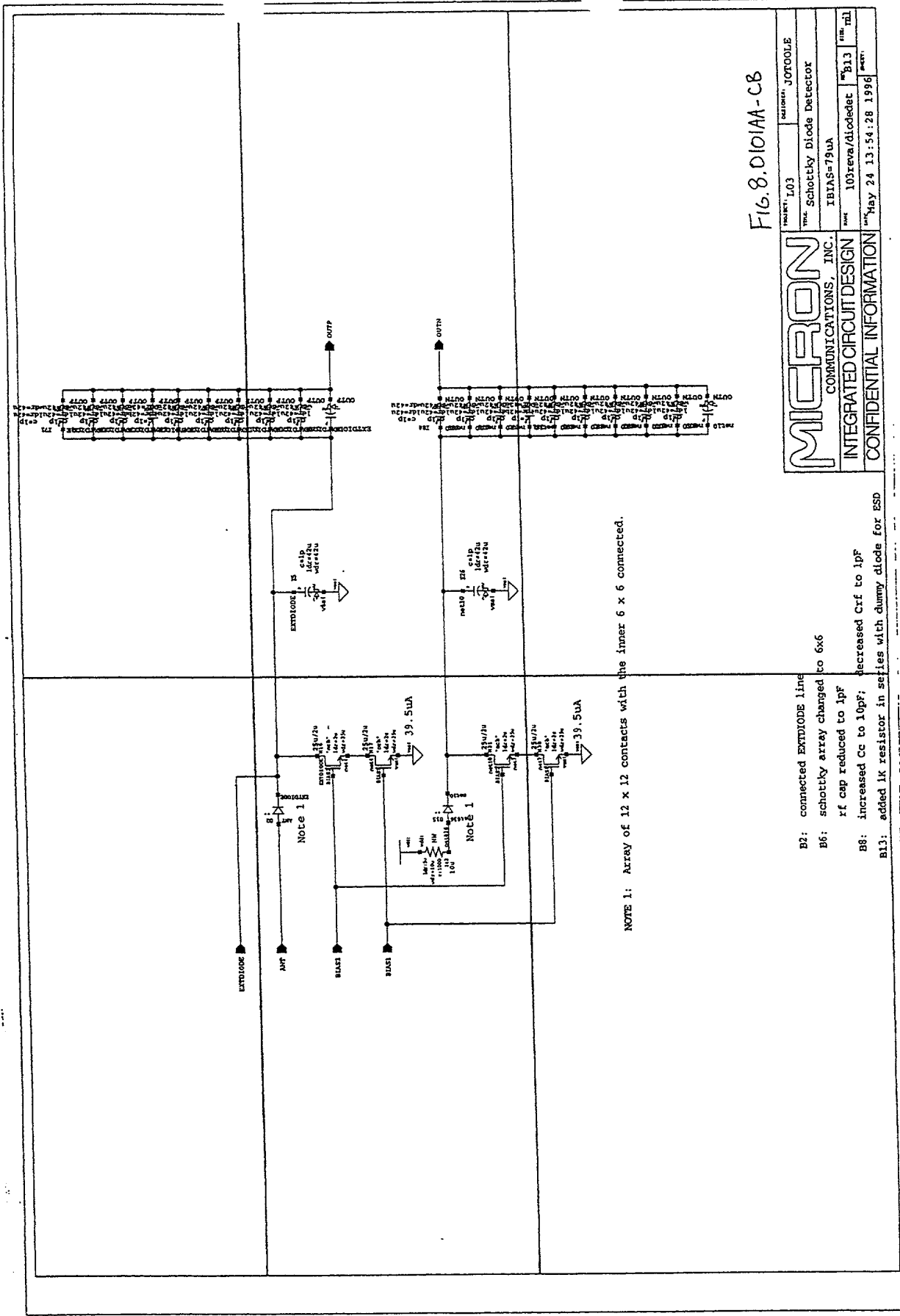


Fig. 8.D101AA-C8

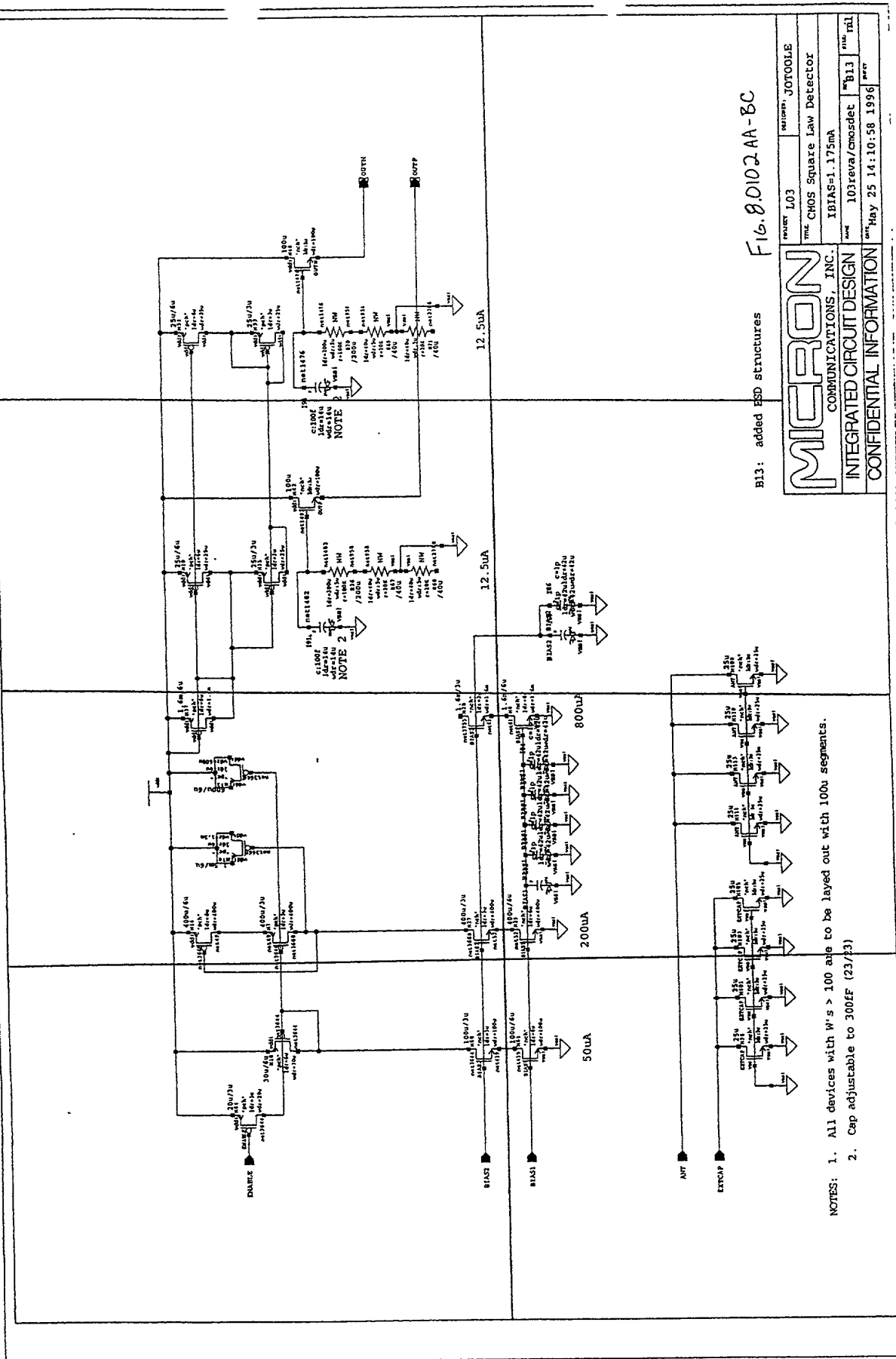
MICRON COMMUNICATIONS, INC. INTEGRATED CIRCUIT DESIGN CONFIDENTIAL INFORMATION	PROJECT: L03	DESIGN: J010010E
	TYPE: Schottky Diode Detector	
	IBIAS=79uA	
	103revs/Alcodelat	Rev B11
DATE: May 24 13:54:28 1996		FILE

- B2: connected EXTIOCODE line
- B6: schottky array changed to 6x6
- rf cap reduced to 1pF
- B8: increased Cc to 10pF; decreased Ccf to 1pF
- B11: added 1K resistor in series with dummy diode for ESD

ALL INFORMATION CONTAINED  
HEREIN IS UNCLASSIFIED

8.0102AA	8.0102AB	8.0102AC	8.0102AD
8.0102BA	8.0102BB	8.0102BC	

SECRET



HB13: added PSD structures

**micron**  
COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

DATE OF BIRTH: 1911-11-11

NOTES: 1. All devices with W's > 100 are to be layed out with 100u segments.

2. Can adjustable to 300FF (23/23)

can adjustable to 300FF (23/13)

8.0103AA 8.0103AB 8.0103AC 8.0103AD 8.0103AE 8.0103AF

8.0103AA	8.0103AB	8.0103AC	8.0103AD	8.0103AE	8.0103AF
8.0103BA	8.0103BB	8.0103BC	8.0103BD	8.0103BE	8.0103BF
8.0103CA	8.0103CB	8.0103CC	8.0103CD	8.0103CE	8.0103CF

8.0103 8.0103 8.0103 8.0103 8.0103 8.0103



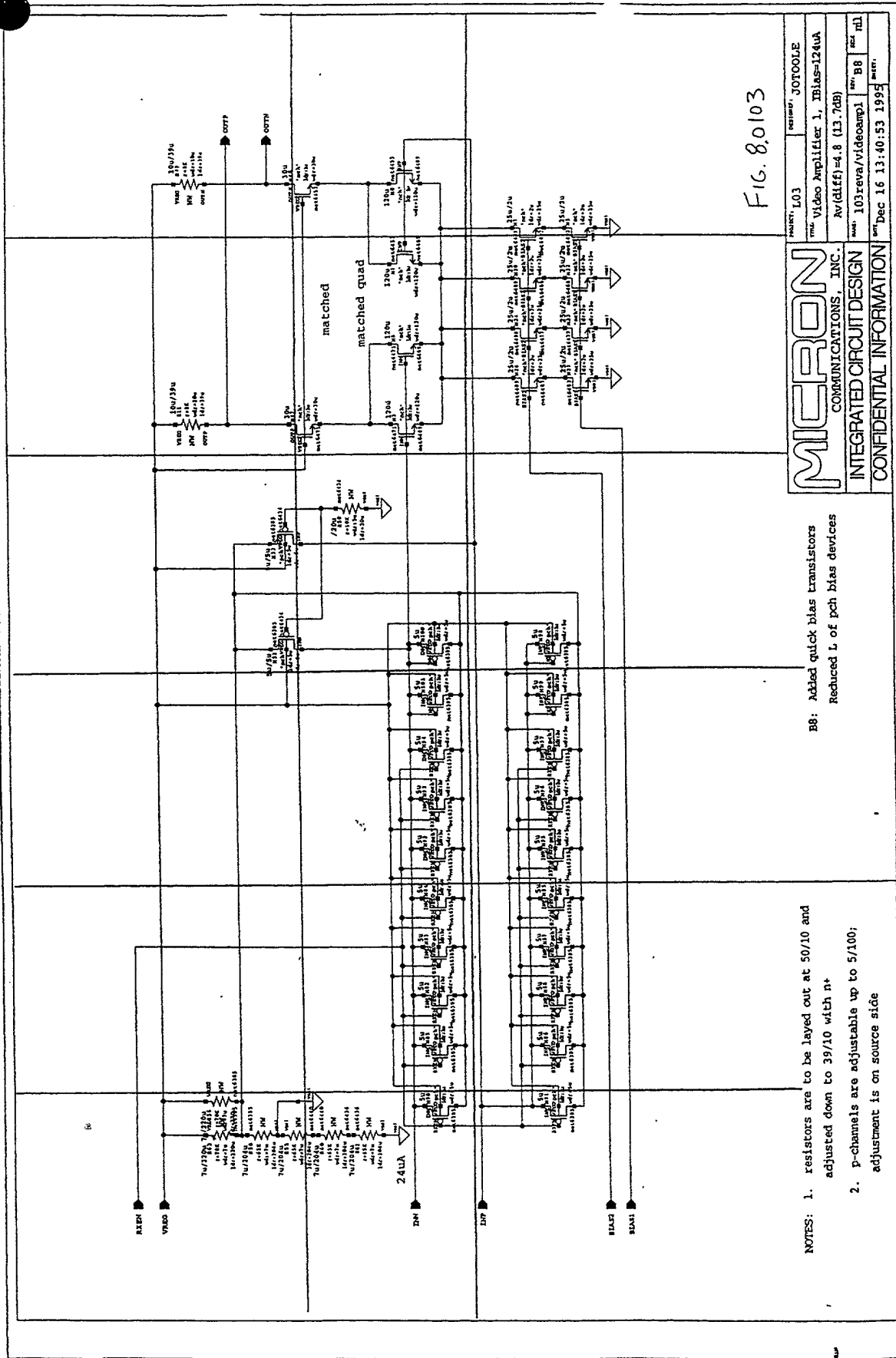


FIG. 8.0103

B8: Added quick bias transistors  
Reduced L of pch bias devices

- NOTES:
1. resistors are to be layed out at 50/10 and adjusted down to 39/10 with n+
  2. p-channels are adjustable up to 5/100; adjustment is on source side

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

PROJECT: L03	REVISION: J07000LE
TYPE: Video Amplifier 1, IBias=12uA	
AV(diff)=4.8 (13.7dB)	
UNIT: 103reva/videoamp1	REV: B8
DATE: Dec 16 13:40:53 1995	REV: n11

8.0104AA	8.0104AB	8.0104AC
8.0104BA	8.0104BB	8.0104BC

EX-107 88.00110044

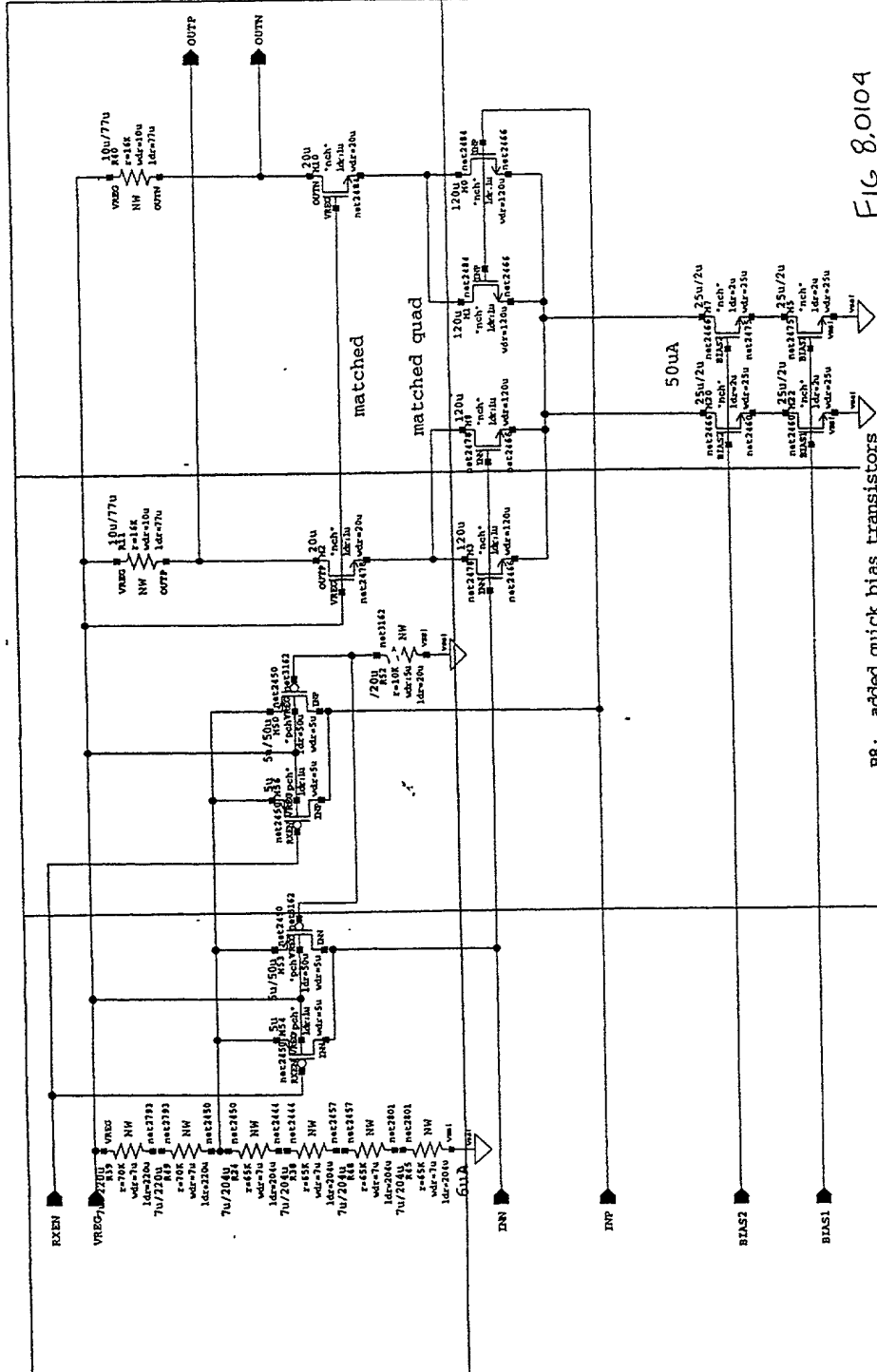


FIG 8.0104

B8: added quick bias transistors

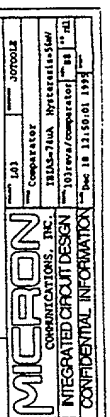
NOTES:

1. resistors are to be layed out at 100/10 and adjusted down to 77/10 with n+
2. p-channels are adjustable up to 5/100; adjustment is on the source side

MICRON		PROJECT: L03		DESIGNER: JOTOOLE	
COMMUNICATIONS, INC.		TITLE: Video Amplifier 2, IBias=56uA			
INTEGRATED CIRCUIT DESIGN		Av(diff)=5.6 (15dB)			
CONFIDENTIAL INFORMATION		NAME: 103:reva/videoamp2		REV: B8	SIZE: A
		DATE: Dec 16 13:42:25 1995		SHEET:	

8.0105AA	8.0105AB	8.0105AC	8.0105AD	
8.0105BA	8.0105BB	8.0105BC	8.0105BD	
8.0105CA	8.0105CB	8.0105CC	8.0105CD	8.0105CE
8.0105DA	8.0105DB	8.0105DC	8.0105DD	8.0105DE
8.0105EA	8.0105EB	8.0105EC	8.0105ED	

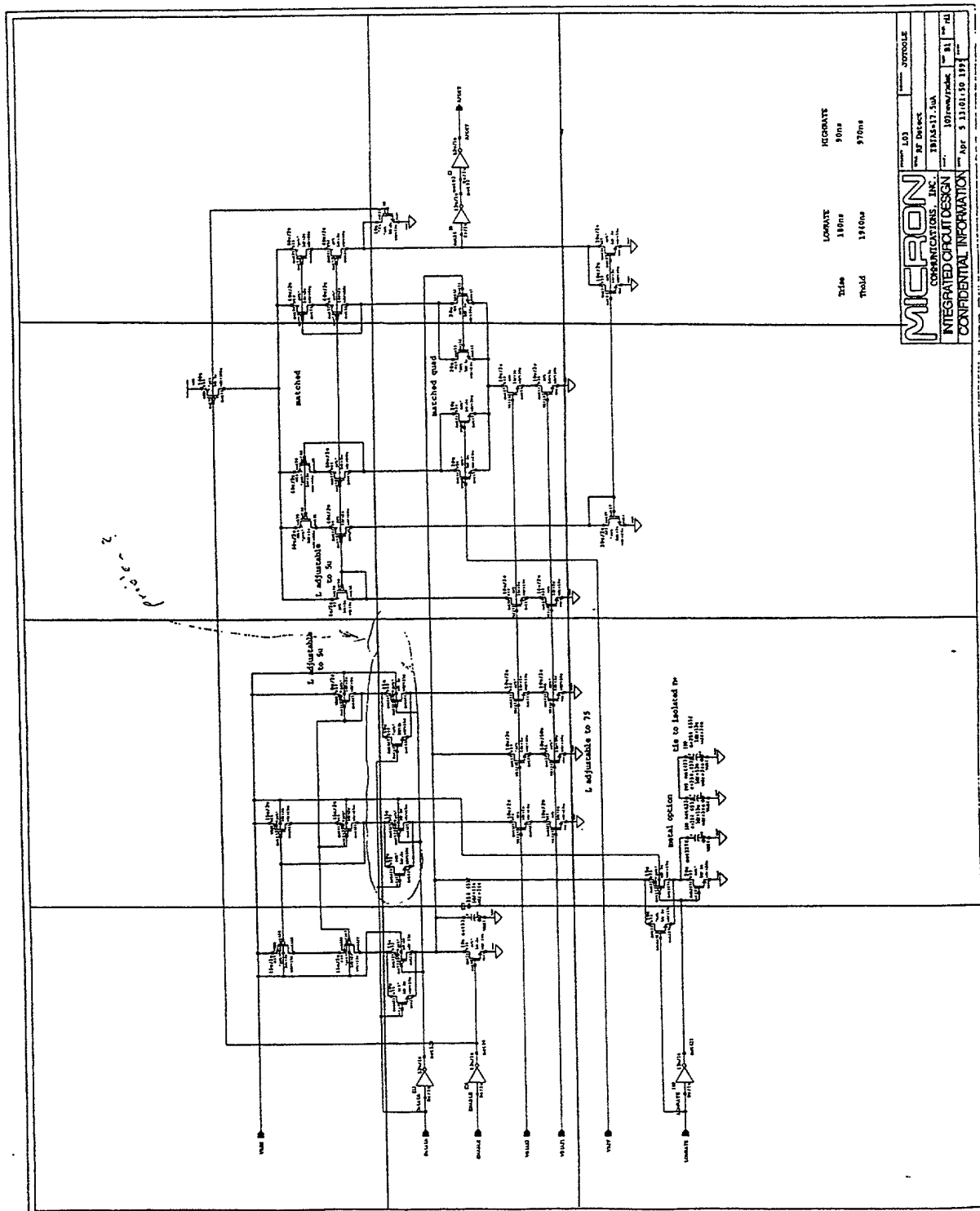
EE 88.01.11.005



8.0106AA	8.0106AB	8.0106AC	8.0106AD
8.0106BA	8.0106BB	8.0106BC	8.0106BD
8.0106CA	8.0106CB	8.0106CC	8.0106CD

II II II II II II

FIG. 8.0106

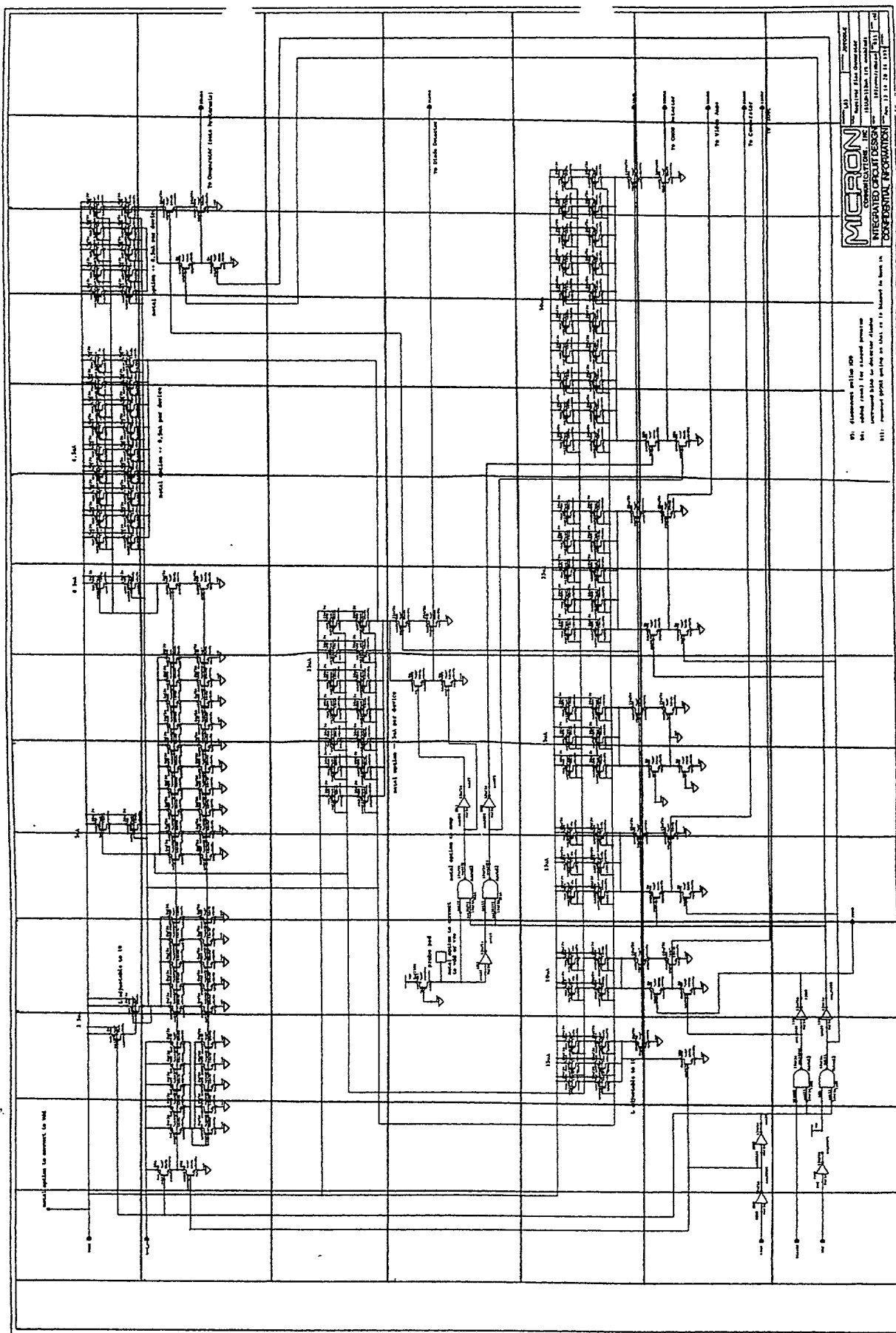


8.0107AA	8.0107AB	8.0107AC	8.0107AD	8.0107AE	8.0107AF	8.0107AG	8.0107AH	8.0107AJ	8.0107AK	8.0107AL	8.0107AM
8.0107BA	8.0107BB	8.0107BC	8.0107BD	8.0107BE	8.0107BF	8.0107BG	8.0107BH	8.0107BJ	8.0107BK	8.0107BL	8.0107BN
8.0107CA	8.0107CB	8.0107CC	8.0107CD	8.0107CE	8.0107CF	8.0107CG	8.0107CH	8.0107CJ	8.0107CK	8.0107CL	8.0107CN
8.0107DA	8.0107DB	8.0107DC	8.0107DD	8.0107DE	8.0107DF	8.0107DG	8.0107DH	8.0107DJ	8.0107DK	8.0107DL	8.0107DN
8.0107EA	8.0107EB	8.0107EC	8.0107ED	8.0107EE	8.0107EF	8.0107EG	8.0107EH	8.0107EJ	8.0107EK	8.0107EL	8.0107EN
8.0107FA	8.0107FB	8.0107FC	8.0107FD	8.0107FE	8.0107FF	8.0107FG	8.0107FH	8.0107FJ	8.0107FK	8.0107FL	8.0107FN
8.0107GA	8.0107GB	8.0107GC	8.0107GD	8.0107GE	8.0107GF	8.0107GG	8.0107GH	8.0107GJ	8.0107GK	8.0107GL	8.0107GN


 БЕЛОРУССКАЯ НАРОДНАЯ РЕСПУБЛИКА  
 МИНУСТВА ВНЕШНИХ СВОТОН  
 БЕЛОРУССКАЯ НАРОДНАЯ РЕСПУБЛИКА



Variable	Mean	Standard Deviation	Minimum	Maximum
Age	34.5	10.5	20	55
Gender	0.5	0.5	0	1
Marital Status	0.5	0.5	0	1
Education	12.5	1.5	10	15
Income	3500	1500	1000	7000
Health	0.5	0.5	0	1
Smoking	0.2	0.4	0	1
Drinking	0.1	0.3	0	1
Exercise	0.3	0.5	0	1
Stress	0.4	0.5	0	1
Sleep	0.5	0.5	0	1
Work	0.5	0.5	0	1
Family	0.5	0.5	0	1
Friends	0.5	0.5	0	1
Community	0.5	0.5	0	1
Society	0.5	0.5	0	1
Nature	0.5	0.5	0	1
Art	0.5	0.5	0	1
Music	0.5	0.5	0	1
Food	0.5	0.5	0	1
Travel	0.5	0.5	0	1
Shopping	0.5	0.5	0	1
Reading	0.5	0.5	0	1
Writing	0.5	0.5	0	1
Painting	0.5	0.5	0	1
Dancing	0.5	0.5	0	1
Gardening	0.5	0.5	0	1
Fishing	0.5	0.5	0	1
Hiking	0.5	0.5	0	1
Cycling	0.5	0.5	0	1
Swimming	0.5	0.5	0	1
Boating	0.5	0.5	0	1
Volunteering	0.5	0.5	0	1
Charity	0.5	0.5	0	1
Religion	0.5	0.5	0	1
Philosophy	0.5	0.5	0	1
Science	0.5	0.5	0	1
History	0.5	0.5	0	1
Geography	0.5	0.5	0	1
Language	0.5	0.5	0	1
Mathematics	0.5	0.5	0	1
Physics	0.5	0.5	0	1
Chemistry	0.5	0.5	0	1
Biology	0.5	0.5	0	1
Medicine	0.5	0.5	0	1
Law	0.5	0.5	0	1
Business	0.5	0.5	0	1
Engineering	0.5	0.5	0	1
Architecture	0.5	0.5	0	1
Design	0.5	0.5	0	1
Technology	0.5	0.5	0	1
Environment	0.5	0.5	0	1
Climate	0.5	0.5	0	1
Weather	0.5	0.5	0	1
Time	0.5	0.5	0	1
Space	0.5	0.5	0	1
Energy	0.5	0.5	0	1
Force	0.5	0.5	0	1
Mass	0.5	0.5	0	1
Length	0.5	0.5	0	1
Width	0.5	0.5	0	1
Height	0.5	0.5	0	1
Weight	0.5	0.5	0	1
Volume	0.5	0.5	0	1
Area	0.5	0.5	0	1
Perimeter	0.5	0.5	0	1
Angle	0.5	0.5	0	1
Speed	0.5	0.5	0	1
Distance	0.5	0.5	0	1
Time	0.5	0.5	0	1
Frequency	0.5	0.5	0	1
Wavelength	0.5	0.5	0	1
Amplitude	0.5	0.5	0	1
Phase	0.5	0.5	0	1
Frequency	0.5	0.5	0	1
Wavelength	0.5	0.5	0	1
Amplitude	0.5	0.5	0	1
Phase	0.5	0.5	0	1
Frequency	0.5	0.5	0	1
Wavelength	0.5	0.5	0	1
Amplitude	0.5	0.5	0	1
Phase	0.5	0.5	0	1
Frequency	0.5	0.5	0	1
Wavelength	0.5	0.5	0	1
Amplitude	0.5	0.5	0	1
Phase	0.5	0.5	0	1
Frequency	0.5	0.5	0	



516.8.0107

ALL INFORMATION CONTAINED  
HEREIN IS UNCLASSIFIED

8.0108AA	8.0108AB	8.0108AC
----------	----------	----------

EX-107 88.00.11.0088

micron communications, inc. 10000 ne 10th ave. denver, co 80231

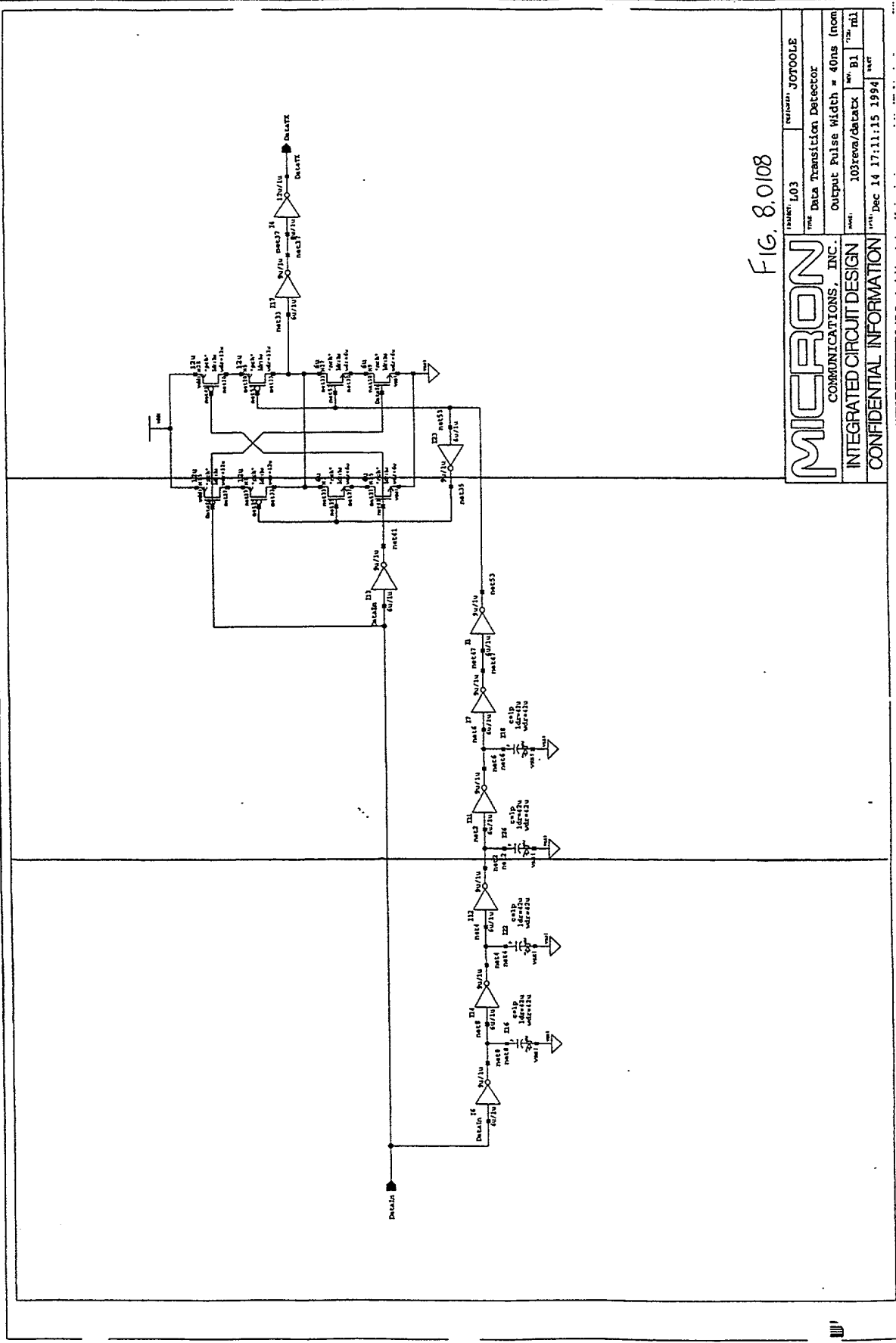


Fig. 8.0108

MICRON		PROJECT: L03	REVISION: J07000LE
COMMUNICATIONS, INC.		Data Transition Detector	
INTEGRATED CIRCUIT DESIGN		Output Pulse Width = 40ns (nom)	
CONFIDENTIAL INFORMATION		part: 103revm/databx	rev: B1
		date: Dec 14 17:11:15 1994	

8.02AA	8.02AB	8.02AC
8.02BA	8.02BB	8.02BC

EE 88.002

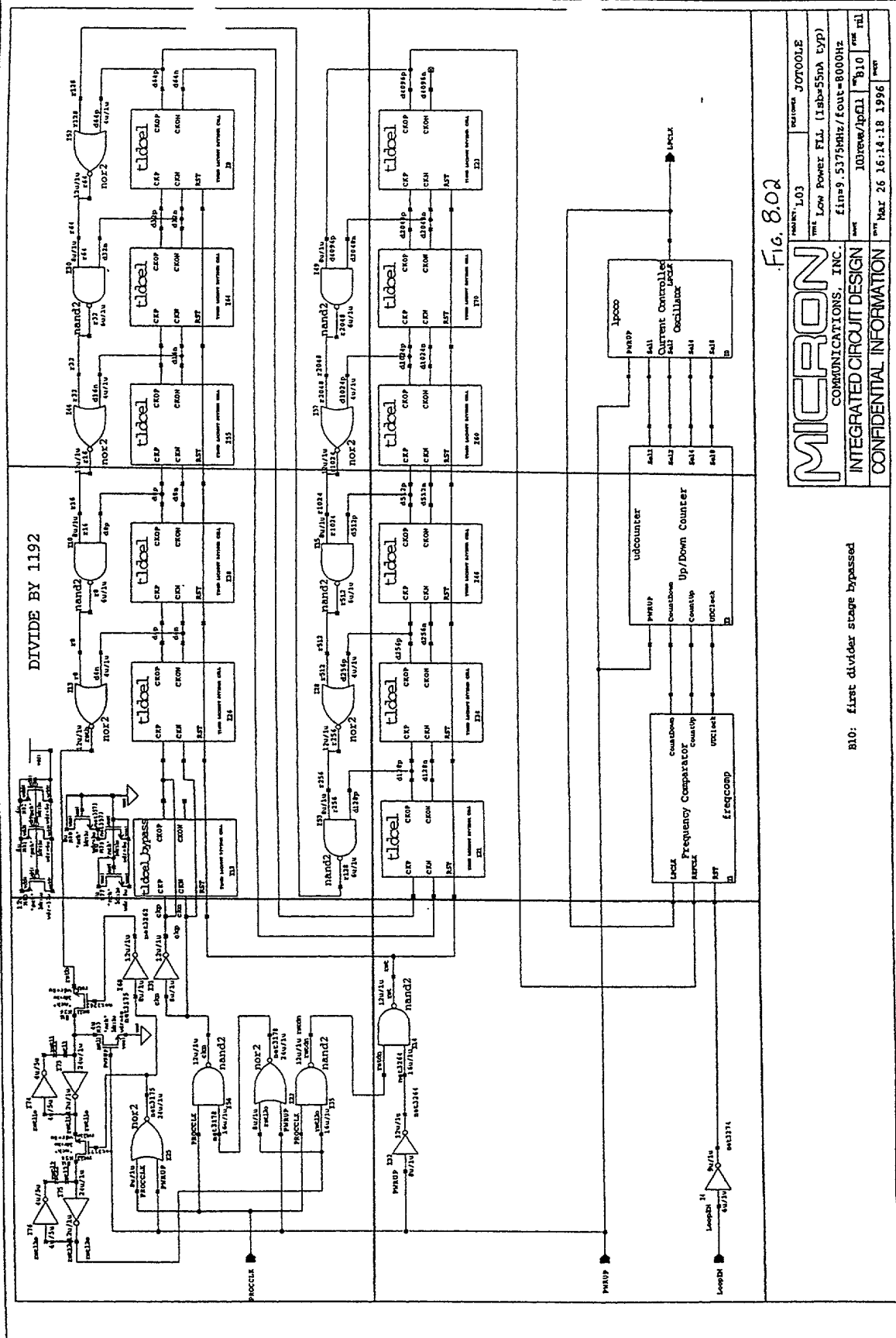


Fig. 8.02

MICRON		Part Number	J0700LE
COMMUNICATIONS, INC.		Low Power PLL (100-550a typ)	
INTEGRATED CIRCUIT DESIGN		Pin 9, 5.375MHz/fout=8000Hz	
CONFIDENTIAL INFORMATION		Date	10/26/81
		Rev	1.0
		Printed	Mar 26 16:14:18 1996

810: first divider stage bypassed

8.0201AB

8.0201AA

EX 8.0201

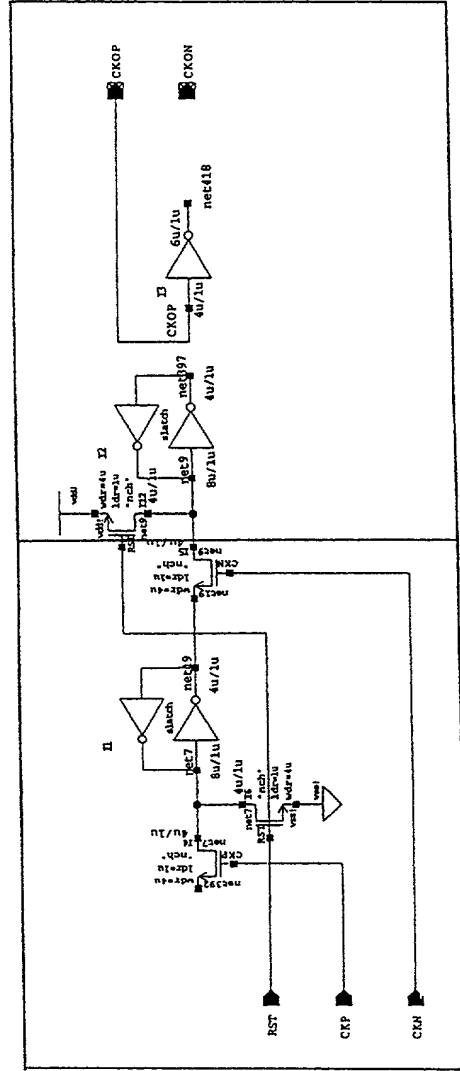


Fig. 8.0201

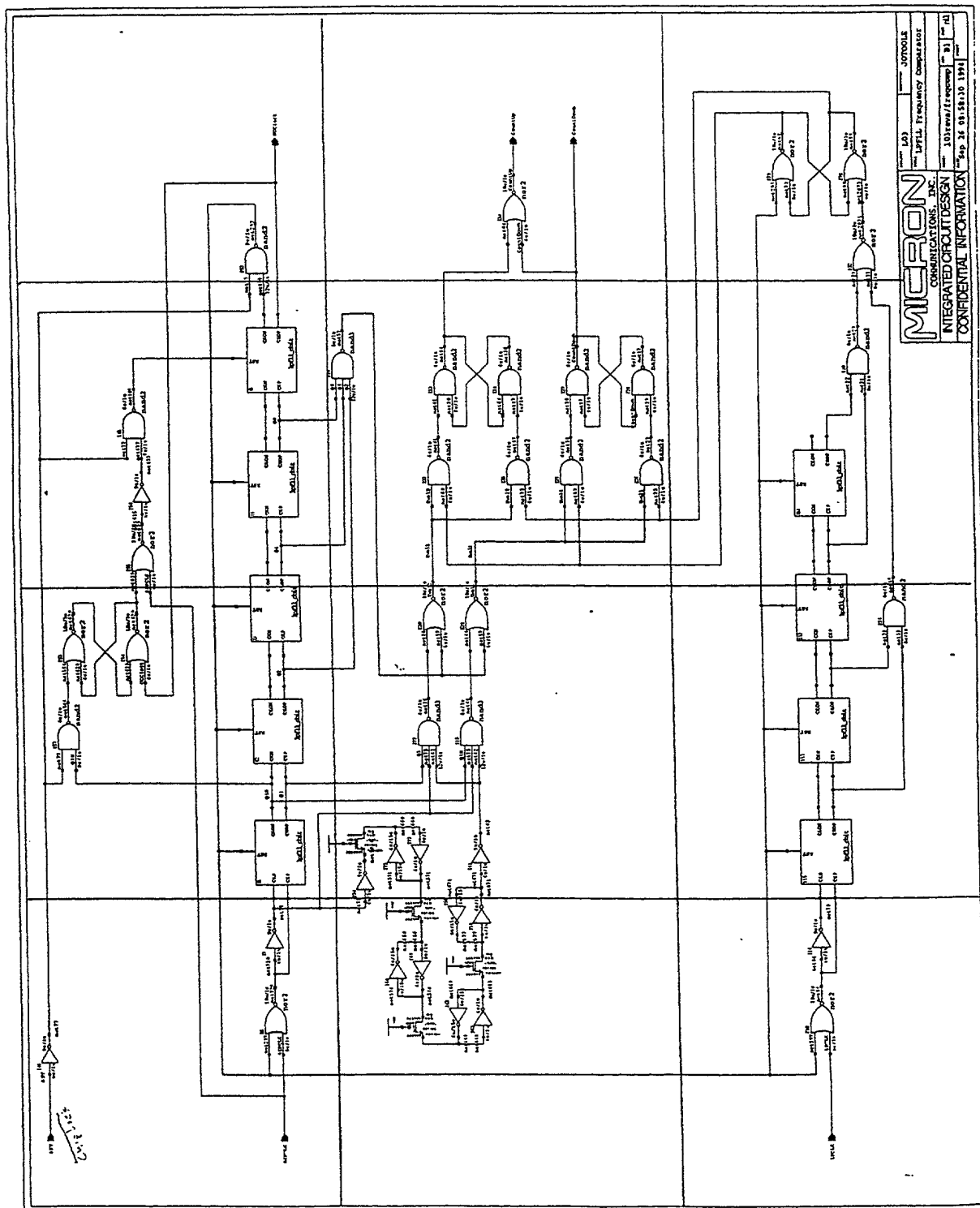
PROJECT: L03		DESIGNER: JOTOOLE	
TITLE: Titled Lockout Divider Cell			
PART: T03reva/tldcel_bypass		REV: B10	
DATE: Mar 26 13:54:47 1996		SHEET: A	

B10: new cell to bypass 1st counter stage

8.0202AA	8.0202AB	8.0202AC	8.0202AD
8.0202BA	8.0202BB	8.0202BC	8.0202BD
8.0202CA	8.0202CB	8.0202CC	8.0202CD



SECRET



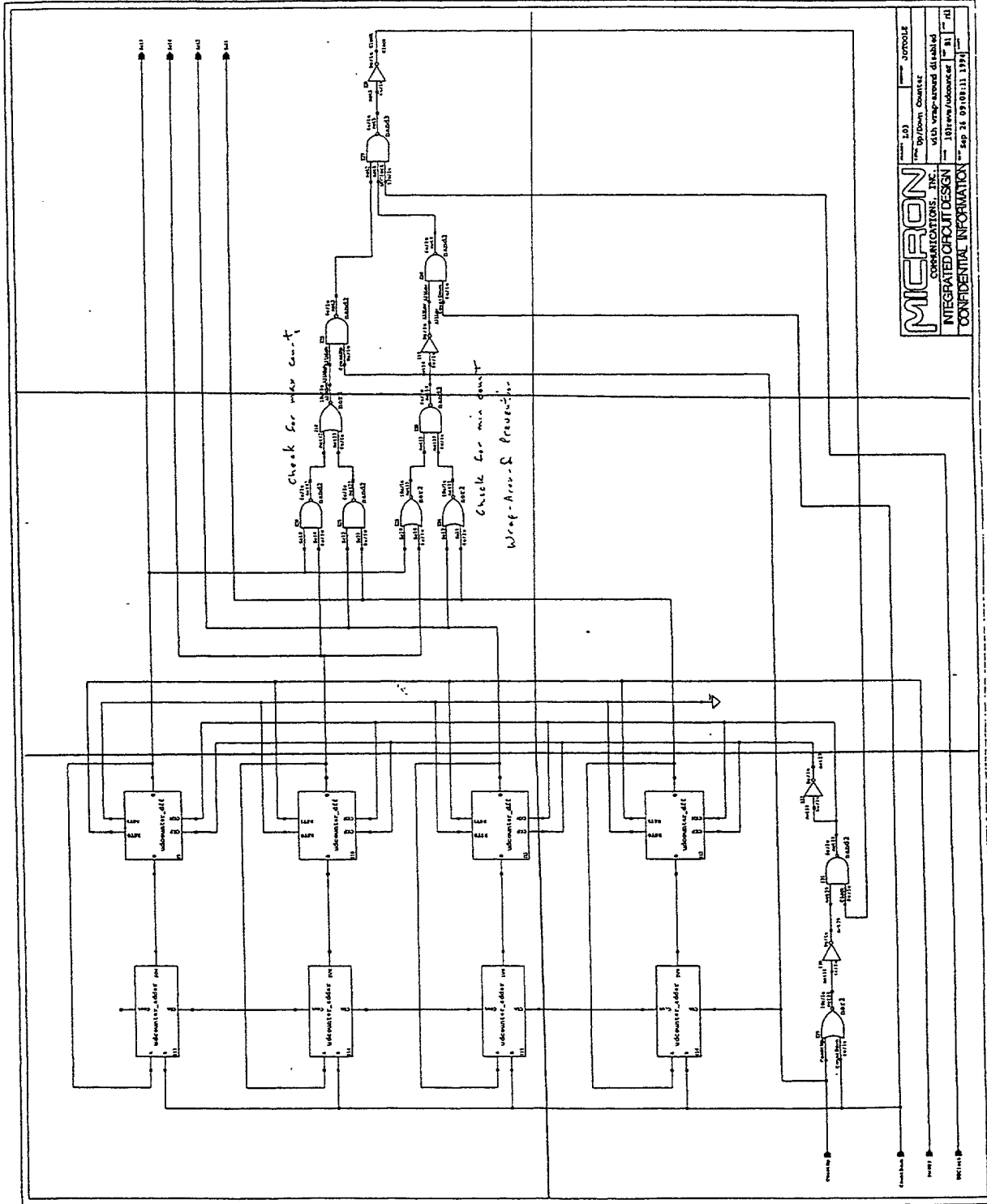
**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION  
Sep 24 09:58:10 1991

FIG. 8,0202

ALL INFORMATION CONTAINED HEREIN IS UNCLASSIFIED

8.0203AA	8.0203AB	8.0203AC
8.0203BA	8.0203BB	8.0203BC

ALL INFORMATION CONTAINED HEREIN IS UNCLASSIFIED



**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

Rev. 1.0  
New Up/Down Counter  
with wrap-around disabled  
1810000/1810000  
Rev. 1.0  
Sep. 24, 1974

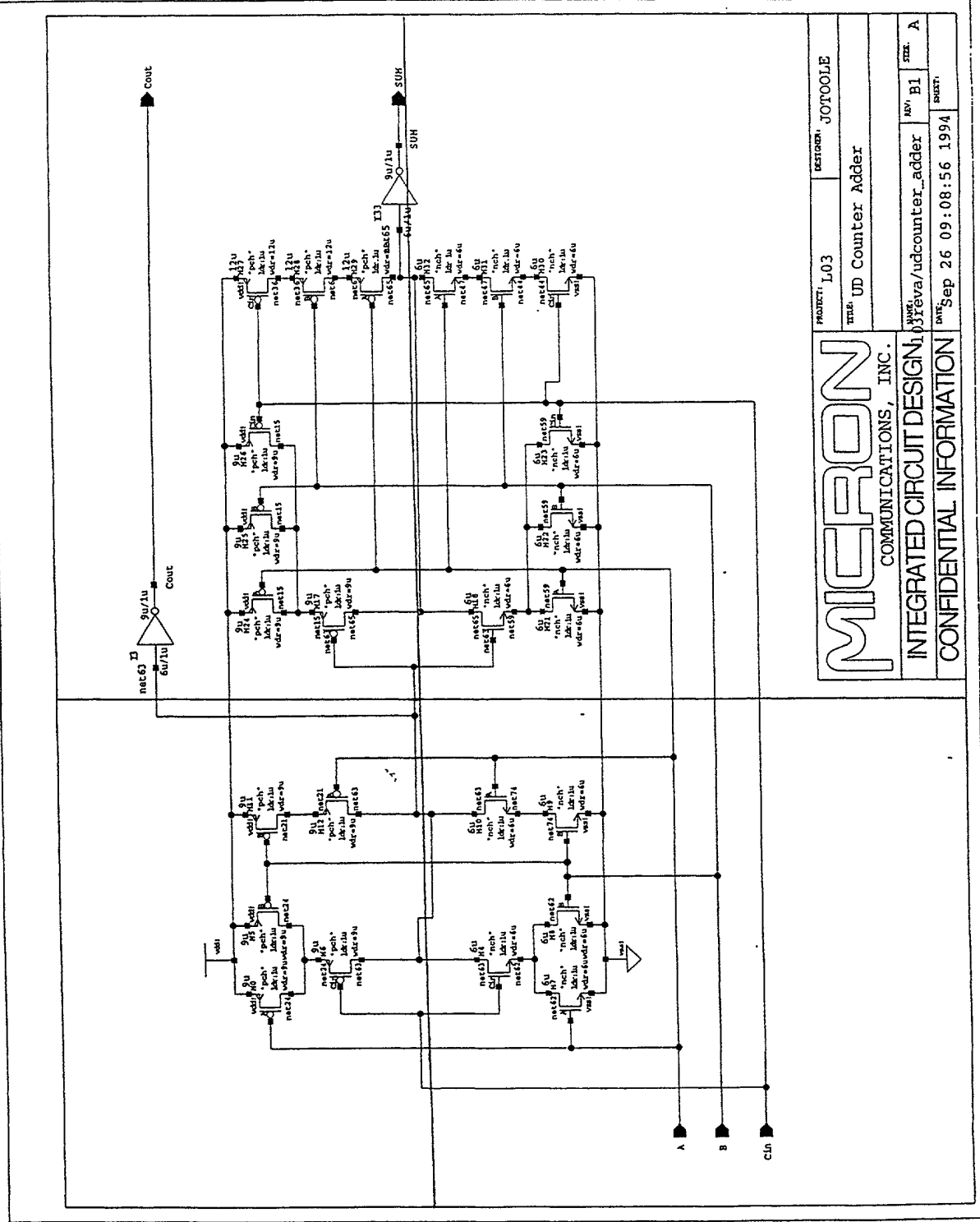
FIG. 8.0203

ALL INFORMATION CONTAINED HEREIN IS UNCLASSIFIED

8.020301AA	8.020301AB
8.020301BA	8.020301BB

ALL INFORMATION CONTAINED HEREIN IS UNCLASSIFIED

SECRET



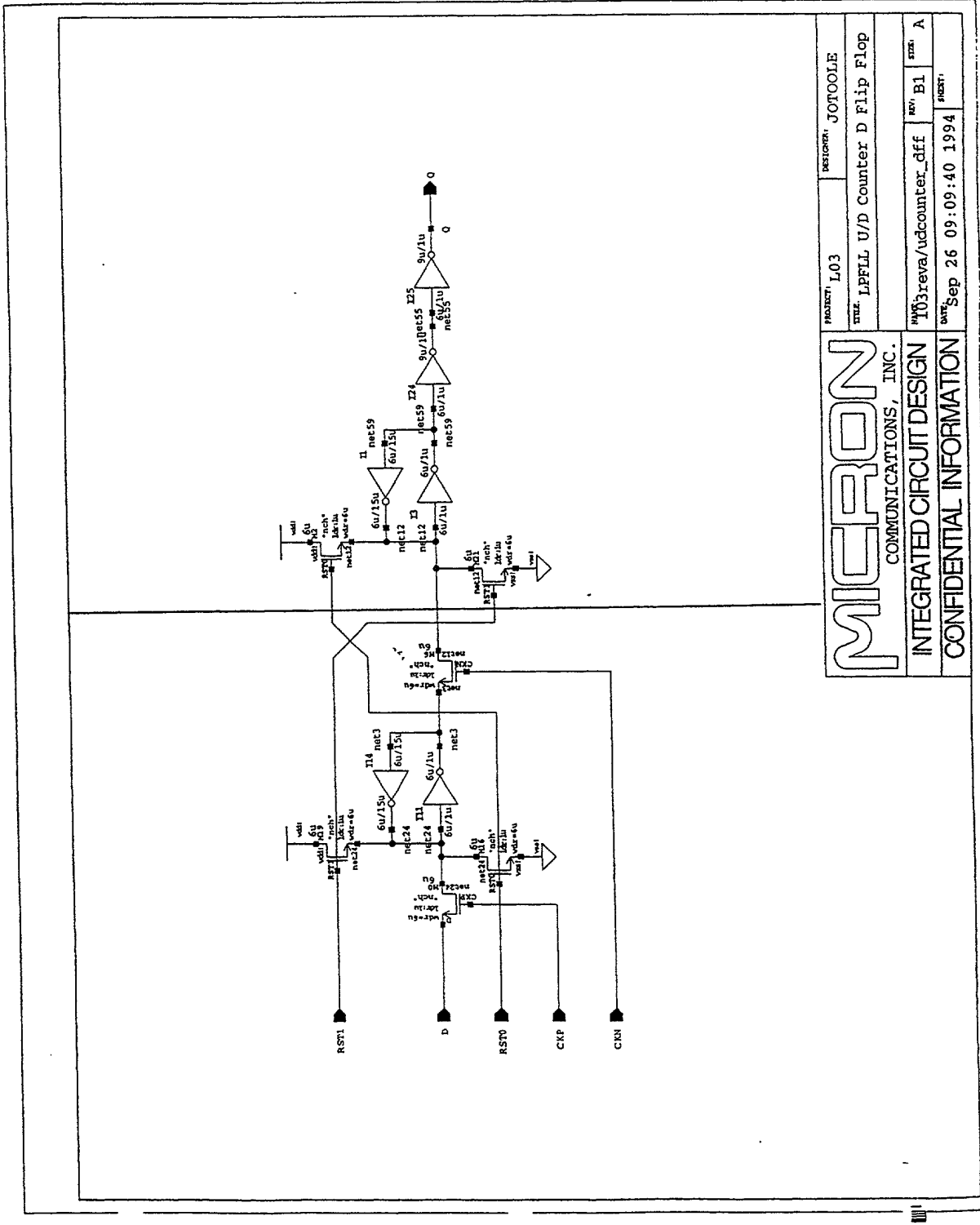
MICRON		PROJECT: L03	SECTION: J0700LE
COMMUNICATIONS, INC.		TITLE: UD Counter Address	
INTEGRATED CIRCUIT DESIGN		NAME: jreva/udcounter_addr	REV: B1
CONFIDENTIAL INFORMATION		DATE: Sep 26 09:08:56 1994	SIZE: A

SECRET

FIG. 8.020501

8.020302AB

2008.08.27



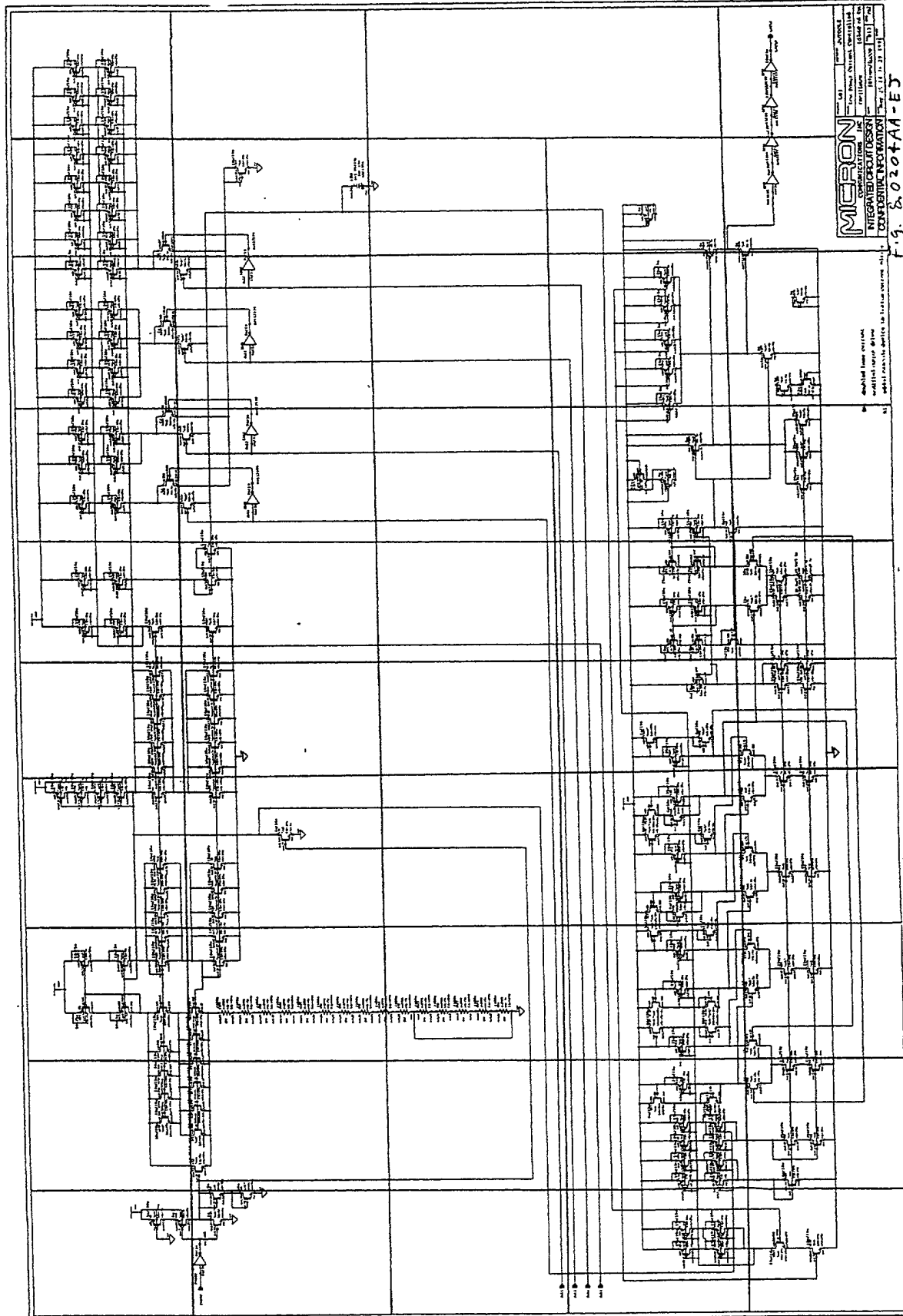
MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TYPE: LPFLL U/D Counter D Flip Flop	
INTEGRATED CIRCUIT DESIGN		REV: B1	SIZE: A
CONFIDENTIAL INFORMATION		DATE: Sep 26 09:09:40 1994	SHEET: 1

Fig. 8.020302

8.0204AA	8.0204AB	8.0204AC	8.0204AD	8.0204AE	8.0204AF	8.0204AG	8.0204AH	8.0204AI	8.0204AJ
8.0204BA	8.0204BB	8.0204BC	8.0204BD	8.0204BE	8.0204BF	8.0204BG	8.0204BH	8.0204BI	8.0204BJ
8.0204CA	8.0204CB	8.0204CC	8.0204CD	8.0204CE	8.0204CF	8.0204CG	8.0204CH	8.0204CI	
8.0204DA	8.0204DB	8.0204DC	8.0204DD	8.0204DE	8.0204DF	8.0204DG	8.0204DH	8.0204DI	
8.0204EA	8.0204EB	8.0204EC	8.0204ED	8.0204EE	8.0204EF	8.0204EG	8.0204EH	8.0204EI	8.0204EJ



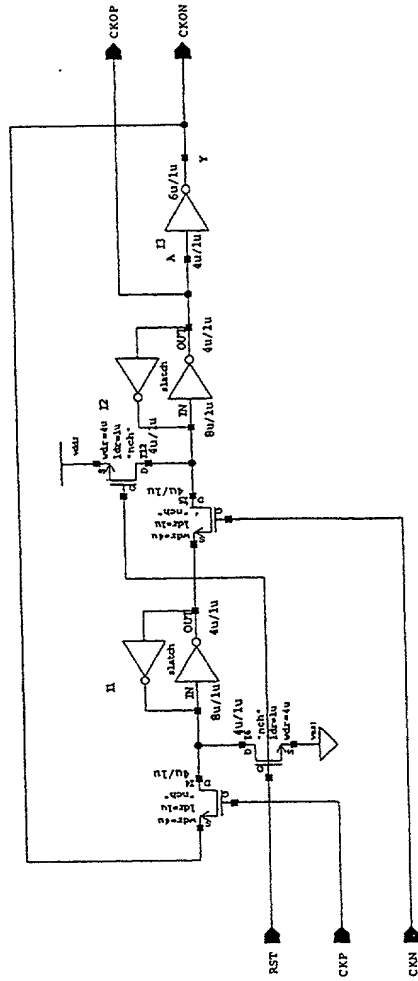
SECRET



**MICRON**  
MICRO CHANNEL ARCHITECTURE  
CONFIDENTIAL INFORMATION  
Rev. 1.0, 12-11-78

F.S. 8.0204AA-E3

SECRET



12/29/92

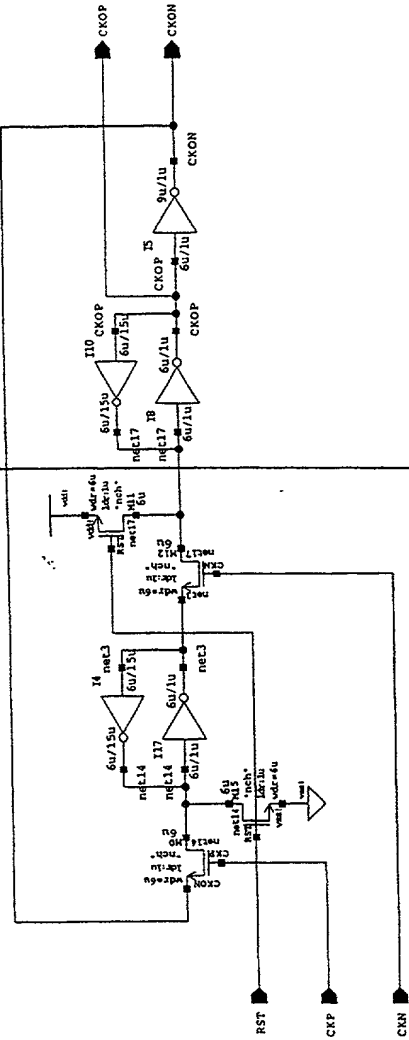
MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: Timed Lockout Divider Cell	
INTEGRATED CIRCUIT DESIGN		NAME: 103revb/tldcel	REV: A
CONFIDENTIAL INFORMATION		DATE: Sep 22 15:26:56 1994	SHEET: 1

~~SECRET~~  
F1680205

8.03AB

8.03AA

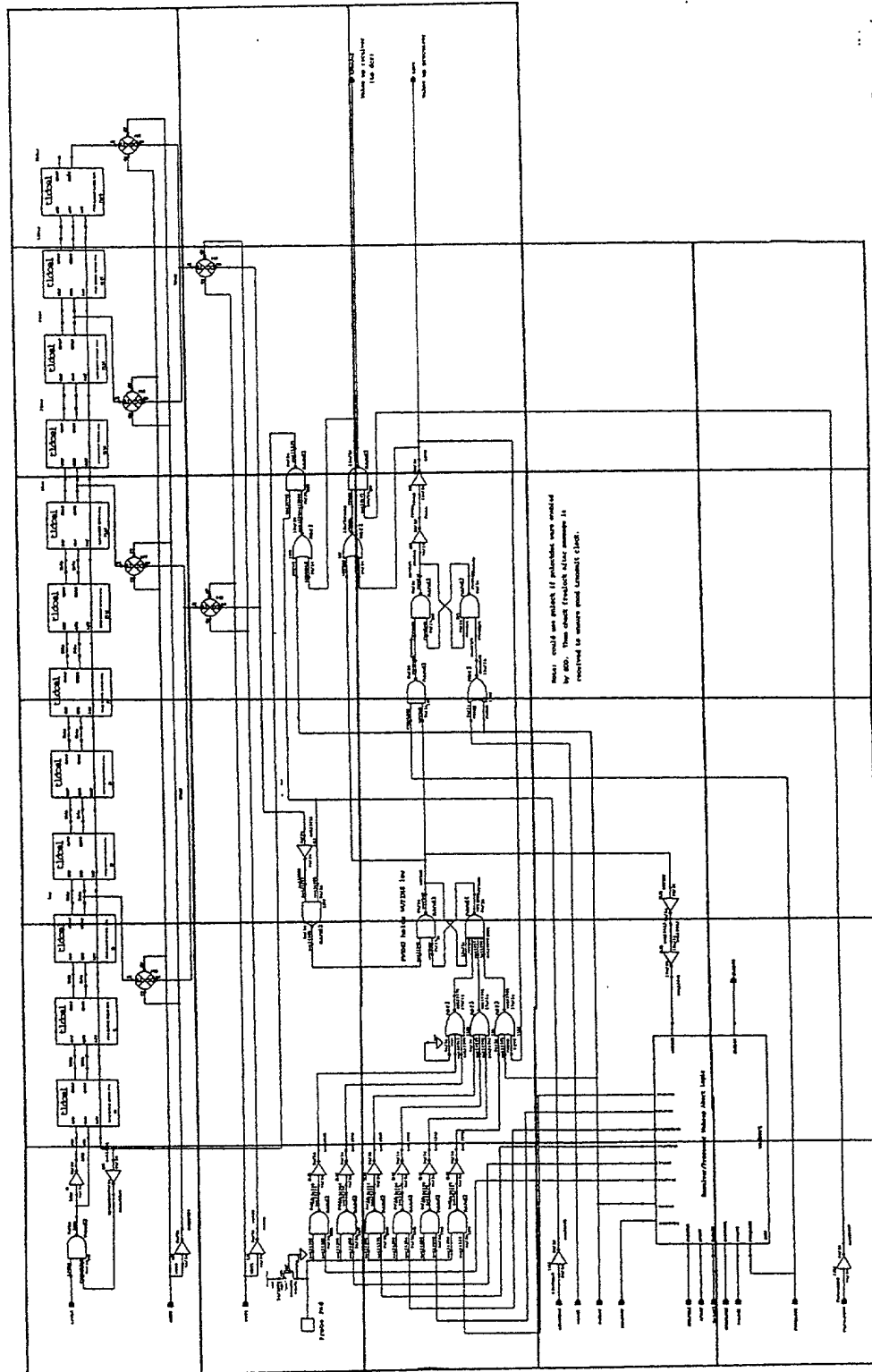
BB.0000



MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: LPFLL Counter Bit	
INTEGRATED CIRCUIT DESIGN		NAME: 103revs/lpfll_bit	REV: B1
CONFIDENTIAL INFORMATION		DATE: Sep 26 08:59:33 1994	SHEET: A

FIG. 8.03

И. П. Б. И.



Note: could use stack if pointers were added by 800. Then check feedback after pointer is received to ensure good transmit clock.

FIG. 804

801. Power channel to power  
802. Adjusted power lines to transmitter for with last  
803. Adjusted power lines to transmitter for with last  
804. Adjusted power lines to transmitter for with last

Π Π Π Π Π

The diagram illustrates the internal logic of the MICRON 8.0401AA-EE integrated circuit. It features several key functional blocks:

- RF Detect Timer:** Located on the left, it includes a chain of D-type flip-flops and logic gates (AND, OR, NOT) for timing and detection.
- Chip Clock Counter:** A central component consisting of a series of D-type flip-flops used for clock division and counting.
- Data Transition Counter:** Located on the right, it uses D-type flip-flops and logic gates to monitor data transitions.
- Control Logic:** Various AND, OR, and NOT gates are distributed throughout the circuit to manage the flow of data and control signals.

**Legend:**

- 81, output to output stage
- 82, internal logic only, not to be used
- 83, internal logic only, not to be used
- 84, internal logic only, not to be used
- 85, internal logic only, not to be used
- 86, internal logic only, not to be used
- 87, internal logic only, not to be used
- 88, internal logic only, not to be used
- 89, internal logic only, not to be used
- 90, internal logic only, not to be used
- 91, internal logic only, not to be used
- 92, internal logic only, not to be used
- 93, internal logic only, not to be used
- 94, internal logic only, not to be used
- 95, internal logic only, not to be used
- 96, internal logic only, not to be used
- 97, internal logic only, not to be used
- 98, internal logic only, not to be used
- 99, internal logic only, not to be used
- 100, internal logic only, not to be used

**Figure 8.0401AA-EE**

**MICRON**  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

**MICRON**  
 COMMUNICATIONS, INC.  
 INTEGRATED CIRCUIT DESIGN  
 CONFIDENTIAL INFORMATION

NAME: SAI TITLE: SALES  
 COMPANY: SAI ADDRESS: 10111 101st Ave. N.E.  
 CITY: REDLAND, CA 92350 PHONE: (714) 781-1111  
 FAX: (714) 781-1111

[illegible]

... .. with ... ..

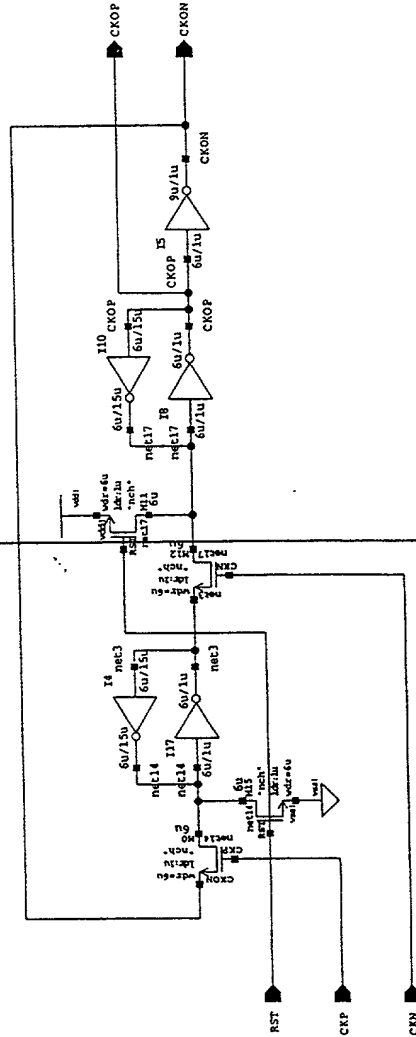


8.040101AB

8.040101AA

II II III

CONFIDENTIAL



**MICRON**  
COMMUNICATIONS, INC.

INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

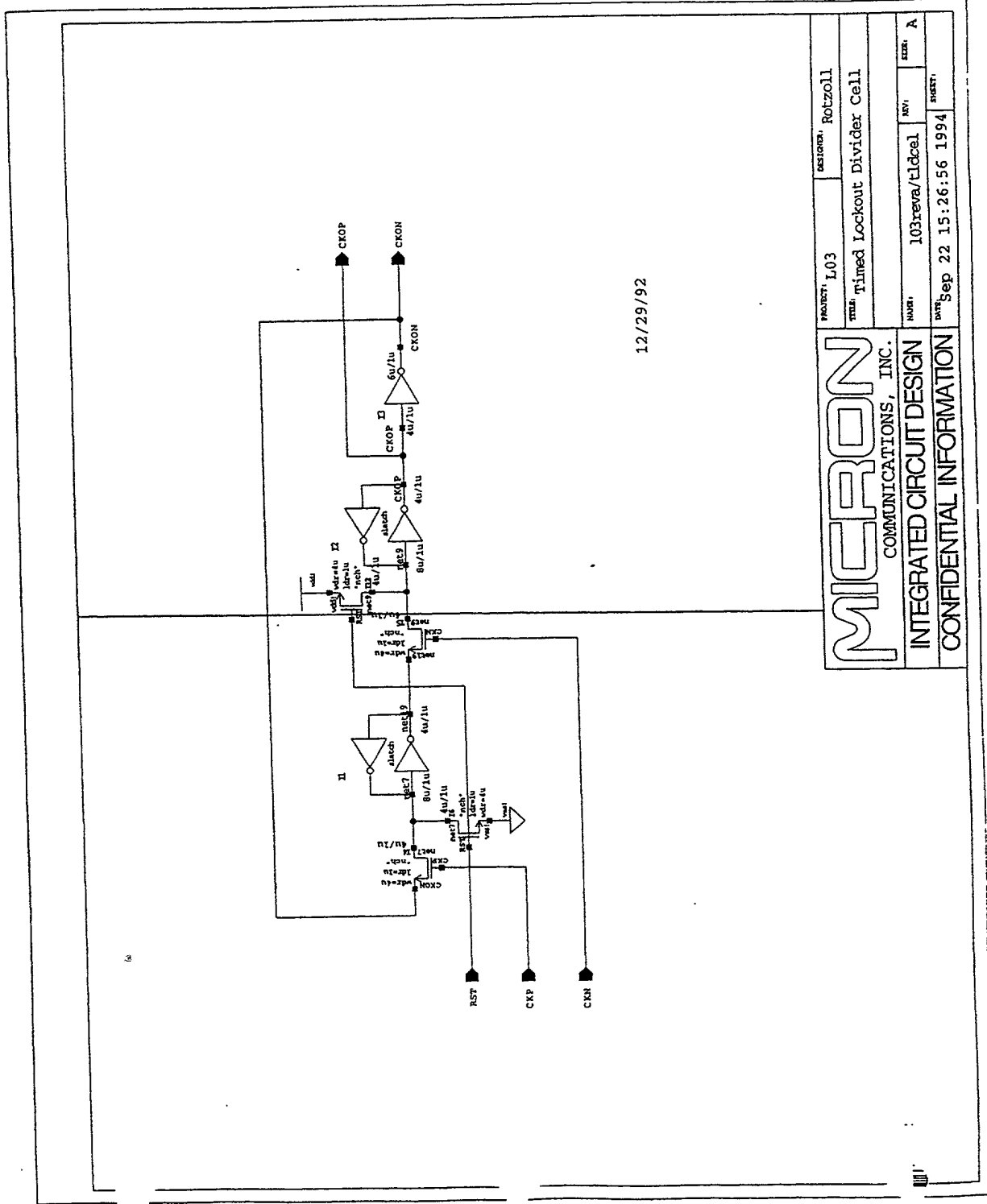
PROJECT	L03	DESIGN	J0700LE
TITLE	Wakeup Abort Logic Counter Bit		
NAME	I03reva/wlabort_cbit	REV	B1
DATE	Sep 26 08:40:51 1994	SIZE	A

FIG. 8.040101

ALL INFORMATION CONTAINED HEREIN IS UNCLASSIFIED

8.0402AA	8.0402AB
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EX-98 88.04.02



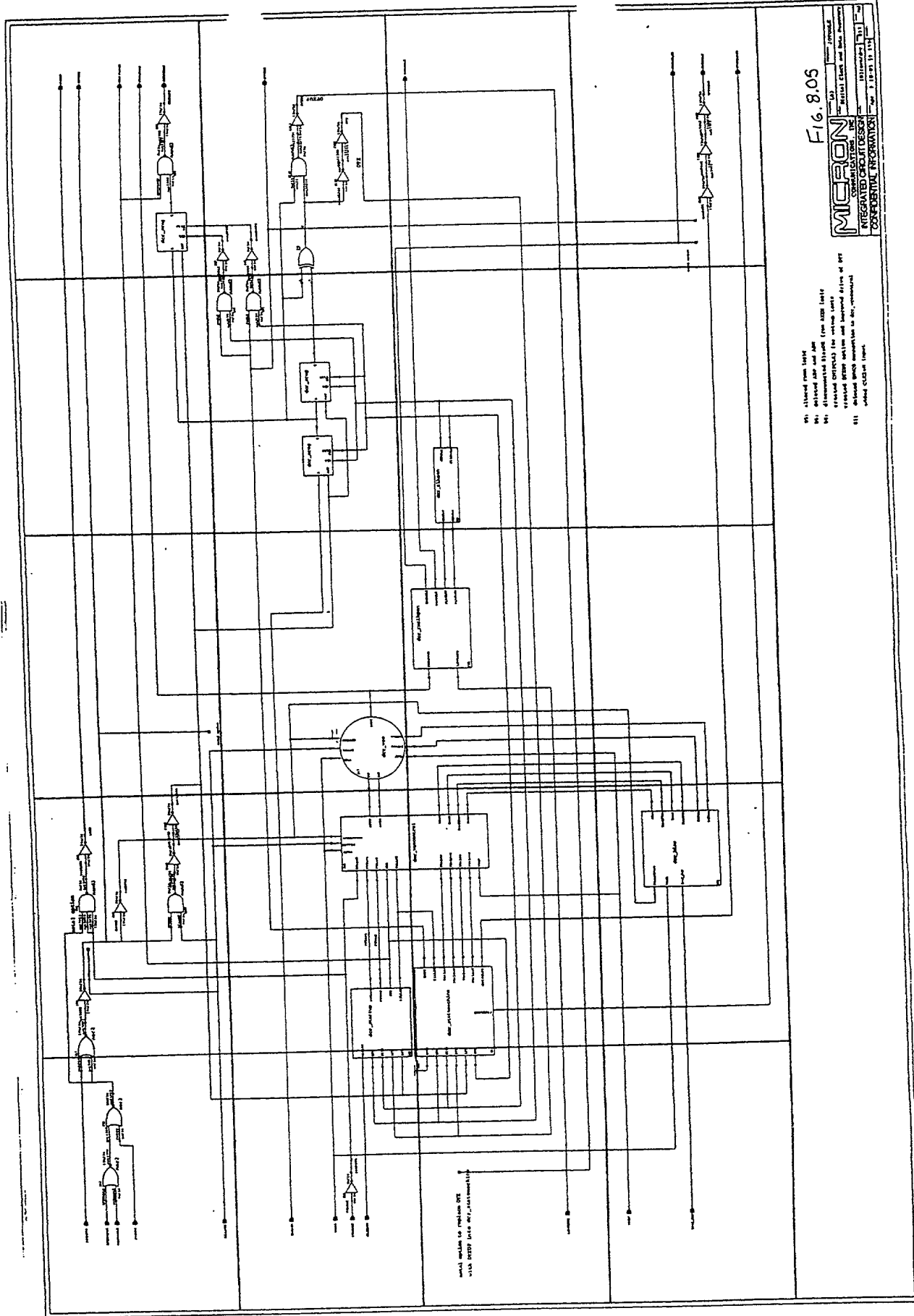
PROJECT: L03		DESIGNER: Rotzoll	
TITLE: Timed Lockout Divider Cell			
NAME:	103revA/1dcel	REV:	A
DATE:	Sep 22 15:26:56 1994	SHEET:	

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

FIG. 8.0402

8.11.5

SECRET



MI40-030

8.0501AA	8.0501AB	8.0501AC	8.0501AD	8.0501AE
8.0501BA	8.0501BB	8.0501BC	8.0501BD	8.0501BE

MI40-030

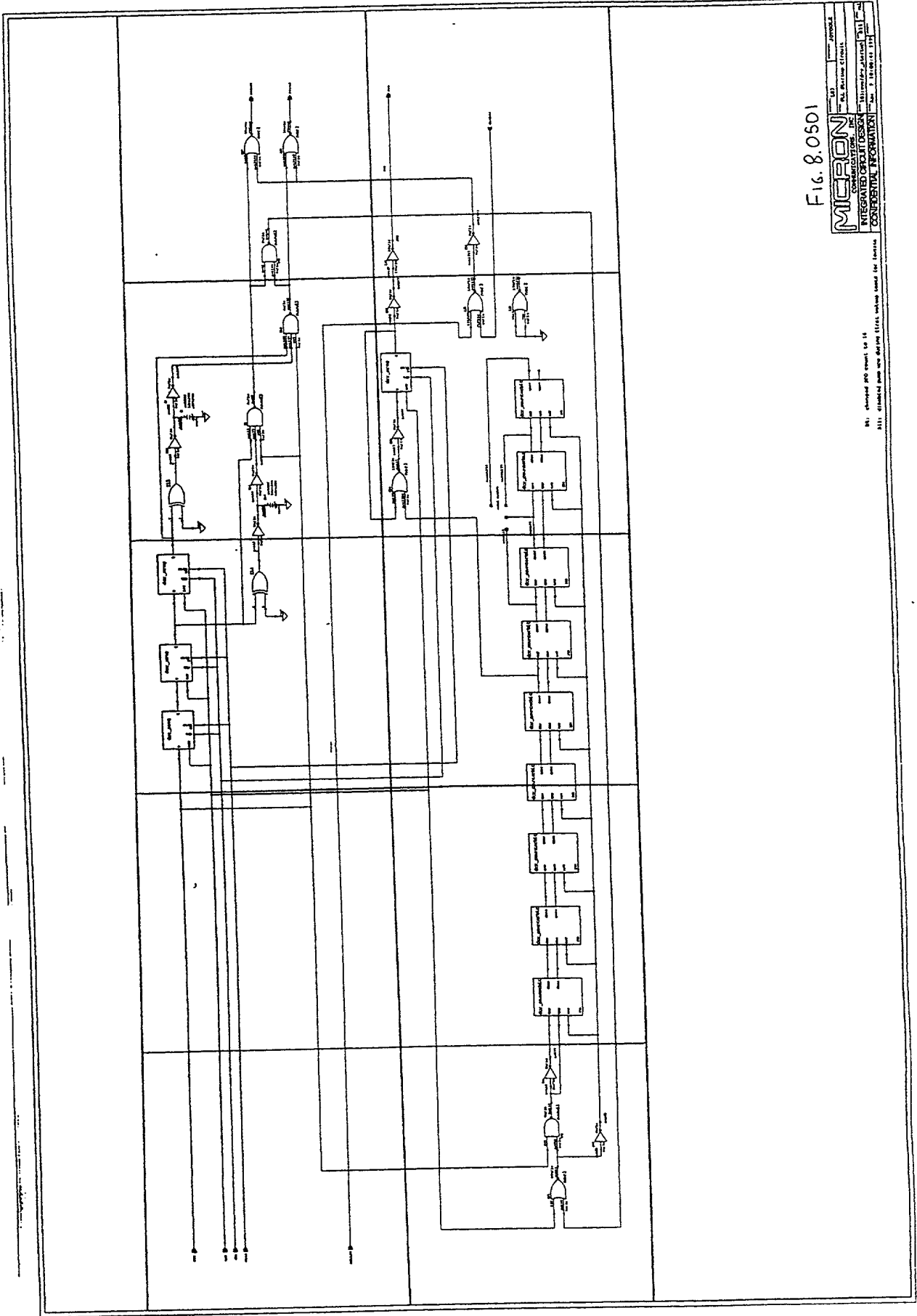


FIG. 8.0501

**MICRON**  
 INTEGRATED CIRCUIT DESIGN  
 CONFIDENTIAL INFORMATION  
 Part 2, 10/11/77

84: changed J20 input to 14  
 811: changed J20 and J21 input lines to 14



8.050101AB

8.050101AA

EX 07 BB.0501.01.1



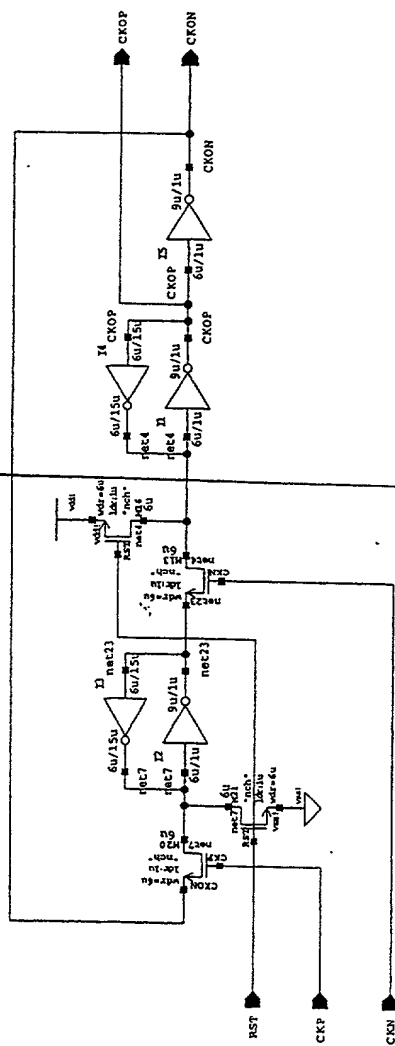
ALL INFORMATION CONTAINED HEREIN IS UNCLASSIFIED

a

8.050102AA

8.050102AB

EX-101 88-0501102



<b>MICRON</b>		PROJECT: L03	DESIGNER: JOTOOLE
		TITLE: Counter Bit	
		WWS:revs/dcr_counterbit	REV: B1
		DATE: Sep 26 09:26:35 1994	
		SHEET: 1	

Fig. 8.050102

8.0502AA	8.0502AB	8.0502AC	8.0502AD
8.0502BA	8.0502BB	8.0502BC	8.0502BD
8.0502CA	8.0502CB	8.0502CC	8.0502CD

SECRET

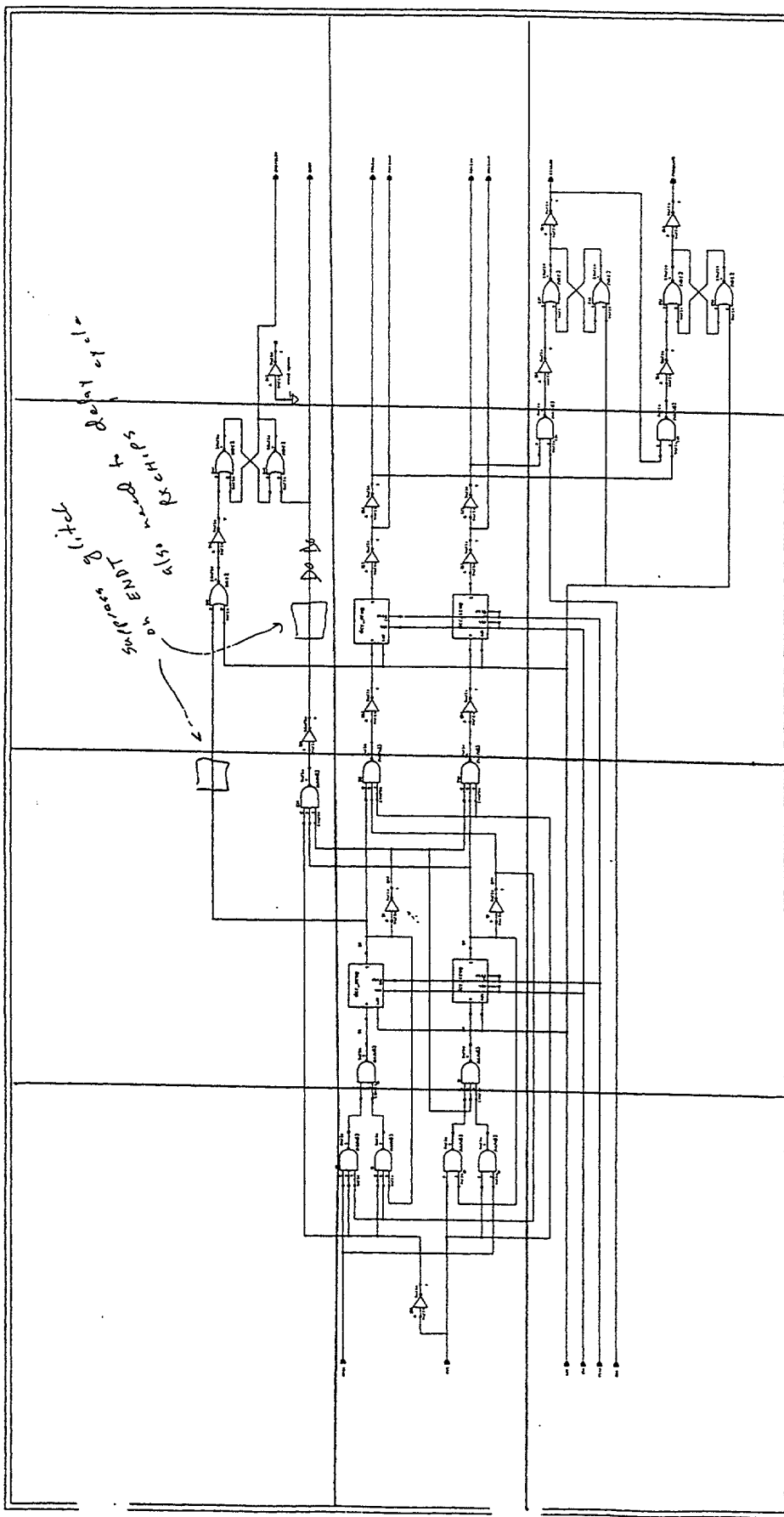


Fig 8.0502

8.0503AA	8.0503AB	8.0503AC	8.0503AD	8.0503AE	8.0503AF	8.0503AG	8.0503AH	8.0503AI	8.0503AJ	8.0503AK	8.0503AL	8.0503AM	8.0503AN	8.0503AO
8.0503BA	8.0503BB	8.0503BC	8.0503BD	8.0503BE	8.0503BF	8.0503BG	8.0503BH	8.0503BI	8.0503BJ	8.0503BK	8.0503BL	8.0503BM	8.0503BN	8.0503BO
8.0503CA	8.0503CB	8.0503CC	8.0503CD	8.0503CE	8.0503CF	8.0503CG	8.0503CH	8.0503CI	8.0503CJ	8.0503CK	8.0503CL	8.0503CM	8.0503CN	8.0503CO
8.0503DA	8.0503DB	8.0503DC	8.0503DD	8.0503DE	8.0503DF	8.0503DG	8.0503DH	8.0503DI	8.0503DJ	8.0503DK	8.0503DL	8.0503DM	8.0503DN	8.0503DO
8.0503EA	8.0503EB	8.0503EC	8.0503ED	8.0503EE	8.0503EF	8.0503EG	8.0503EH	8.0503EI	8.0503EJ	8.0503EK	8.0503EL	8.0503EM	8.0503EN	
8.0503FA	8.0503FB	8.0503FC	8.0503FD	8.0503FE	8.0503FF	8.0503FG	8.0503FH	8.0503FI	8.0503FJ	8.0503FK	8.0503FL	8.0503FM	8.0503FN	

DEPARTMENT OF DEFENSE

The diagram is a complex circuit schematic for a Micron integrated circuit. It features a grid-based layout with numerous logic blocks, interconnects, and functional units. Key components include:

- Logic Blocks:** Various rectangular blocks containing internal circuitry, some labeled with names like "Register", "Multiplexer", and "Demultiplexer".
- Interconnects:** A dense network of lines connecting the various blocks across the grid.
- Functional Units:** Specific blocks labeled "Register", "Multiplexer", and "Demultiplexer" are distributed throughout the circuit.
- Labels and Annotations:** The diagram includes several labels such as "Micron", "Integrated Circuit Design", and "CONFIDENTIAL INFORMATION". It also features a header with the text "Micron Technology" and "Integrated Circuit Design".
- Grid System:** The circuit is organized into a grid of squares, with each square representing a specific functional area or component.

The overall structure suggests a highly integrated and complex digital circuit, likely designed for high-performance computing or data processing applications.

<b>MICRON</b>	DATE	_____	BY	_____
<b>COMMUNICATIONS, INC.</b>	<b>FOR BILL NUMBER</b>			
<b>INTEGRATED CIRCUIT DESIGN</b>	NO.	10100000000000000000	Q1	(M)
<b>CONFIDENTIAL INFORMATION</b>	"ALL DATA IS IN THE PUBLIC DOMAIN"			

[illegible]

**Reduction of Effort**

F16.8.0503



8.0504AA	8.0504AB	8.0504AC	8.0504AD
8.0504BA	8.0504BB	8.0504BC	8.0504BD
8.0506CA	8.0504CB	8.0504CC	8.0504CD
8.0504DA	8.0504DB	8.0504DC	8.0504DD
8.0504EA	8.0504EB	8.0504EC	8.0504ED
			8.0504EE

Fig. 8.0504

MICRON  
CONTRACTORS  
MICRON CONTRACTORS  
MICRON CONTRACTORS

1. 112 pin package will be used in the  
2. 112 pin package will be used  
3. 112 pin package will be used  
4. 112 pin package will be used  
5. 112 pin package will be used

8.050401A	8.050401B	8.050401C	8.050401D	8.050401E	8.050401F	8.050401G	8.050401H	8.050401I	8.050401J		
8.050401A	8.050401B	8.050401C	8.050401D	8.050401E	8.050401F	8.050401G	8.050401H	8.050401I	8.050401J	8.050401K	8.050401L
8.050401A	8.050401B	8.050401C	8.050401D	8.050401E	8.050401F	8.050401G	8.050401H	8.050401I	8.050401J	8.050401K	8.050401L

8.050401A 8.050401B 8.050401C 8.050401D 8.050401E 8.050401F 8.050401G 8.050401H 8.050401I 8.050401J 8.050401K 8.050401L 8.050401M 8.050401N 8.050401O 8.050401P 8.050401Q 8.050401R 8.050401S 8.050401T 8.050401U 8.050401V 8.050401W 8.050401X 8.050401Y 8.050401Z

Variable	Control group		Experimental group		p-value
	Mean	SD	Mean	SD	
Age	30.5	4.2	30.8	4.5	0.85
Gender	15 M, 15 F		15 M, 15 F		0.92
Education	12.5	1.2	12.6	1.1	0.78
Occupation	10.5	1.5	10.6	1.4	0.91
Marital status	10.5	1.5	10.6	1.4	0.91
Family size	3.5	1.2	3.6	1.1	0.88
Religion	10.5	1.5	10.6	1.4	0.91
Income	10.5	1.5	10.6	1.4	0.91
Health status	10.5	1.5	10.6	1.4	0.91
Stress level	10.5	1.5	10.6	1.4	0.91
Life satisfaction	10.5	1.5	10.6	1.4	0.91
Self-esteem	10.5	1.5	10.6	1.4	0.91
Resilience	10.5	1.5	10.6	1.4	0.91
Optimism	10.5	1.5	10.6	1.4	0.91
Emotional stability	10.5	1.5	10.6	1.4	0.91
Neuroticism	10.5	1.5	10.6	1.4	0.91
Extraversion	10.5	1.5	10.6	1.4	0.91
Agreeableness	10.5	1.5	10.6	1.4	0.91
Conscientiousness	10.5	1.5	10.6	1.4	0.91
Openness	10.5	1.5	10.6	1.4	0.91
Intelligence	10.5	1.5	10.6	1.4	0.91
Memory	10.5	1.5	10.6	1.4	0.91
Attention	10.5	1.5	10.6	1.4	0.91
Perception	10.5	1.5	10.6	1.4	0.91
Reasoning	10.5	1.5	10.6	1.4	0.91
Problem solving	10.5	1.5	10.6	1.4	0.91
Decision making	10.5	1.5	10.6	1.4	0.91
Communication	10.5	1.5	10.6	1.4	0.91
Interpersonal skills	10.5	1.5	10.6	1.4	0.91
Leadership	10.5	1.5	10.6	1.4	0.91
Teamwork	10.5	1.5	10.6	1.4	0.91
Conflict resolution	10.5	1.5	10.6	1.4	0.91
Stress management	10.5	1.5	10.6	1.4	0.91
Emotional regulation	10.5	1.5	10.6	1.4	0.91
Self-regulation	10.5	1.5	10.6	1.4	0.91
Goal setting	10.5	1.5	10.6	1.4	0.91
Time management	10.5	1.5	10.6	1.4	0.91
Organization	10.5	1.5	10.6	1.4	0.91
Productivity	10.5	1.5	10.6	1.4	0.91
Efficiency	10.5	1.5	10.6	1.4	0.91
Quality of work	10.5	1.5	10.6	1.4	0.91
Job satisfaction	10.5	1.5	10.6	1.4	0.91
Work-life balance	10.5	1.5	10.6	1.4	0.91
Overall well-being	10.5	1.5	10.6	1.4	0.91

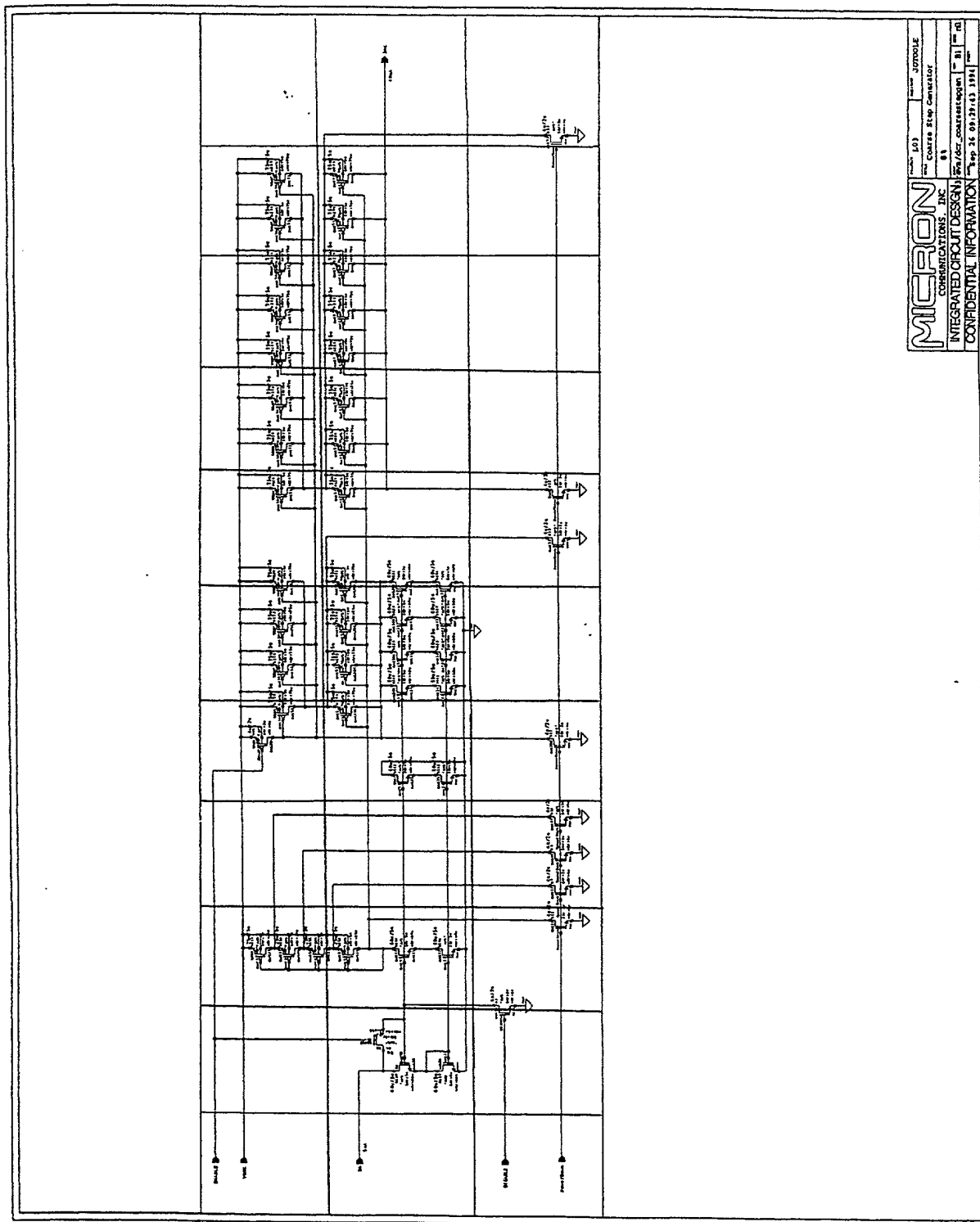


Fig 8.050401

8.050402AA	8.050402AB	8.050402AC	8.050402AD	8.050402AE	8.050402AF	8.050402AG	8.050402AH	8.050402AI	8.050402AJ
8.050402BA	8.050402BB	8.050402BC	8.050402BD	8.050402BE	8.050402BF	8.050402BG	8.050402BH	8.050402BI	8.050402BJ
8.050402CA	8.050402CB	8.050402CC	8.050402CD	8.050402CE	8.050402CF	8.050402CG	8.050402CH	8.050402CI	8.050402CJ

8.050402

SECRET

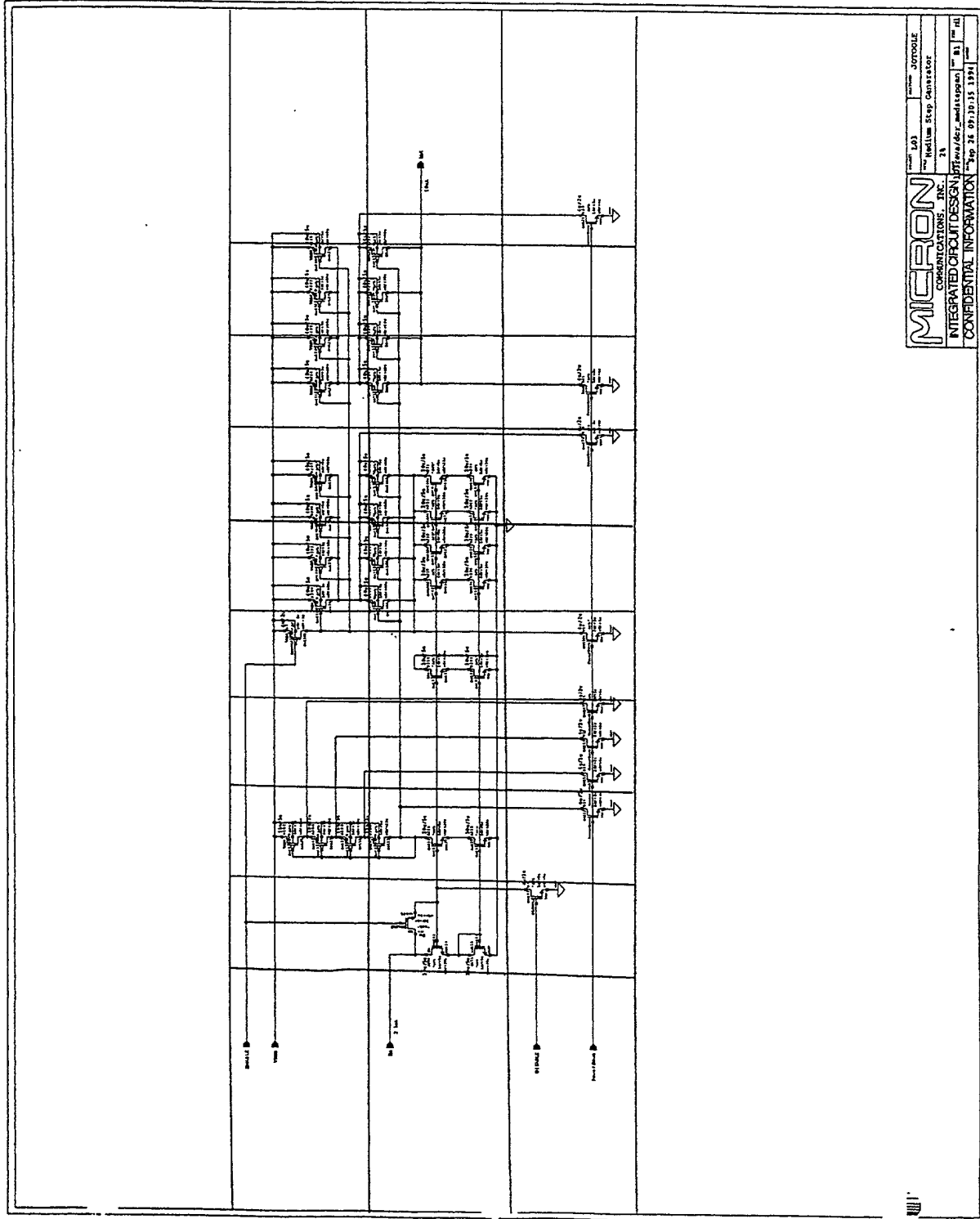
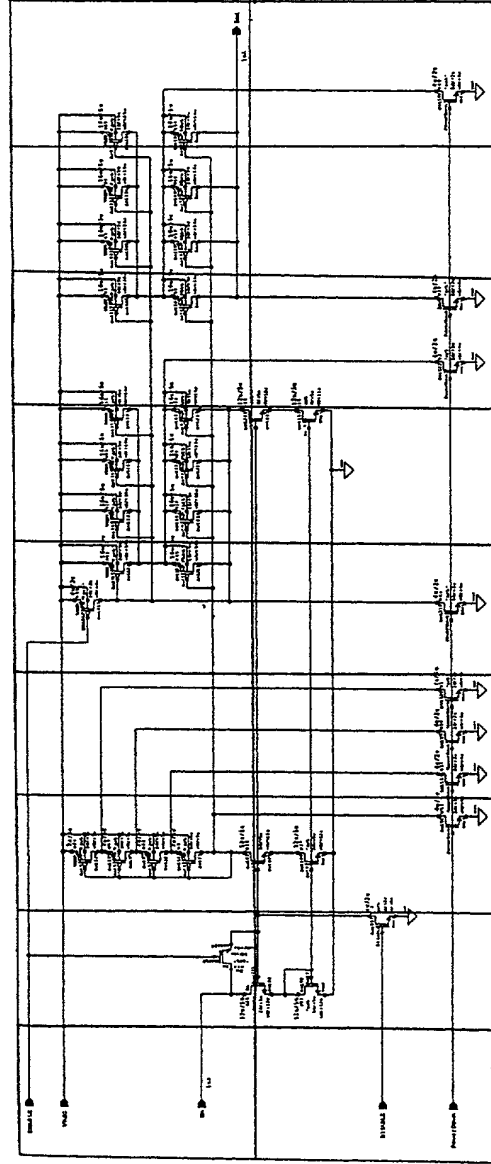


Fig. 8.050402

8.050403AA	8.050403AB	8.050403AC	8.050403AD	8.050403AE	8.050403AF	8.050403AG	8.050403AH	8.050403AI
8.050403BA	8.050403BB	8.050403BC	8.050403BD	8.050403BE	8.050403BF	8.050403BG	8.050403BH	8.050403BI

8.050403

SECRET



<b>MICRON</b>		LOT	JOTROCK
COMMUNICATIONS, INC.		Medium Fine Step Generator	
INTEGRATED CIRCUIT DESIGN		0.21	
CONFIDENTIAL INFORMATION		Rev 28 09/11/83	14

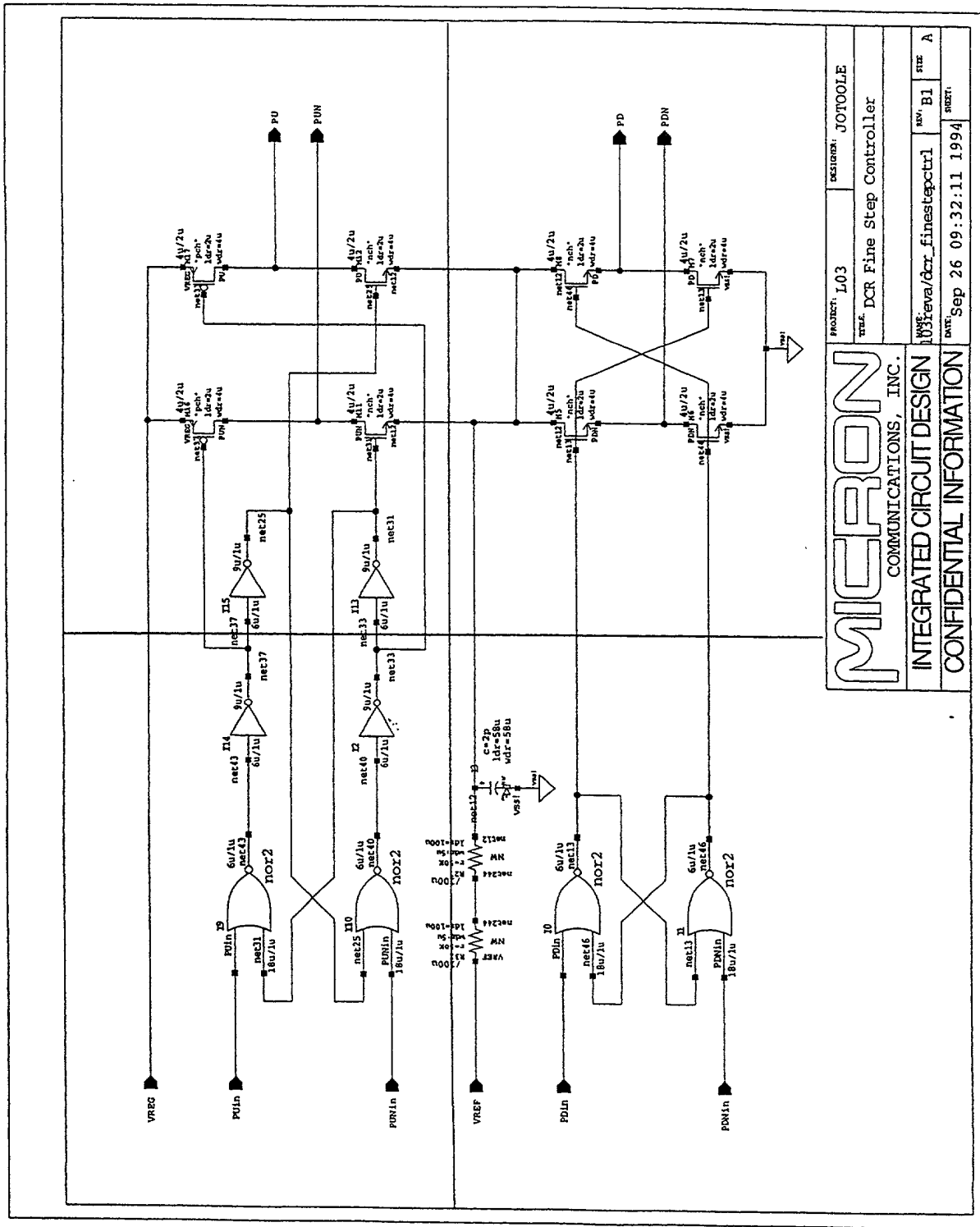
Fig. 8.050403



ALL INFORMATION CONTAINED HEREIN IS UNCLASSIFIED

8.050404AA	8.050404AB
8.050404BA	8.050404BB

EX-105 8.050404



MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: DCR Fine Step Controller	
INTEGRATED CIRCUIT DESIGN		REV: B1	SHEET: A
CONFIDENTIAL INFORMATION		DATE: Sep 26 09:32:11 1994	SHEET: 1

Fig. 8.050404

8.050405AA	8.050405AB	8.050405AC	8.050405AD	8.050405AE	8.050405AF	8.050405AG	8.050405AH	8.050405AI	8.050405AJ	8.050405AK	8.050405AL	8.050405AM
8.050405BA	8.050405BB	8.050405BC	8.050405BD	8.050405BE	8.050405BF	8.050405BG	8.050405BH	8.050405BI	8.050405BJ	8.050405BK	8.050405BL	8.050405BM
8.050405CA	8.050405CB	8.050405CC	8.050405CD	8.050405CE	8.050405CF	8.050405CG	8.050405CH	8.050405CI	8.050405CJ	8.050405CK	8.050405CL	8.050405CM
8.050405DA	8.050405DB	8.050405DC	8.050405DD	8.050405DE	8.050405DF	8.050405DG	8.050405DH	8.050405DI	8.050405DJ			
8.050405EA	8.050405EB	8.050405EC	8.050405ED	8.050405EE	8.050405EF	8.050405EG	8.050405EH	8.050405EI	8.050405EJ			

















Fig 8.050405



MI40-030 8.050540

8.05054A	8.05054B	8.05054C	8.05054D	8.05054E	8.05054F	8.05054G	8.05054H	8.05054I	8.05054J	8.05054K	8.05054L	8.05054M	8.05054N
8.0505BA	8.0505BB	8.0505BC	8.0505BD	8.0505BE	8.0505BF	8.0505BG	8.0505BH	8.0505BI	8.0505BJ	8.0505BK	8.0505BL	8.0505BM	8.0505BN
8.0505CA	8.0505CB	8.0505CC	8.0505CD	8.0505CE	8.0505CF	8.0505CG	8.0505CH	8.0505CI	8.0505CJ	8.0505CK	8.0505CL	8.0505CM	8.0505CN
8.0505DA	8.0505DB	8.0505DC	8.0505DD	8.0505DE	8.0505DF	8.0505DG	8.0505DH	8.0505DI	8.0505DJ	8.0505DK	8.0505DL	8.0505DM	8.0505DN
8.0505EA	8.0505EB	8.0505EC	8.0505ED	8.0505EE	8.0505EF								

MI40-030 8.050540

FIG. 8.0505

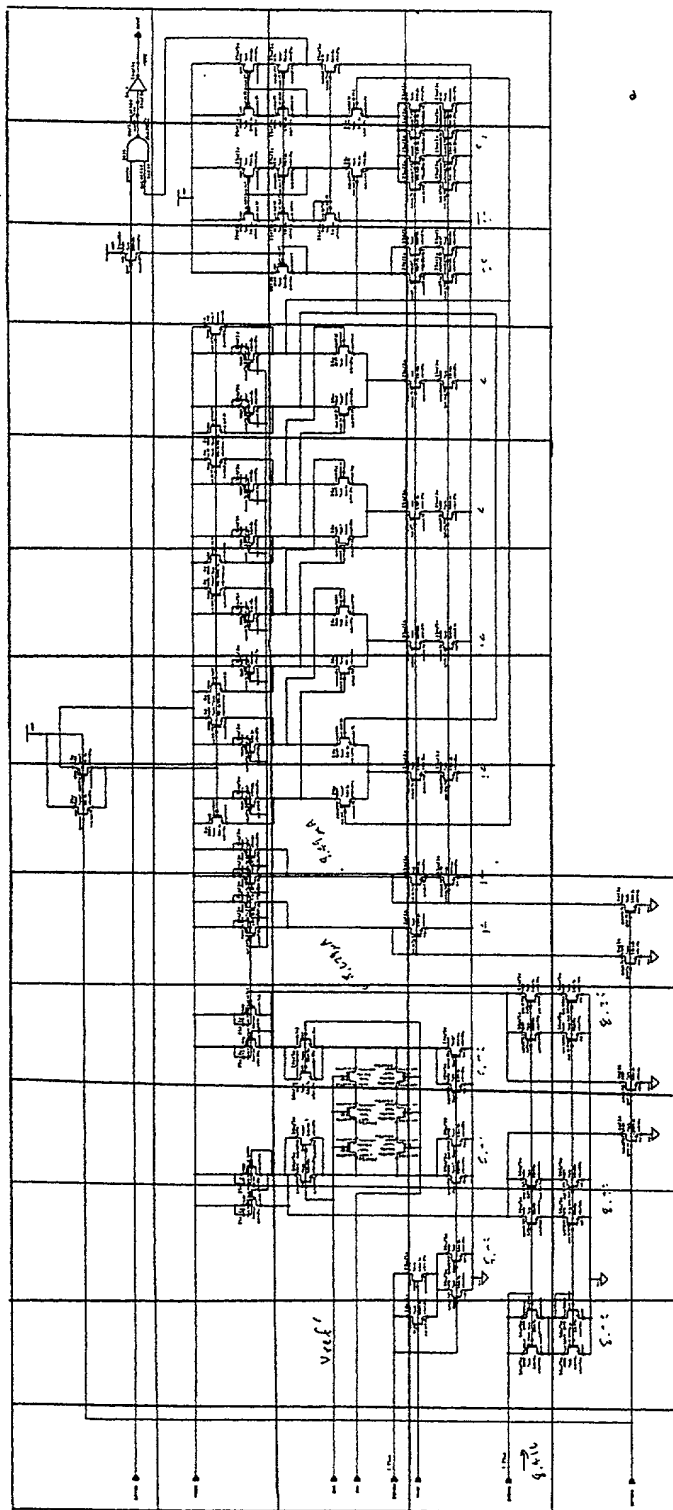


FIG. 8.0505

275,6

ALL INFORMATION CONTAINED HEREIN IS UNCLASSIFIED

8.0506AA	8.0506AB
8.0506BA	8.0506BB

U.S. 88.0506

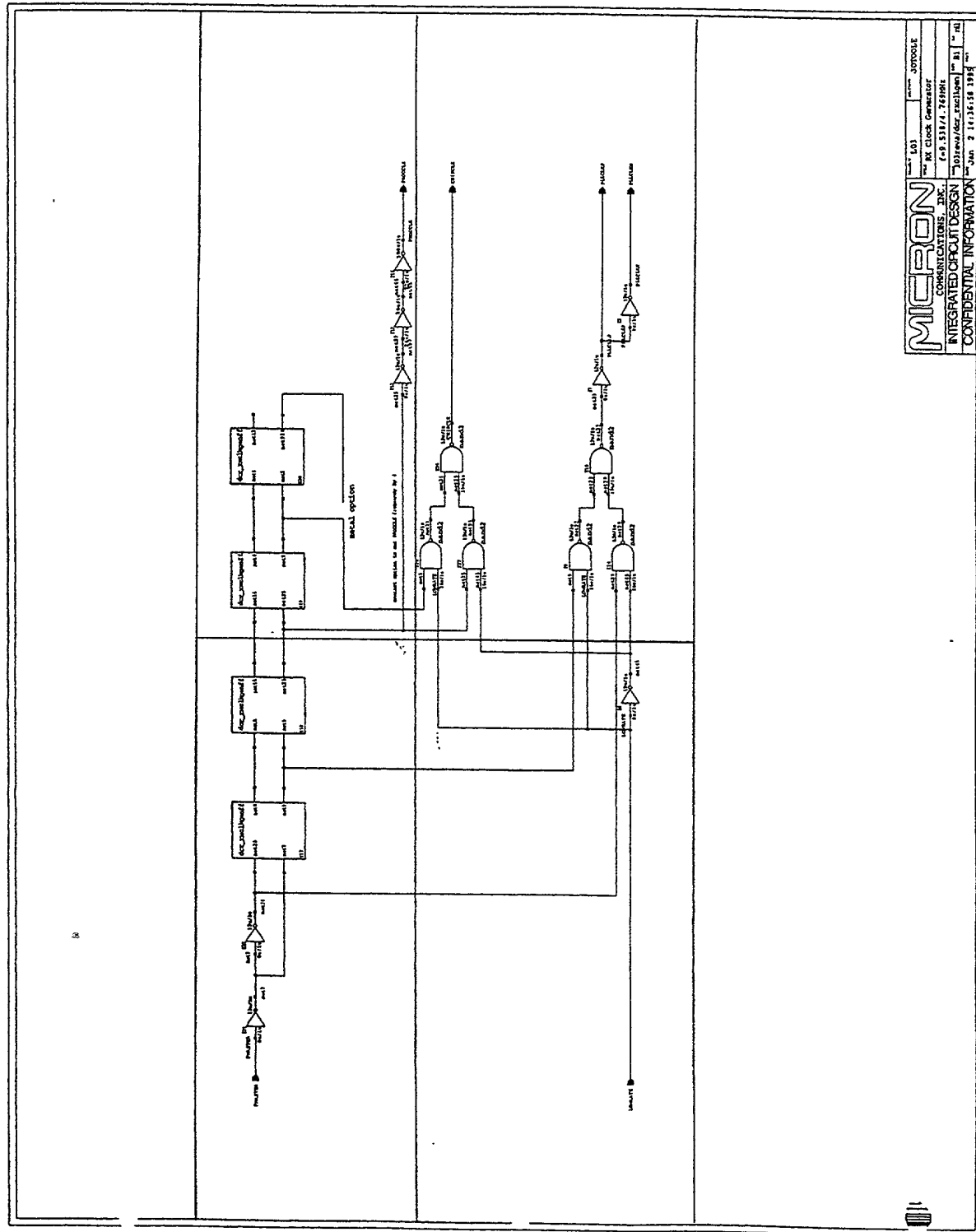


FIG. 8.0506

MICRON		80506	80506
INTEGRATED CIRCUIT DESIGN		80506	80506
CONFIDENTIAL INFORMATION		80506	80506



415

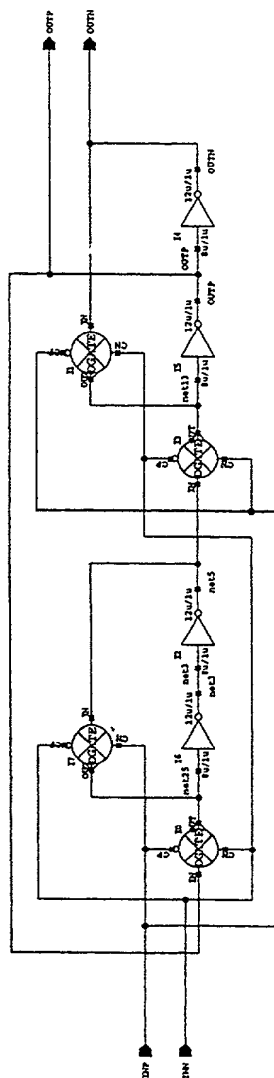


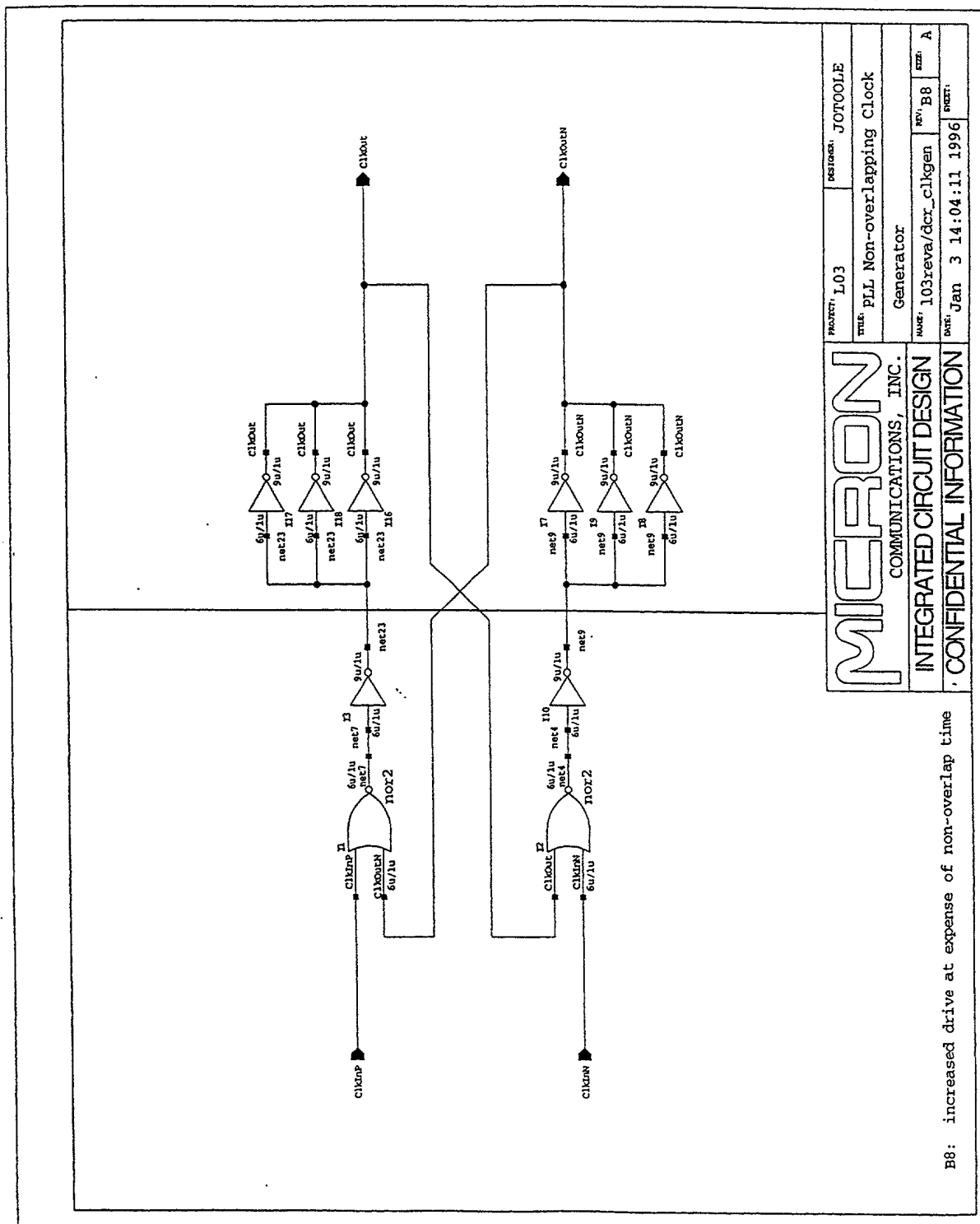
FIG. 8.050601

PROJECT: L03		DESIGNER: JOTOOLE	
TITLE: Rx Clock Generator			
PART: Flip-Flop			
Y03:rxpwr/dcr_xrclkgenoff	REV: B1	FILE: rli	
"Sep 26 09:36:05 1994"		SHEET: 1	

ALL INFORMATION CONTAINED  
HEREIN IS UNCLASSIFIED

8.0507AA	8.0507AB
----------	----------

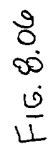
EX-88-0507



P.O. Box 1000  
Eugene, OR 97402

8.06AA	8.06AB	8.06AC	8.06AD
8.06BA	8.06BB	8.06BC	8.06BD
8.06CA	8.06CB	8.06CC	8.06CD
8.06DA	8.06DB	8.06DC	8.06DD
8.06EA	8.06EB	8.06EC	8.06ED

EE.0000



18; added test mode to backscatter  
modified current sources  
redesigned backscatter transmitter

PARAS(Backscatter)=1.4672.46mV

乙卯年

**COMMUNICATIONS, INC.**  
**INTEGRATED CIRCUIT DESIGN**

**CONFIDENTIAL INFORMATION**

8.0601AA	8.0601AB
8.0601BA	8.0601BB

И. П. Ог В. П. Ог



Итого





DATE	1.03	NEW YORK	1000000000
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203	TV Phase/Erection Detector
-----	----------------------------

**ix Phase/frequency detector**

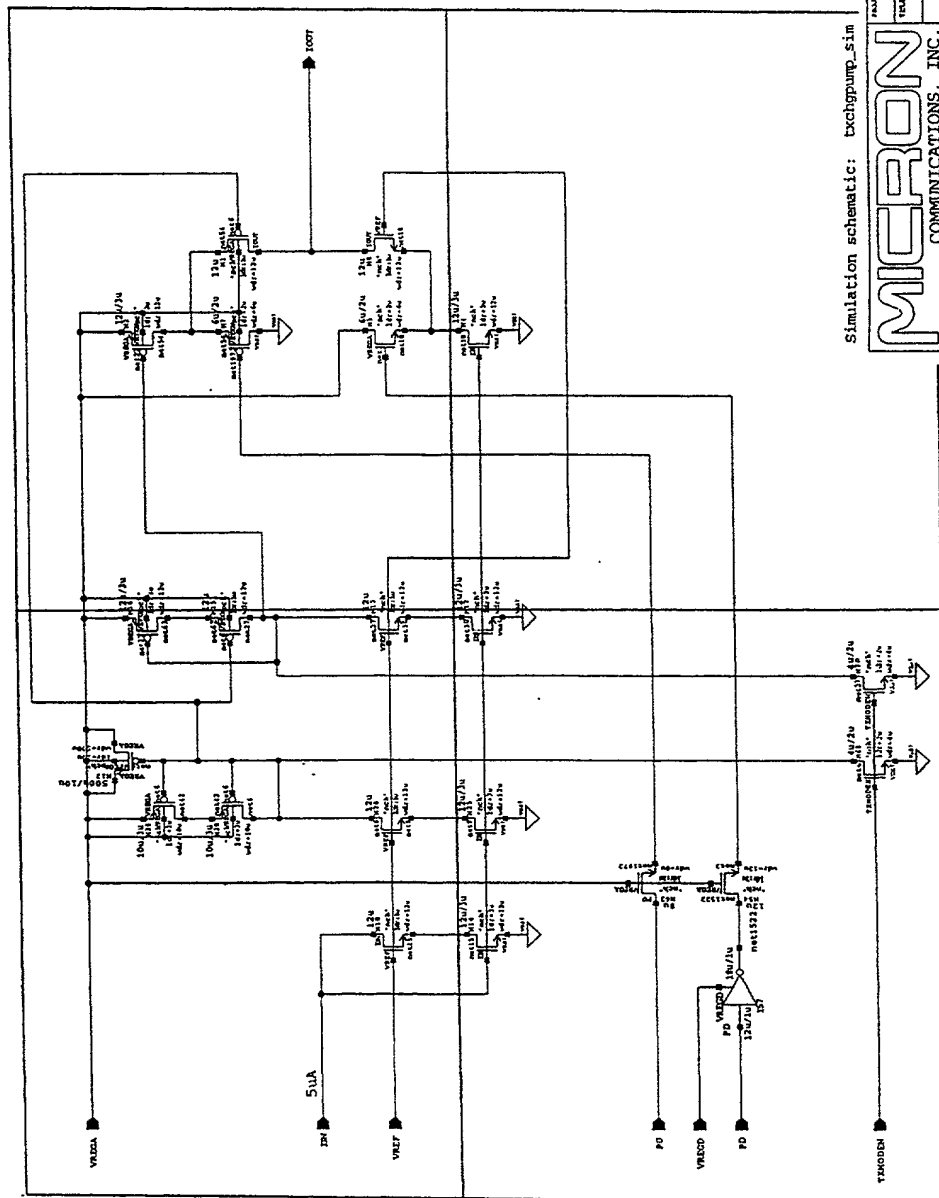
DATE:	10/1/2010	REV:	1.0
-------	-----------	------	-----

DATE	02 12 02	NO	0000	DEPT	0000
T03reva/Expidet			B1		

Mar 29 16:00:53 1995

8.060102AA	8.060102AB
8.060102BA	8.060102BB

FIG. 8.060102



Simulation schematic: txchgump\_sim

MICROCOM COMMUNICATIONS, INC.

REF ID: A66087	DESIGN: JOTOOLE
----------------	-----------------

TX PLL Charge Pump

INTEGRATED CIRCUIT DESIGN	NAME	103reva/txchgppump	REV	B1	Q13	ml
CONFIDENTIAL INFORMATION	DATE	Feb 28 09:55:50 1995				

DATE	20 00 55 50 1005	TIME
------	------------------	------

60 11 88 88.07.07

Total cap available = 55pF

12 channels of circuitry, each consisting of a 10k resistor, a 100pF capacitor, and a 100k resistor. The channels are labeled with their respective input and output signals.

Channels are labeled with their respective input and output signals:

- Channel 1: 10k, 100pF, 100k
- Channel 2: 10k, 100pF, 100k
- Channel 3: 10k, 100pF, 100k
- Channel 4: 10k, 100pF, 100k
- Channel 5: 10k, 100pF, 100k
- Channel 6: 10k, 100pF, 100k
- Channel 7: 10k, 100pF, 100k
- Channel 8: 10k, 100pF, 100k
- Channel 9: 10k, 100pF, 100k
- Channel 10: 10k, 100pF, 100k
- Channel 11: 10k, 100pF, 100k
- Channel 12: 10k, 100pF, 100k

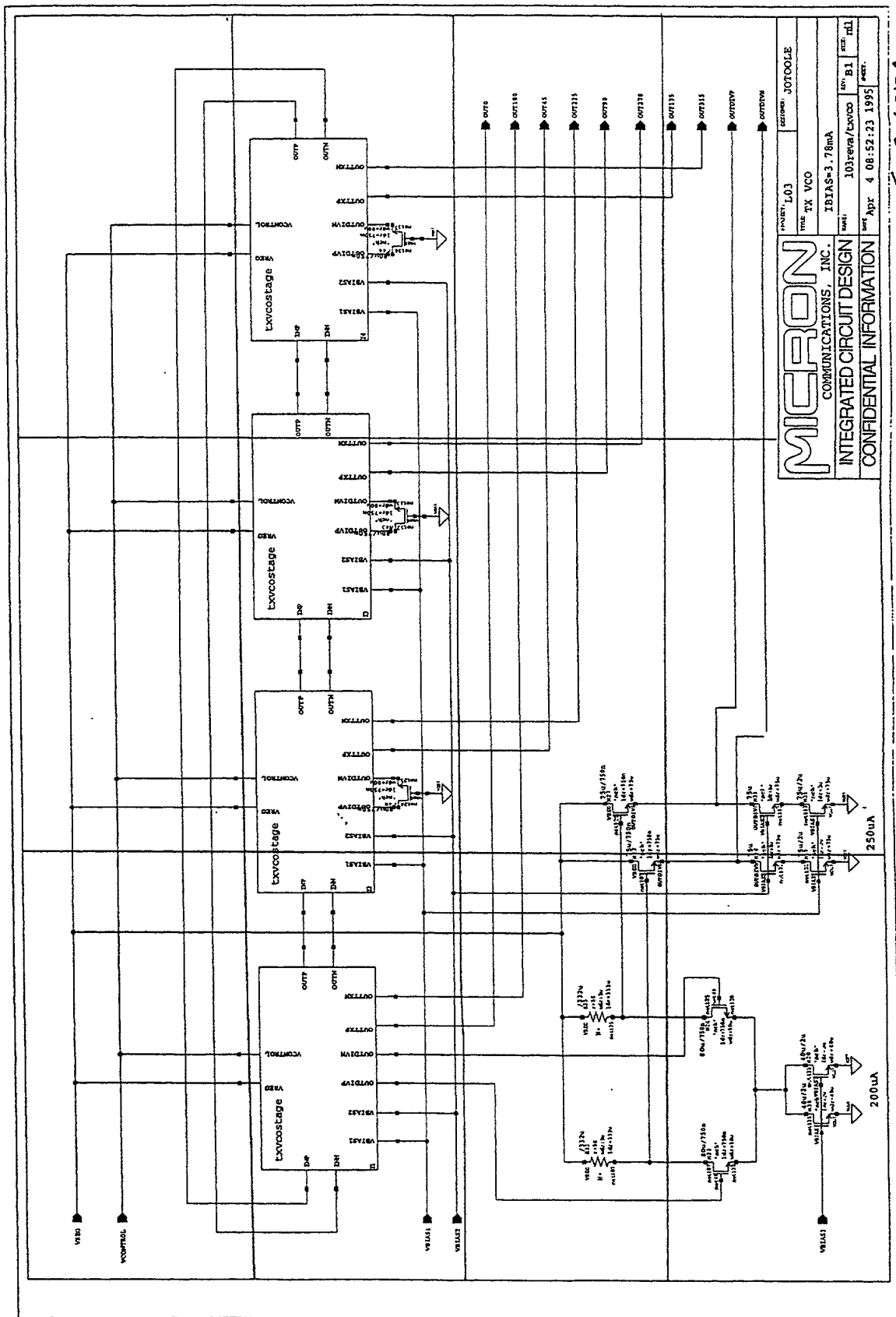
The diagram is a detailed schematic of a multi-channel analyzer system.

PROPERTY NO. 103		DATE
TX. TX. TX. Loop Filter		DATE
BW=700KHz PM=60deg		DATE
MAX. 103Vrms/4000Hz		DATE
NOV. 5 14:40:11 1996		DATE

B82: moved extra caps to biasok  
B88: moved 2 2K resistors to bmbms

11.000.000.000

FIG. 8.060104



MICRON		JOTOOLE	
COMMUNICATIONS, INC.		TX VCO	
INTEGRATED CIRCUIT DESIGN		IBIAS=3.78uA	
CONFIDENTIAL INFORMATION		103revA/LXVCO	
		B1	
		REV.	
		APR 4 08:52:23 1995	

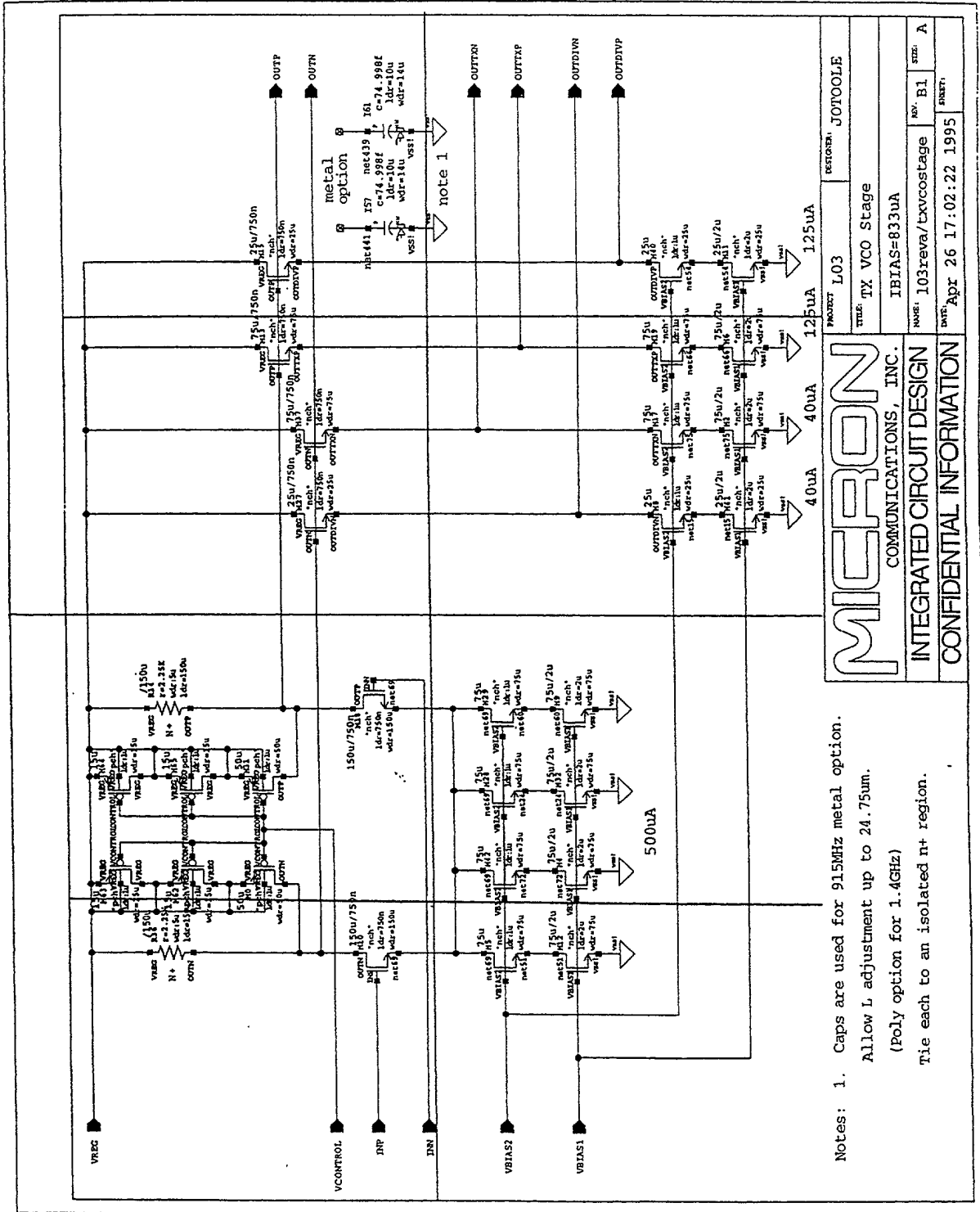
Fig. 8.060104

SECRET

8.06010401AA	8.06010401AB	8.06010401AC	8.06010401AD
8.06010401BA	8.06010401BB	8.06010401BC	8.06010401BD

SECRET





Notes: 1. Caps are used for 915MHz metal option.  
 Allow L adjustment up to 24.75um.  
 (Poly option for 1.4GHz)  
 Tie each to an isolated n+ region.

MICRON	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
PROJECT	DESIGNER
TX VCO Stage	JOTOOLE
IBIAS=833uA	
NAME	REV. B1
DATE	APR 26 17:02:22 1995
SHEET	A

Fig. 8.06010401

8.0601040101AA

8.0601040101AA	8.0601040101AB	8.0601040101AC
8.0601040101BA	8.0601040101BB	8.0601040101BC

8.0601040101BB

[illegible]

LE 11 11 BB. 0060105

FIG. 8.060105

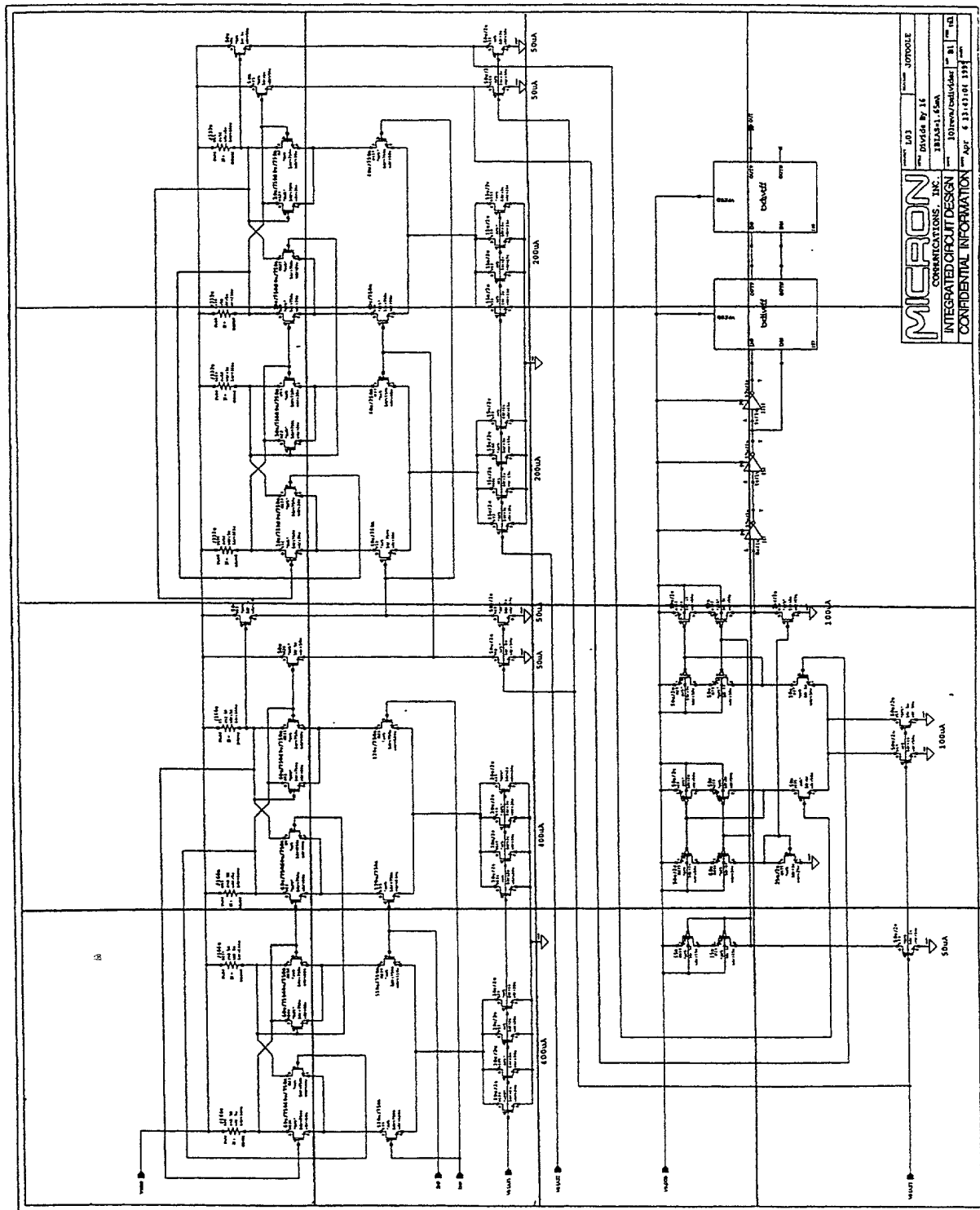


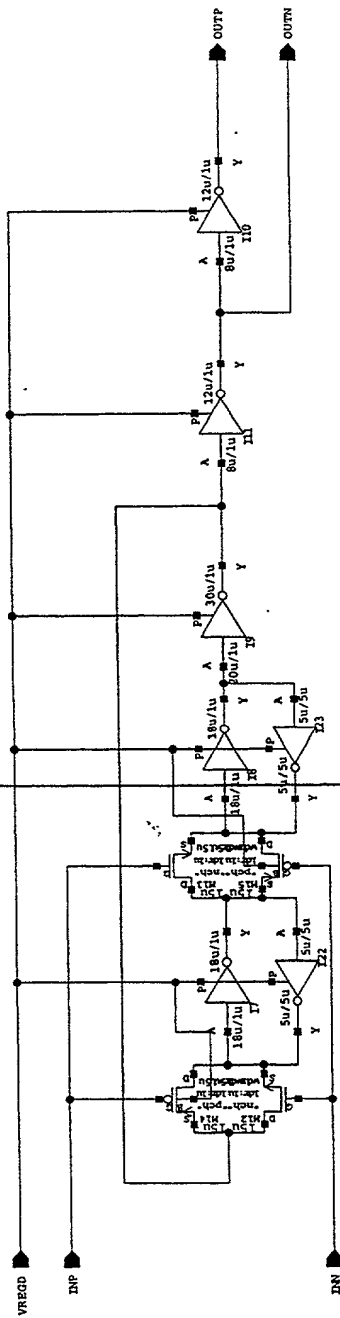
FIG. 8.060105

<b>MICRON</b>	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
Part No. 103	Rev. 10/10/00
Des. By: 1E	Drawn By: 1E
18/04/01, 15/04/01	10/10/00, 10/10/00
10/10/00	10/10/00
10/10/00	10/10/00

8.06010501AB

II II

CONFIDENTIAL



PROJECT: L03		JOTOOLE	
TITLE: Divider Flip-flop			
NAME:	103reva/bdivtff	REV:	B1
DATE:	Mar 30 09:47:12 1995	SHEET:	A

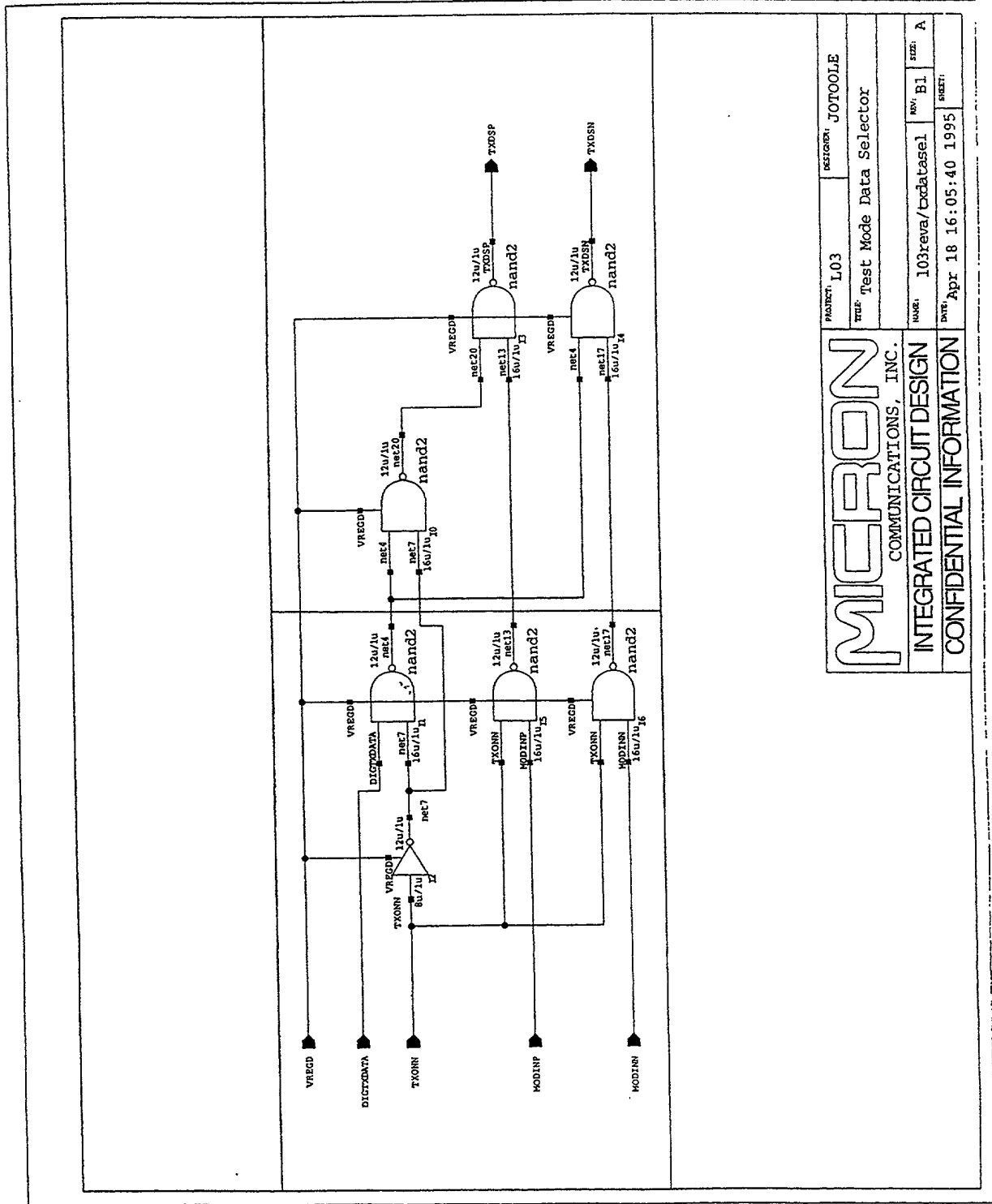
FIG. 8.06010501

8.0602AB

8.0602AA

11.11.11 88.006002





MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Test Mode Data Selector	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/bcdataset	REV: B1
CONFIDENTIAL INFORMATION		DATE: Apr 18 16:05:40 1995	SIZE: A
		SHEET:	

Fig. 8.0602

8.0603AB

8.0603AA

EX-87 88.016003

The schematic diagram illustrates a 500 uA current source circuit. The circuit is divided into two main sections. The left section contains logic gates: a 7400 (NAND), a 7401 (NAND), a 7402 (NAND), and a 7403 (NAND). The right section contains a 500 uA current source, a 500 uA current source, and a 500 uA current source. The circuit is powered by a 5V supply and a 500 uA current source. The output is a 500 uA current source.

Fig. 8.0603

<b>micron</b>		JANUARY '83		J0700LE	
COMMUNICATIONS, INC.		TITLE		PERSONNEL	
		BPSK Modulation Driver			
		IBTAS=500UA			
INTEGRATED CIRCUIT DESIGN		ADDR		MAILING	
CONFIDENTIAL INFORMATION		10drevr/tdbspk		JAN '88	
				18 JAN 18 10:28:46 1986	

**B8: modified current source**

8.0604AB

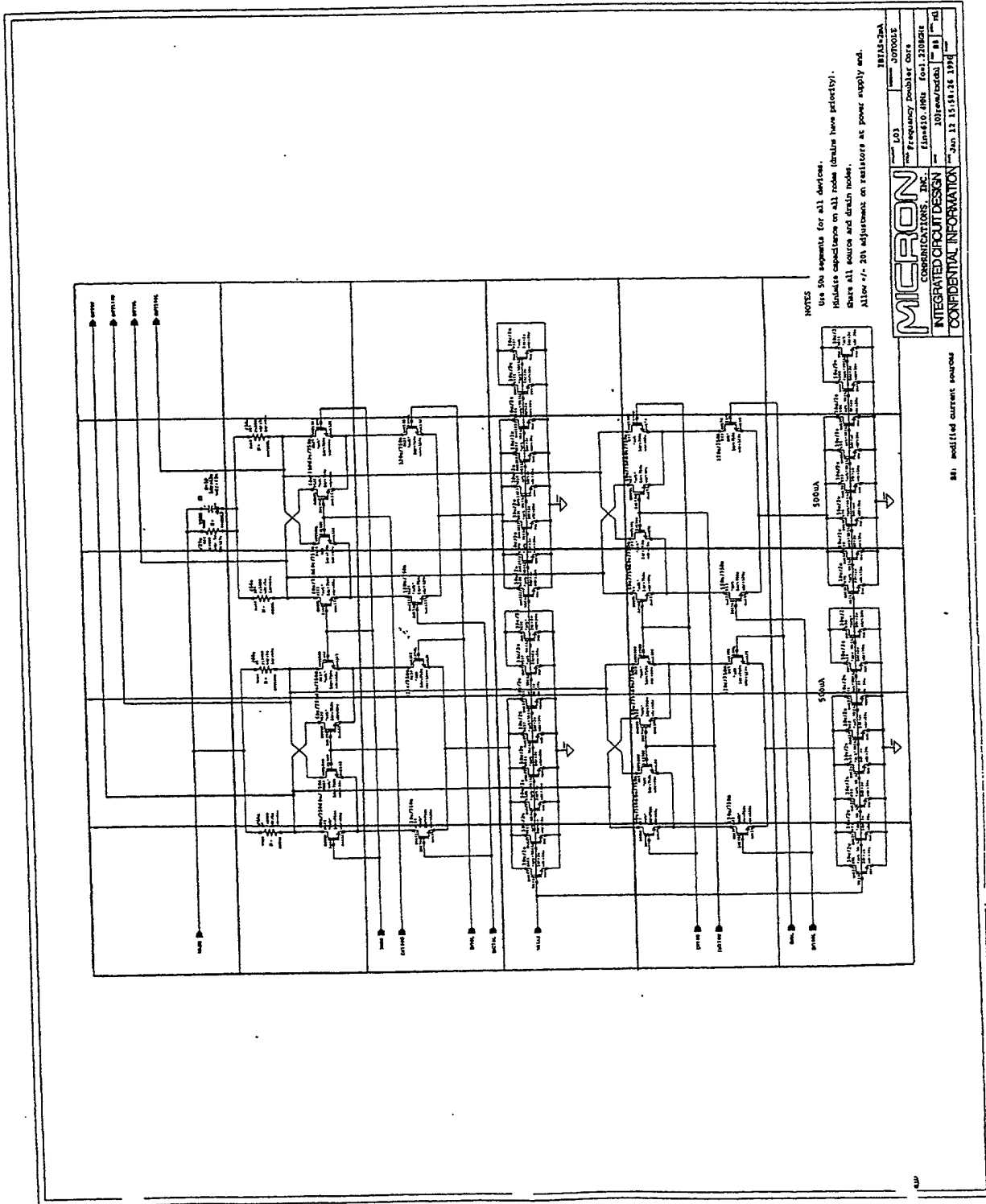
8.0604AA

EX-88.0604



8.060401AA	8.060401AB	8.060401AC	8.060401AD	8.060401AE
8.060401BA	8.060401BB	8.060401BC	8.060401BD	8.060401BE
8.060401CA	8.060401CB	8.060401CC	8.060401CD	8.060401CE
8.060401DA	8.060401DB	8.060401DC	8.060401DD	8.060401DE
8.060401EA	8.060401EB	8.060401EC	8.060401ED	8.060401EE
8.060401FA	8.060401FB	8.060401FC	8.060401FD	8.060401FE

II II 8.060401 II



NOTES  
 Use 500 segments for all devices.  
 Includes capacitance on all nodes (define how priority).  
 Share all source and drain nodes.  
 Allow +/- 20% adjustment on resistors at power supply end.

<b>MICRON</b>		103	103
COMMUNICATIONS, INC.		Frequency Doubler Data	103
INTEGRATED CIRCUIT DESIGN		103	103
CONFIDENTIAL INFORMATION		103	103
		103	103

FIG. 8.060901

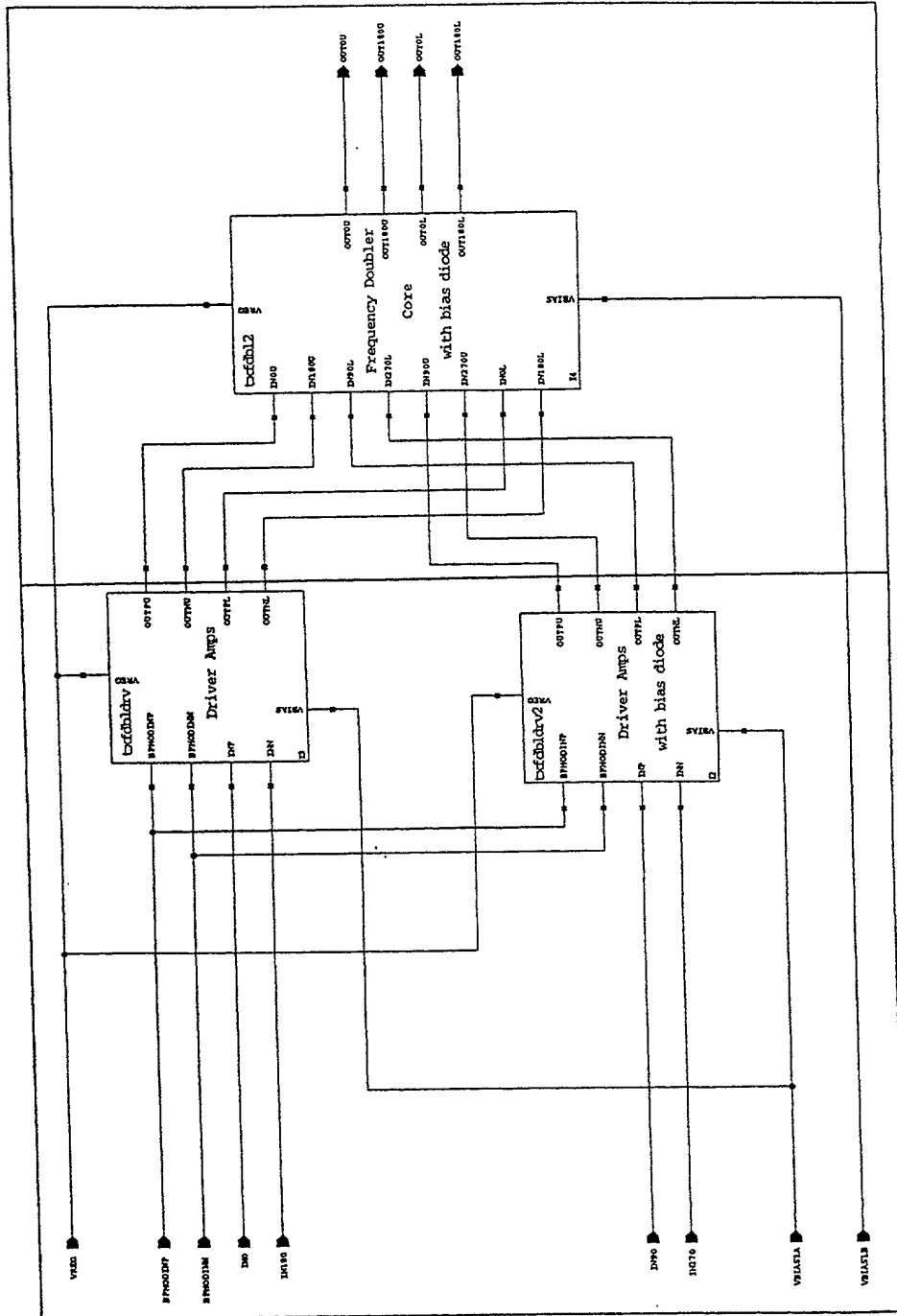
8.0605AB

8.0605AA

EX-88.0605



SECRET

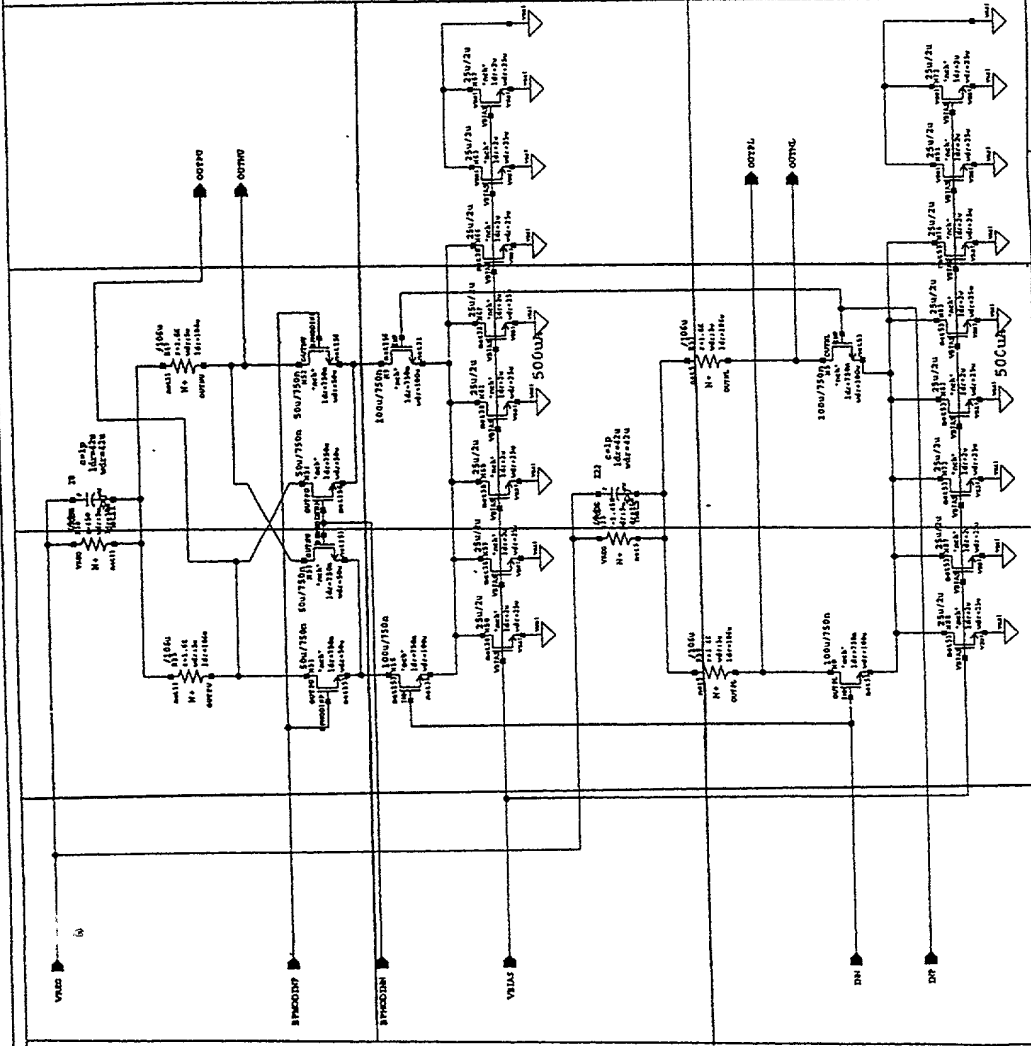


MICRON		PROPERTY L03	REVISION	J07000LE
COMMUNICATIONS, INC.		Frequency Doubler		
INTEGRATED CIRCUIT DESIGN		IBIAS=4mA		
CONFIDENTIAL INFORMATION		NAME	103revA/bdoubler2	REV. B8
		DATE	Jan 12 17:22:51 1996	FILE

B8: current sources modified

8.060501AA	8.060501AB	8.060501AC	8.060501AD
8.060501BA	8.060501BB	8.060501BC	8.060501BD
8.060501CA	8.060501CB	8.060501CC	8.060501CD

II 8.060501



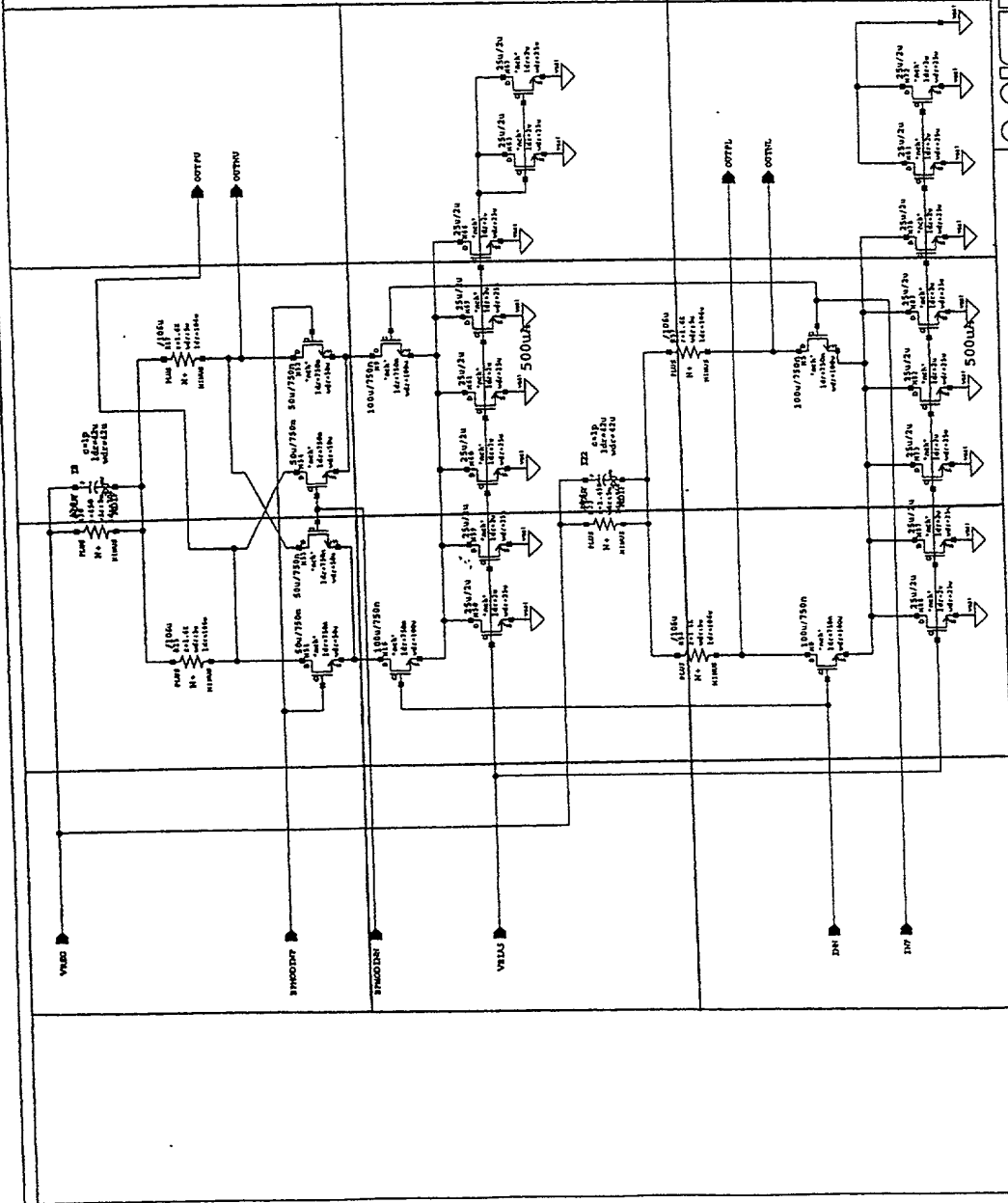
NOTES  
 Minimize capacitance on output nodes.  
 Share all source/drain nodes.  
 Allow +/- 20% adjustment on resistors  
 at supply end.

Fig. 8.060501

MICRON		PROJECT: L03	REVISION: J0700LE
COMMUNICATIONS, INC.		The Doubler Driver Amps	
INTEGRATED CIRCUIT DESIGN		IBIAS=1mA	
CONFIDENTIAL INFORMATION		REV. B8	REV. B8
		DATE: Jan 12 15:37:26 1996	DATE:

B8: modified current sources

И.И.Г 8.060502



NOTES  
Minimize capacitance on output nodes.  
Share all source/drain nodes.  
Allow +/- 20% adjustment on resistors  
at supply end.

FIG. 8.06 0502

PROJECT: L03		REVISION: J0700LE	
NAME: Doubler Driver Amps		IBIAS=1mA	
PART: 103-cen/cx6bldrv2		REV: B8	REV: ml
DATE: Jan 18 08:22:12 1996		REV: 1	

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

B8: modified current sources

8.060503AA	8.060503AB	8.060503AC	8.060503AD	8.060503AE
8.060503BA	8.060503BB	8.060503BC	8.060503BD	8.060503BE
8.060503CA	8.060503CB	8.060503CC	8.060503CD	8.060503CE
8.060503DA	8.060503DB	8.060503DC	8.060503DD	8.060503DE
8.060503EA	8.060503EB	8.060503EC	8.060503ED	8.060503EE
8.060503FA	8.060503FB	8.060503FC	8.060503FD	8.060503FE

8.060503



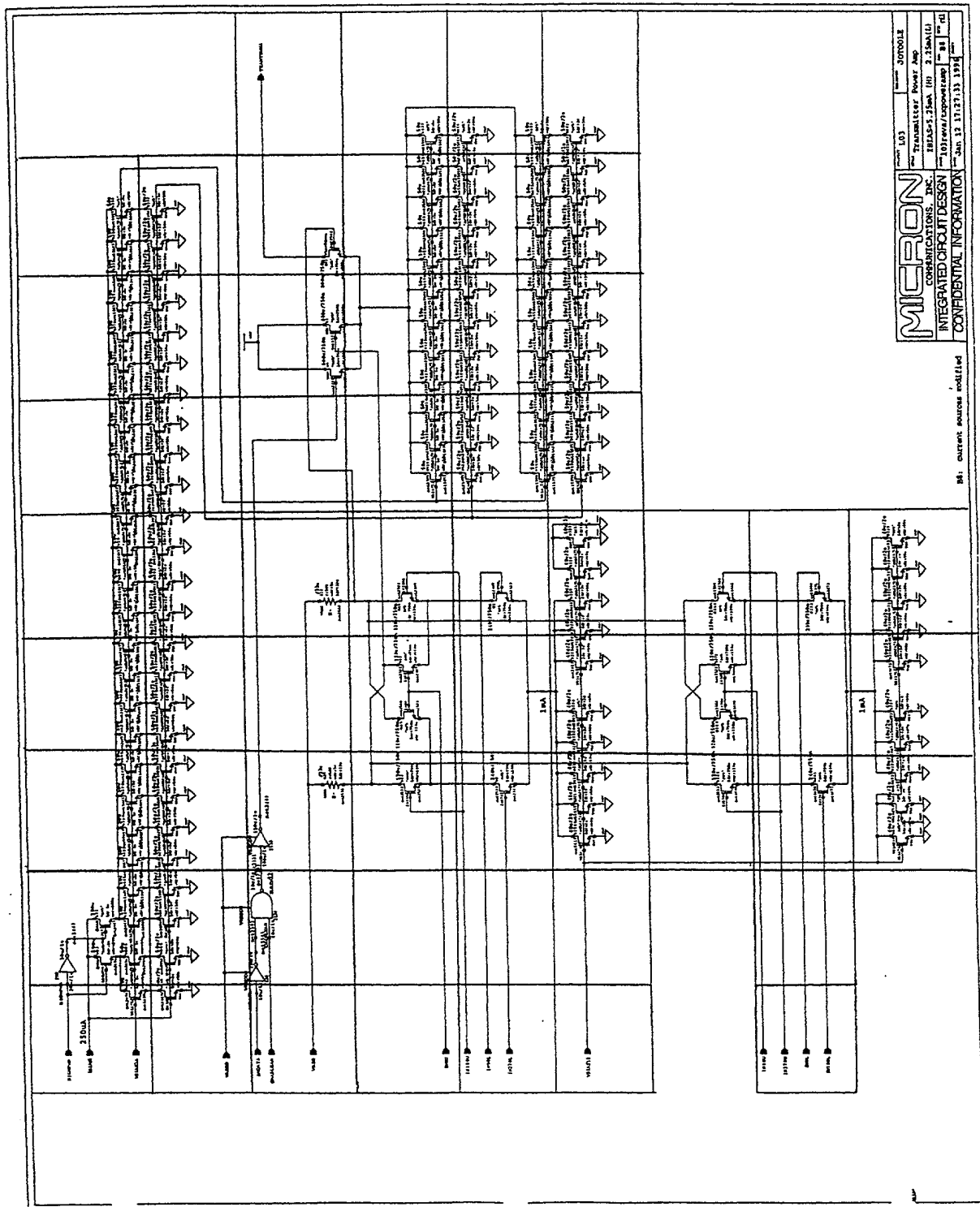
8.0606AA	8.0606AB	8.0606AC	8.0606AD	8.0606AE	8.0606AF	8.0606AG	8.0606AH
8.0606BA	8.0606BB	8.0606BC	8.0606BD	8.0606BE	8.0606BF	8.0606BG	8.0606BH
8.0606CA	8.0606CB	8.0606CC	8.0606CD	8.0606CE	8.0606CF	8.0606CG	8.0606CH
8.0606DA	8.0606DB	8.0606DC	8.0606DD	8.0606DE	8.0606DF	8.0606DG	8.0606DH
8.0606EA	8.0606EB	8.0606EC	8.0606ED	8.0606EE	8.0606EF	8.0606EG	8.0606EH
8.0606FA	8.0606FB	8.0606FC	8.0606FD	8.0606FE	8.0606FF	8.0606FG	8.0606FH
		8.0606GC	8.0606GD	8.0606GE			
8.0606HA	8.0606HB	8.0606HC	8.0606HD	8.0606HE			
	8.0606IB	8.0606IC	8.0606ID	8.0606IE			
							8.0606CI
							8.0606DI
							8.0606EI
							8.0606FI

И. П. О. 88.060606



SECRET

Fig. 8.0606



code "e30e2860"

8.0607AA	8.0607AB	8.0607AC	8.0607AD	8.0607AE	8.0607AF	8.0607AG	8.0607AH	8.0607AI	8.0607AJ
8.0607BA	8.0607BB	8.0607BC	8.0607BD	8.0607BE	8.0607BF	8.0607BG	8.0607BH	8.0607BI	8.0607BJ
8.0607CA	8.0607CB	8.0607CC	8.0607CD	8.0607CE	8.0607CF	8.0607CG	8.0607CH	8.0607CI	8.0607CJ
8.0607DA	8.0607DB	8.0607DC	8.0607DD	8.0607DE	8.0607DF	8.0607DG	8.0607DH	8.0607DI	8.0607DJ
8.0607EA	8.0607EB	8.0607EC	8.0607ED	8.0607EE	8.0607EF	8.0607EG	8.0607EH	8.0607EI	8.0607EJ
8.0607FA	8.0607FB	8.0607FC	8.0607FD	8.0607FE	8.0607FF	8.0607FG	8.0607FH	8.0607FI	8.0607FJ
8.0607GA	8.0607GB	8.0607GC	8.0607GD	8.0607GE	8.0607GF	8.0607GG	8.0607GH	8.0607GI	8.0607GJ
8.0607HA	8.0607HB	8.0607HC	8.0607HD	8.0607HE	8.0607HF	8.0607HG	8.0607HH	8.0607HI	8.0607HJ
8.0607IA	8.0607IB	8.0607IC	8.0607ID	8.0607IE	8.0607IF	8.0607IG	8.0607IH	8.0607II	8.0607IJ
8.0607JA	8.0607JB	8.0607JC	8.0607JD	8.0607JE	8.0607JF	8.0607JG	8.0607JH	8.0607JI	8.0607JJ

II II III III III III

<b>MICRON</b>	NAME	LAST	FIRST	SUFFIX
COMMUNICATIONS, INC.	TITLE			
INTEGRATED CIRCUIT DESIGN	COMPANY NAME			
CONSENSUAL INFORMATION	DATE	JUN 12 1978		

MI40-030

8.0608AA	8.0608AB
8.0608BA	8.0608BB

MI40-030

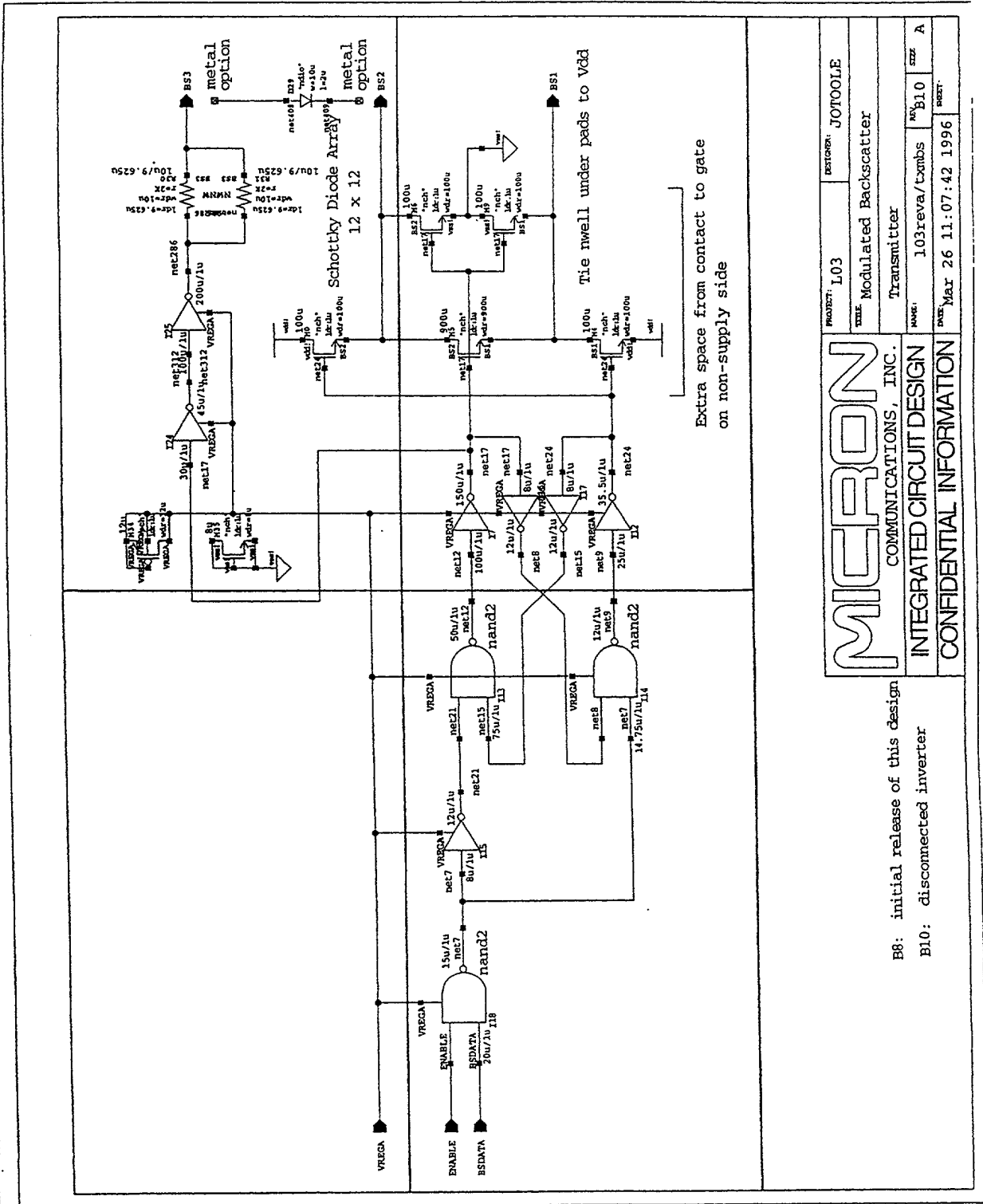


Fig. 8.0608

MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Modulated Backscatter	
INTEGRATED CIRCUIT DESIGN		Transmitter	
CONFIDENTIAL INFORMATION		NAME: 103reva/tombs	NO: B10
		DATE: Mar 26 11:07:42 1996	SIZE: A

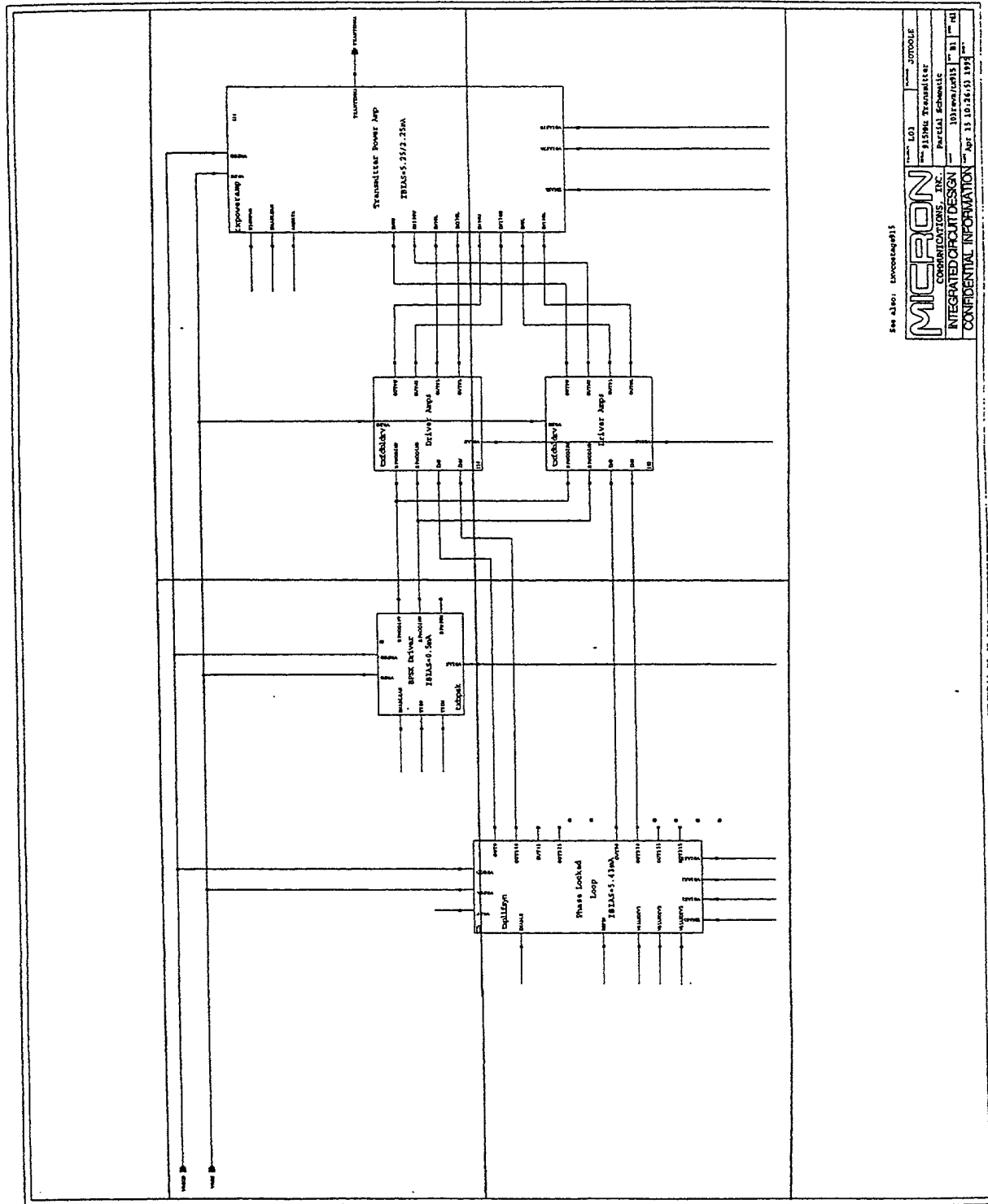
B8: initial release of this design  
B10: disconnected inverter

SECRET

8.07AA	8.07AB
8.07BA	8.07BB

SECRET

Fig. 8.07



8.0701AA	8.0701AB
8.0701BA	8.0701BB
8.0701CA	8.0701CB

II II II II II II

8.0701AA 8.0701AB 8.0701BA 8.0701BB 8.0701CA 8.0701CB



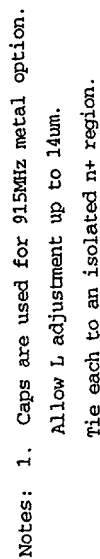


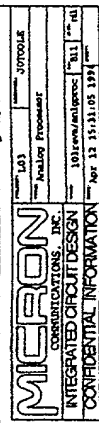
Fig. 8.0701

ALL INFORMATION CONTAINED

9AA	9AB
9BA	9BB
9CA	9CB

SECRET

Fig. 9



9.01AA	9.01AB	9.01AC	9.01AD	9.01AE	9.01AF	9.01AG	9.01AH
9.01BA	9.01BB	9.01BC	9.01BD	9.01BE	9.01BF	9.01BG	9.01BH
9.01CA	9.01CB	9.01CC	9.01CD	9.01CE	9.01CF	9.01CG	9.01CH
9.01DA	9.01DB	9.01DC	9.01DD	9.01DE	9.01DF	9.01DG	9.01DH

II II II II II II II II

SECRET

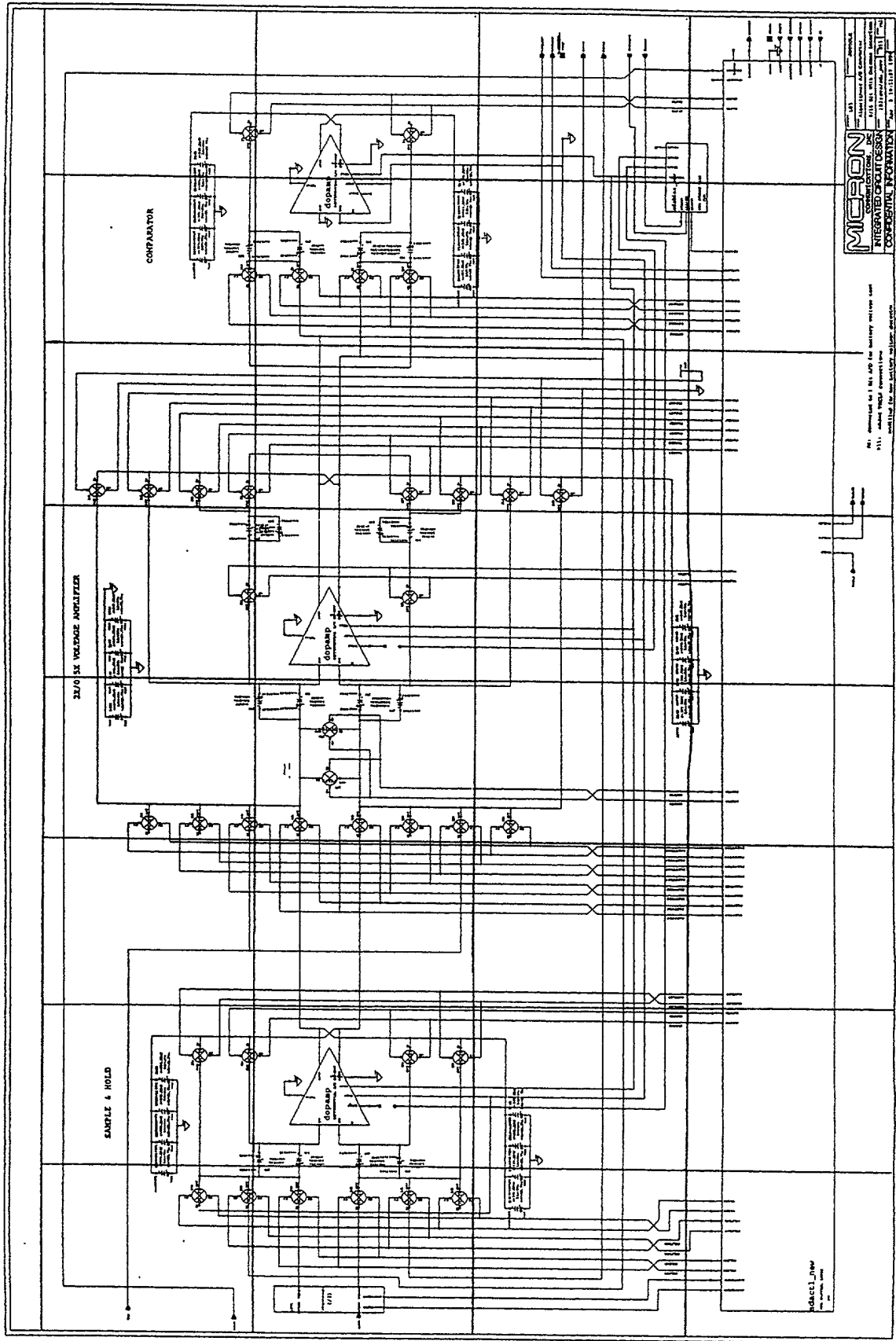
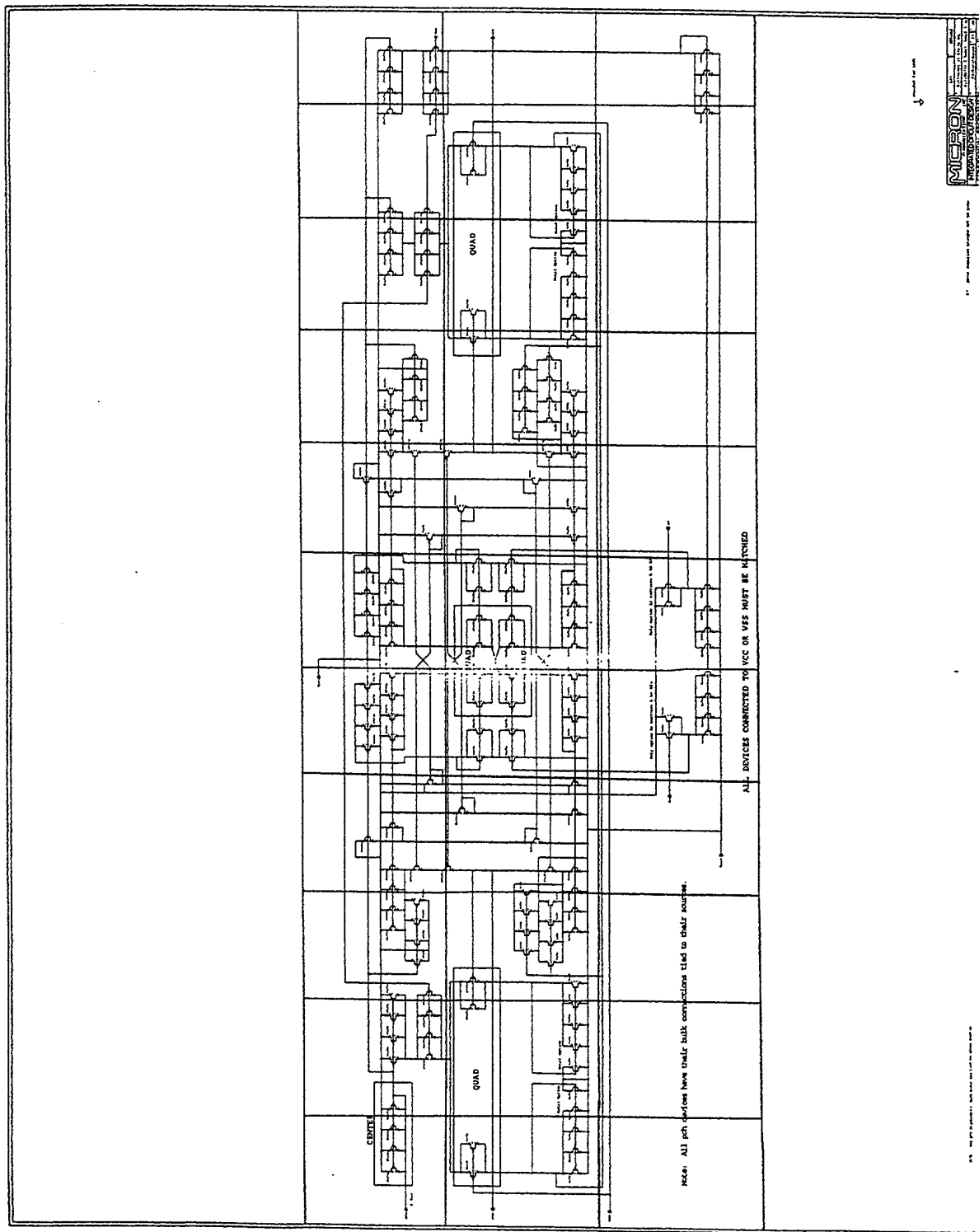


Fig. 9.01

9.0101AA	9.0101AB	9.0101AC	9.0101AD	9.0101AE	9.0101AF	9.0101AG	9.0101AH	9.0101AI	9.0101AJ	9.0101AK
9.0101BA	9.0101BB	9.0101BC	9.0101BD	9.0101BE	9.0101BF	9.0101BG	9.0101BH	9.0101BI	9.0101BJ	9.0101BK
9.0101CA	9.0101CB	9.0101CC	9.0101CD	9.0101CE	9.0101CF	9.0101CG	9.0101CH	9.0101CI	9.0101CJ	9.0101CK

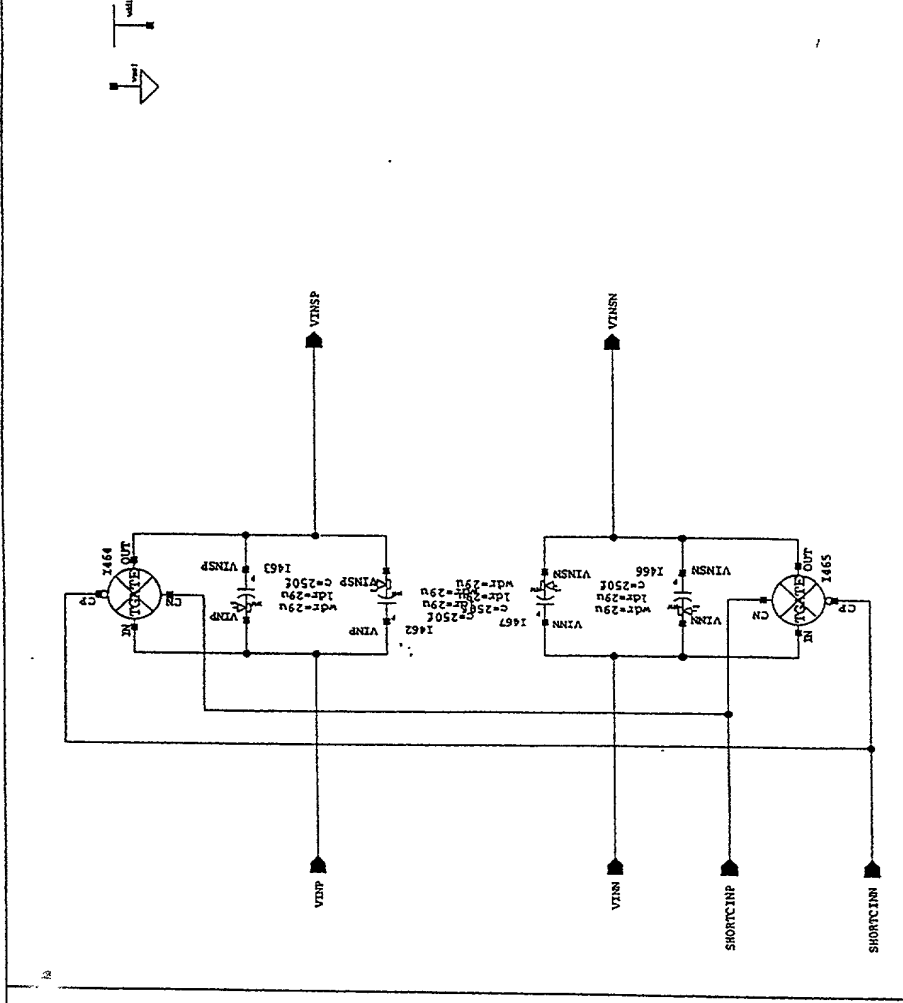
IL II III IV V



ALL DEVICES CONNECTED TO VCC OR VSS MUST BE MATCHED

FIG. 9.0101

CONFIDENTIAL



<b>MICRON</b>		PROJECT: L03		DESIGNER: JOTOOLE	
TITLE: Analog Divide by 2					
COMMUNICATIONS, INC.					
INTEGRATED CIRCUIT DESIGN					
CONFIDENTIAL INFORMATION					
NAME: 103reva/adaprescale		REV: B1		SIZE: A	
DATE: May 19 16:34:53 1995		SHEET: 1			

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

FIG. 9.0102



9.0103BA	9.0103BB	9.0103BC	9.0103BD	9.0103BE	9.0103BF	9.0103BG	9.0103BH	9.0103AJ	9.0103AK	9.0103AL	9.0103AM	9.0103AN	9.0103AO	9.0103AP
								9.0103BJ	9.0103BK	9.0103BL	9.0103BM	9.0103BN	9.0103BO	9.0103BP
9.0103CA	9.0103CB	9.0103CC	9.0103CD	9.0103CE	9.0103CF	9.0103CG	9.0103CH	9.0103CJ	9.0103CK	9.0103CL	9.0103CM	9.0103CN	9.0103CO	9.0103CP
9.0103DA	9.0103DB	9.0103DC	9.0103DD	9.0103DE	9.0103DF	9.0103DG	9.0103DH	9.0103DJ	9.0103DK	9.0103DL	9.0103DM	9.0103DN	9.0103DO	9.0103DP
9.0103EA	9.0103EB	9.0103EC	9.0103ED	9.0103EE	9.0103EF	9.0103EG	9.0103EH	9.0103EJ	9.0103EK	9.0103EL	9.0103EM	9.0103EN	9.0103EO	9.0103EP
9.0103FA	9.0103FB	9.0103FC	9.0103FD	9.0103FE	9.0103FF	9.0103FG	9.0103FH	9.0103FJ	9.0103FK	9.0103FL	9.0103FM	9.0103FN	9.0103FO	9.0103FP



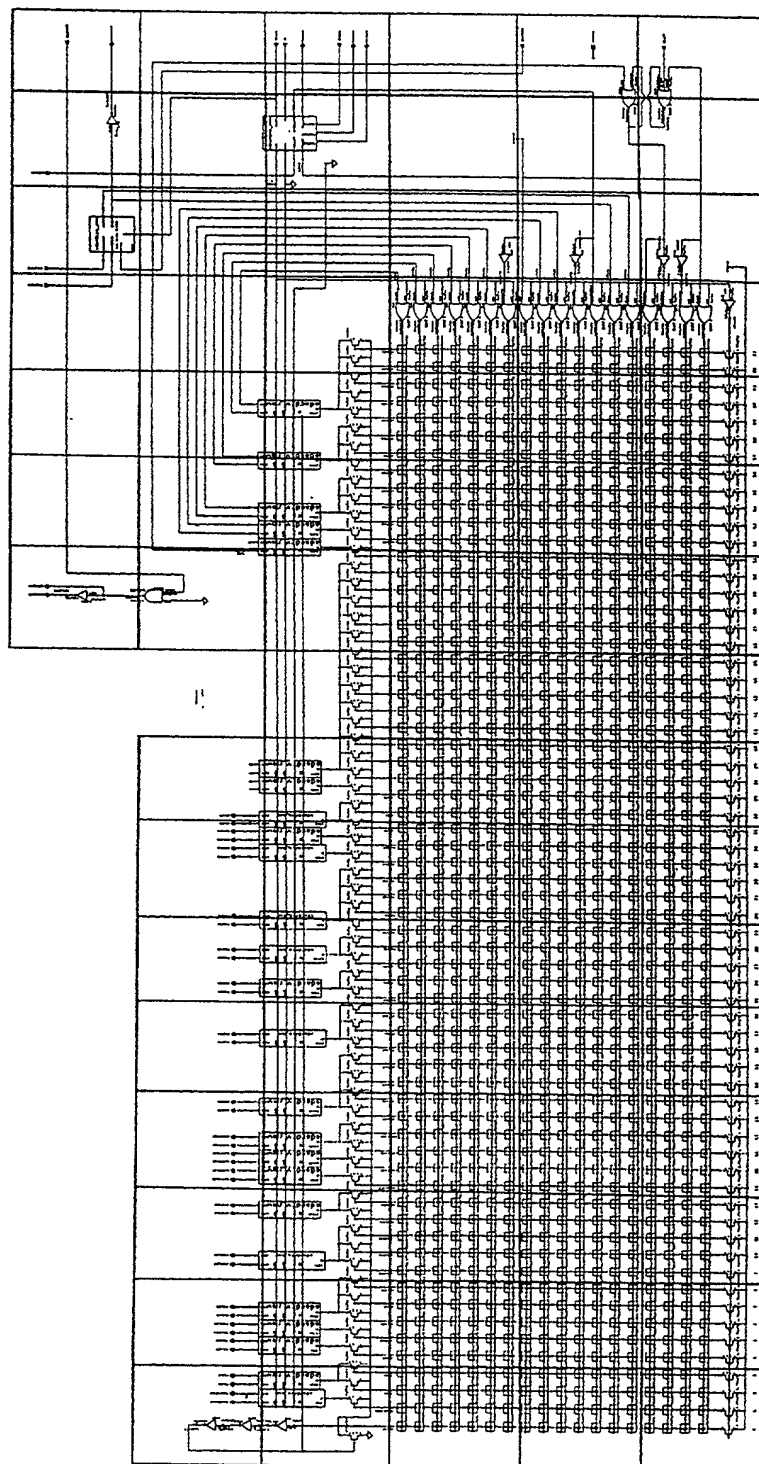



Fig. 9.0103

9.010301AA 9.010301AB 9.010301AC

9.010301AA	9.010301AB	9.010301AC
9.010301BA	9.010301BB	9.010301BC
9.010301CA	9.010301CB	9.010301CC

9.010301DD 9.010301DE 9.010301DF

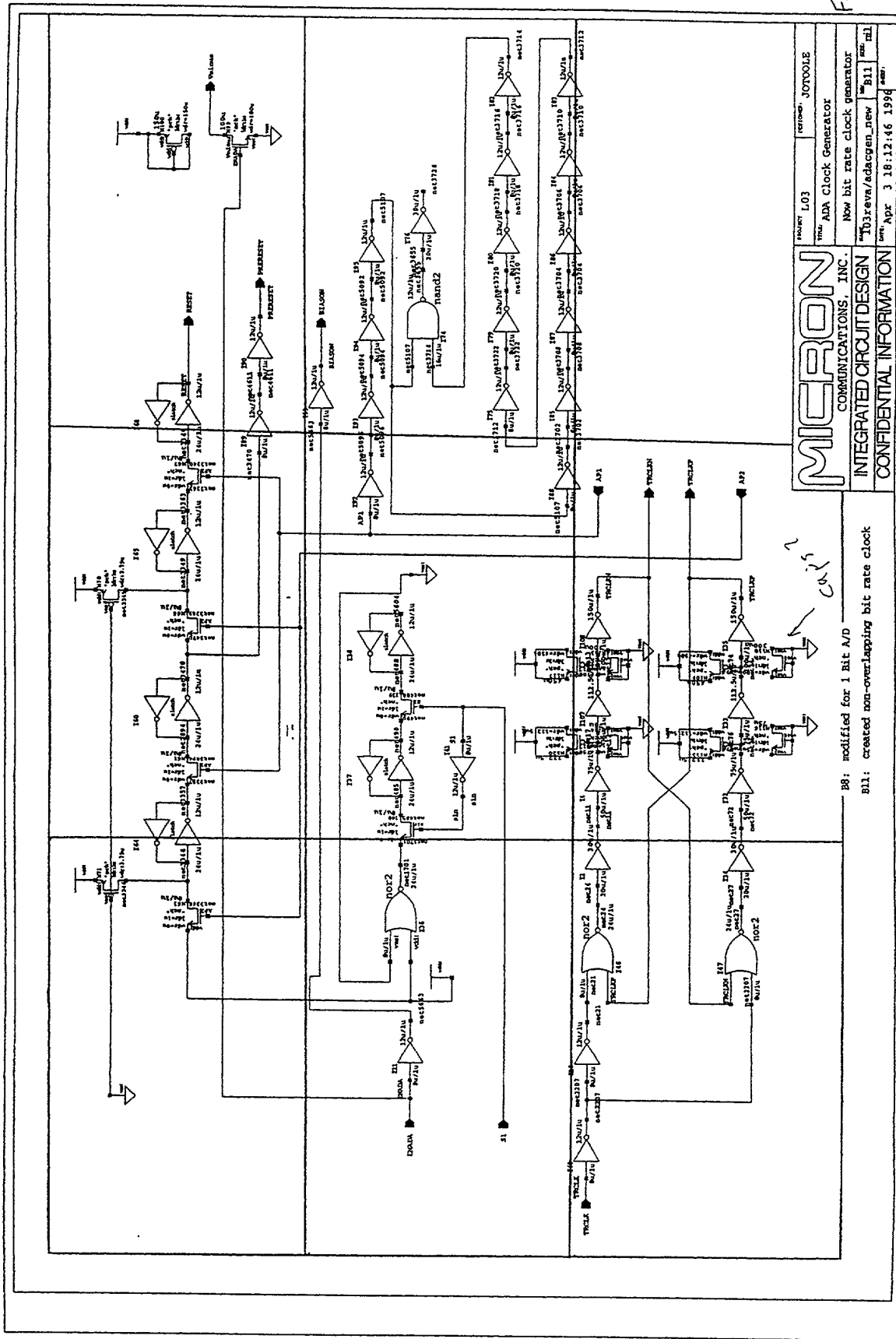


Fig. 9.00301

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

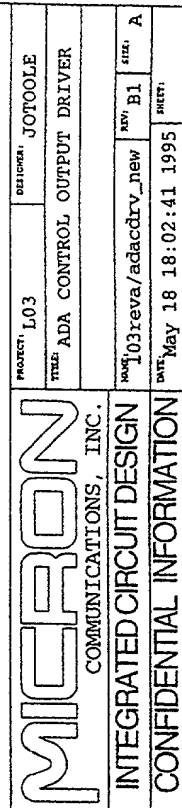
B8: modified for 1 Bit A/D  
B11: created non-overlapping bit rate clock

PROJECT: L03  
NAME: ADA Clock Generator  
NEW bit rate clock generator  
T03revm/adagen\_new  
B11  
APR 3 18:12:46 1994

ALL INFORMATION CONTAINED HEREIN IS UNCLASSIFIED

9.010302AA	9.010302AB
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ALL INFORMATION

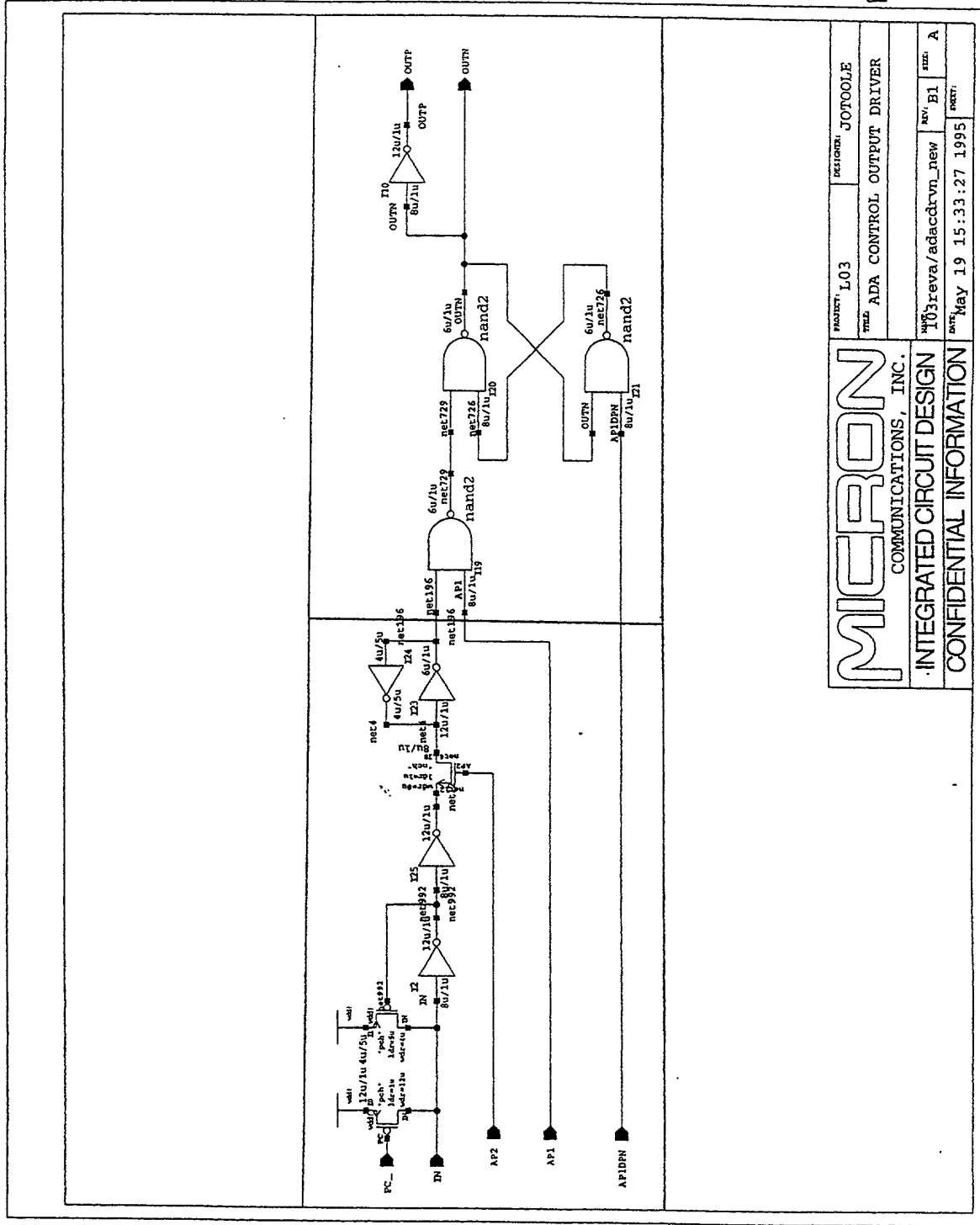


9.010303AB

9.010303AA

EXHIBIT 9.010303

THIS DOCUMENT CONTAINS UNCLASSIFIED INFORMATION EXCEPT WHERE SHOWN OTHERWISE



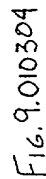
MICRON		DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		FIELD: ADA CONTROL OUTPUT DRIVER
INTEGRATED CIRCUIT DESIGN		REV: B1
CONFIDENTIAL INFORMATION		DATE: May 19 15:33:27 1995

FIG. 9.010303



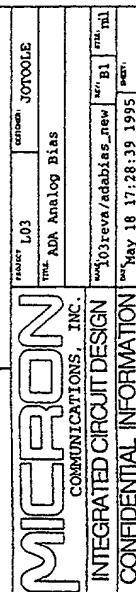
9.010304AA	9.010304AB
9.010304BA	9.010304BB

ALL INFORMATION CONTAINED



MICRON		PROJECT: L03	SECTION: JOTOOLE
COMMUNICATIONS, INC.		TITLE: ADA Data Latch	
INTEGRATED CIRCUIT DESIGN		NAME: _l03reva/adadlat_new	REV: B11
CONFIDENTIAL INFORMATION		DATE: ADR 8 10:39:12 1996	SHEET: A

9.0104AA	9.0104AB	9.0104AC	9.0104AD	9.0104AE
9.0104BA	9.0104BB	9.0104BC	9.0104BD	9.0104BE
9.0104CA	9.0104CB	9.0104CC	9.0104CD	
9.0104DA	9.0104DB	9.0104DC	9.0104DD	



9.02AA	9.02AB	9.02AC	9.02AD	9.02AE	9.02AF	9.02AG	9.02AH	9.02AI	9.02AJ	9.02AK
9.02BA	9.02BB	9.02BC	9.02BD	9.02BE	9.02BF	9.02BG	9.02BH	9.02BI	9.02BJ	9.02BK
9.02CA		9.02CC	9.02CD	9.02CE	9.02CF	9.02CG	9.02CH	9.02CI	9.02CJ	9.02CK
9.02DA	9.02DB	9.02DC	9.02DD		9.02DF	9.02DG	9.02DH	9.02DI	9.02DJ	9.02DK

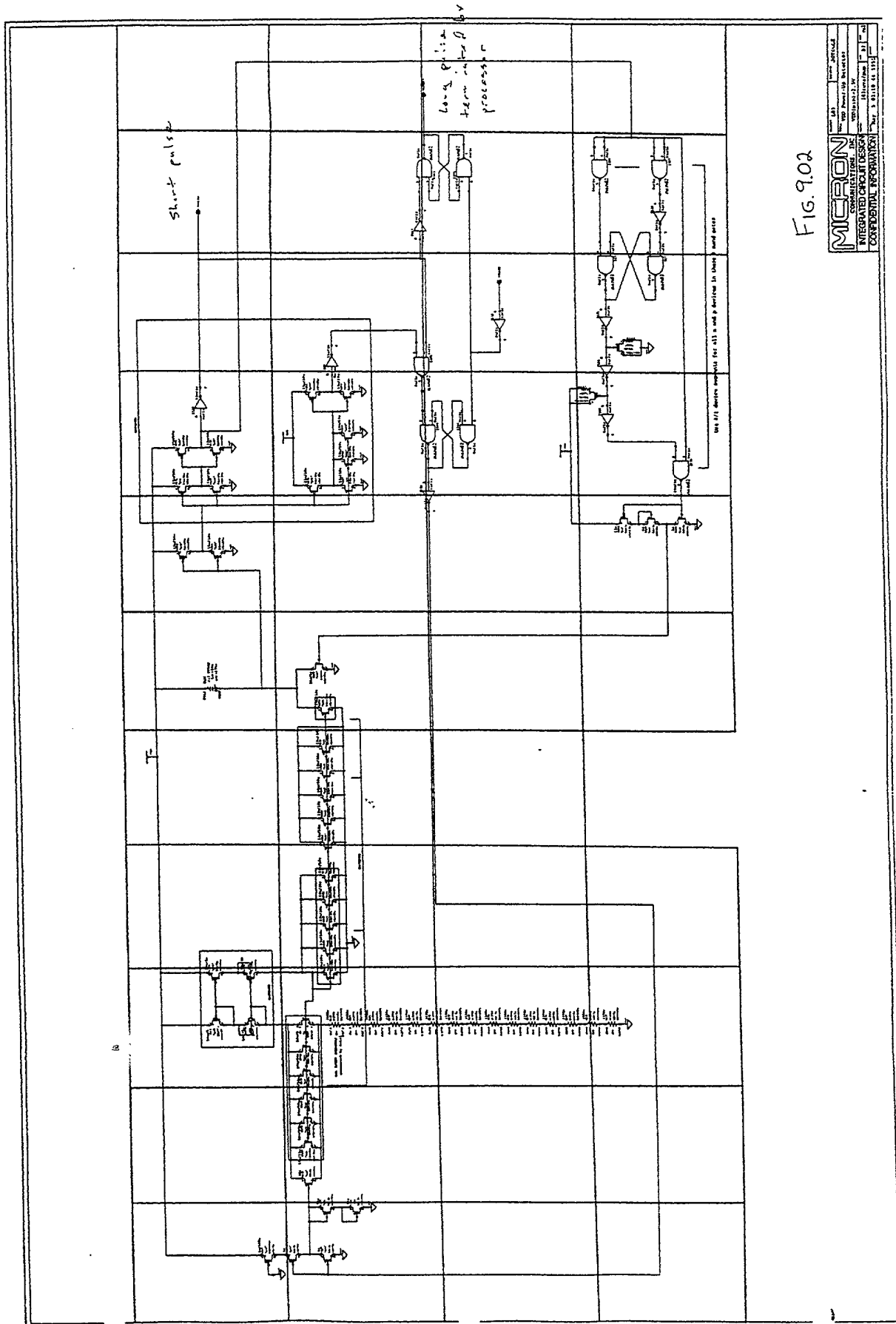
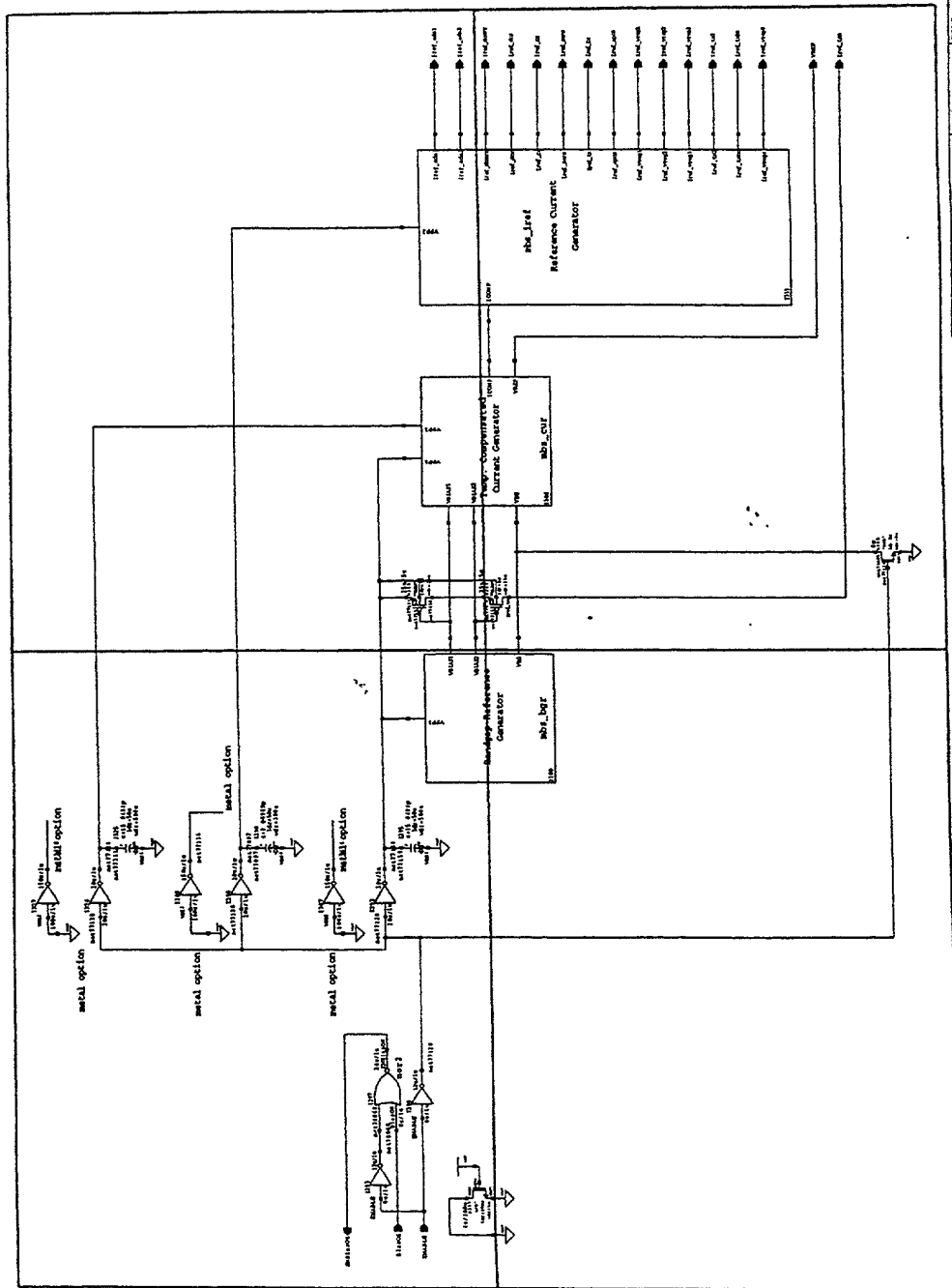


FIG. 9.02

MICRON	
INTEGRATED CIRCUIT DESIGN	CONFIDENTIAL
DATE: 10/18/84	REV: 1.0
CONFIDENTIAL INFORMATION	

9.03AA	9.03AB
9.03BA	9.03BB

FIG. 9.03



821 deleted testing function  
 added delaylock logic  
 created buffered vax

**MICRON**  
 COMMUNICATIONS, INC.  
 INTEGRATED CIRCUIT DESIGN  
 CONFIDENTIAL INFORMATION

10/11/84  
 10/11/84  
 10/11/84



# THEO. B. B. B.

SECRET

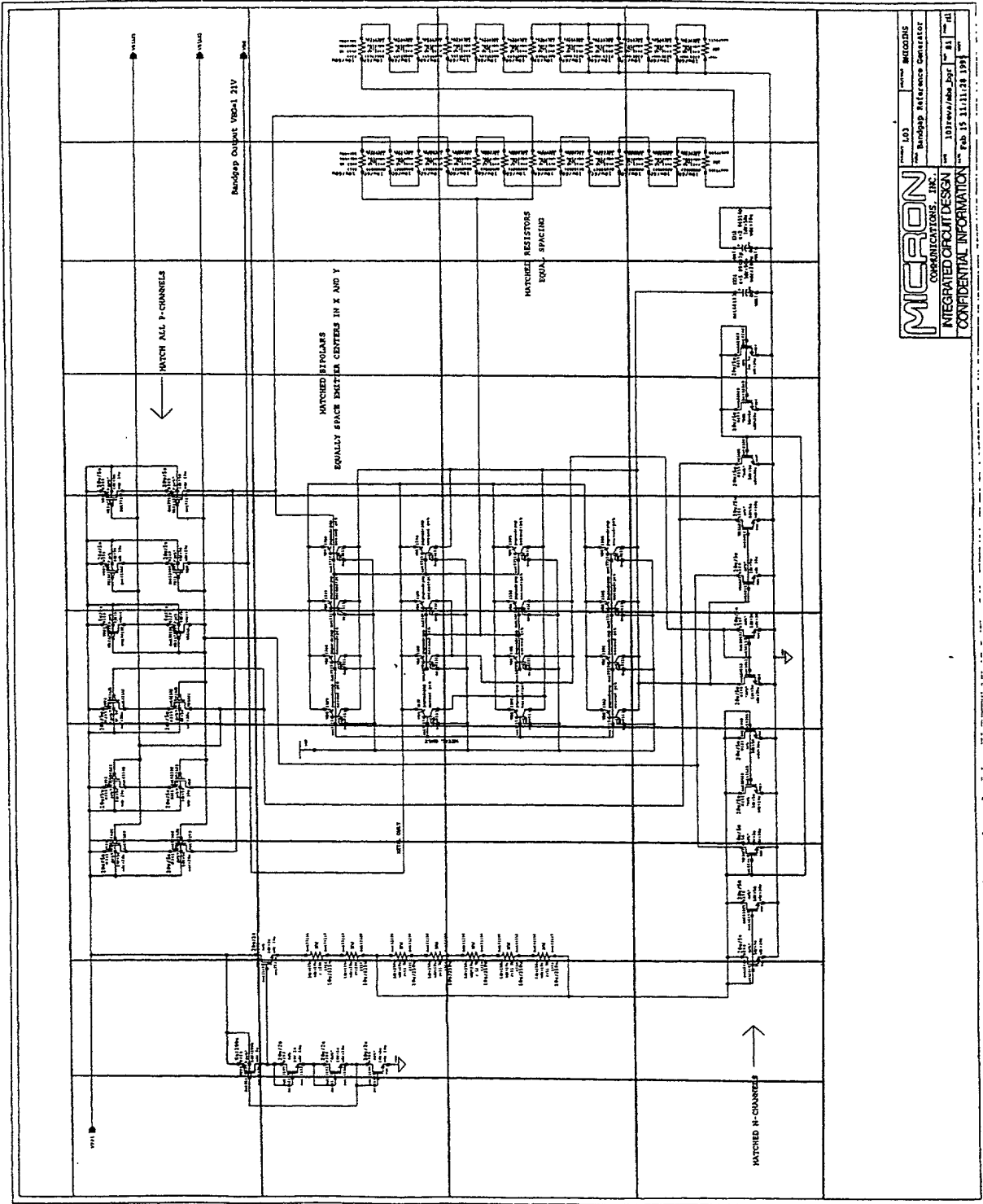


FIG. 9.0301

<b>MICRON</b>	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
DESIGN NO.	103000000
REV.	1
DATE	Feb 15 11/11/28 1991
DESIGNED BY	...
CHECKED BY	...
APPROVED BY	...

9.0302AA	9.0302AB	9.0302AC	9.0302AD	9.0302AE	9.0302AF	9.0302AG	9.0302AH	9.0302AI	9.0302AJ
9.0302BA	9.0302BB	9.0302BC	9.0302BD	9.0302BE	9.0302BF	9.0302BG	9.0302BH	9.0302BI	9.0302BJ
		9.0302CC	9.0302CD	9.0302CE	9.0302CF	9.0302CG	9.0302CH	9.0302CI	9.0302CJ
						9.0302DG	9.0302DH	9.0302DI	

II 9.0302

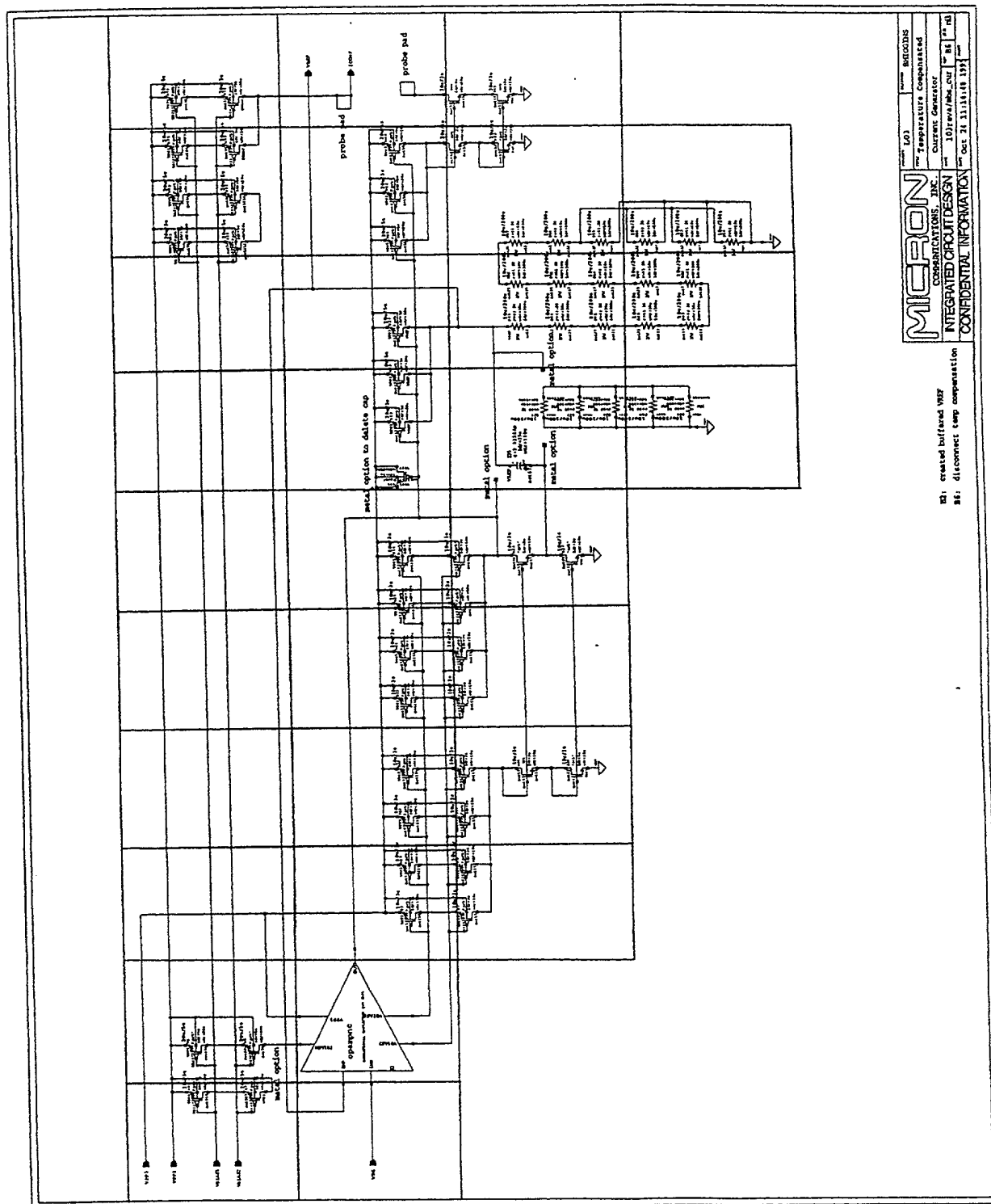
[illegible]

FIG. 9.0302

9.0303AA 9.0303AB 9.0303AC 9.0303AD 9.0303AE 9.0303AF

9.0303AA	9.0303AB	9.0303AC	9.0303AD	9.0303AE	9.0303AF
9.0303BA	9.0303BB	9.0303BC	9.0303BD	9.0303BE	9.0303BF
	9.0303CB	9.0303CC	9.0303CD	9.0303CE	9.0303CF

9.0303DD 9.0303EE 9.0303FF



continued on next page

9.04AA	9.04AB	9.04AC	9.04AD	9.04AE
9.04BA	9.04BB	9.04BC	9.04BD	9.04BE
9.04CA	9.04CB	9.04CC	9.04CD	9.04CE

11 11 11 11 11



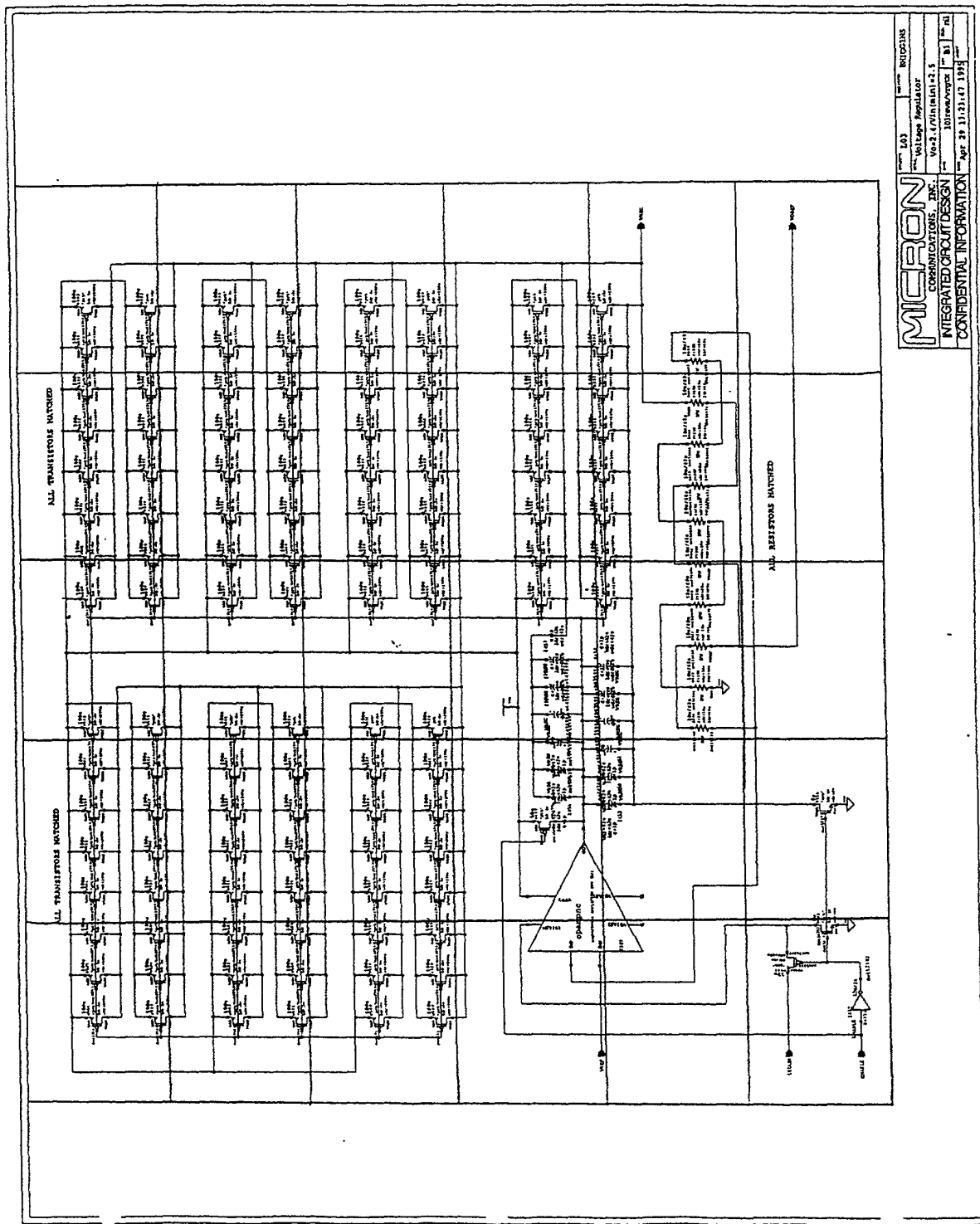


9.05AA 9.05AB 9.05AC 9.05AD 9.05AE

9.05AA	9.05AB	9.05AC	9.05AD	9.05AE
9.05BA	9.05BB	9.05BC	9.05BD	9.05BE
9.05CA	9.05CB	9.05CC	9.05CD	9.05CE
9.05DA	9.05DB	9.05DC	9.05DD	9.05DE
9.05EA	9.05EB	9.05EC	9.05ED	9.05EE
9.05FA	9.05FB	9.05FC	9.05FD	9.05FE

9.05

Parameter	Unit	Value	Standard Error	95% CI	P-value
Intercept		1.00	0.00	1.00	0.00
Age	Year	0.02	0.01	-0.01, 0.05	0.15
Gender					
Male		0.00	0.01	-0.02, 0.02	0.98
Female		0.00	0.01	-0.02, 0.02	0.98
Education	Year	0.01	0.01	-0.01, 0.03	0.45
Income	Year	0.01	0.01	-0.01, 0.03	0.45
Health status					
Good		0.00	0.01	-0.02, 0.02	0.98
Poor		0.00	0.01	-0.02, 0.02	0.98
Marital status					
Married		0.00	0.01	-0.02, 0.02	0.98
Single		0.00	0.01	-0.02, 0.02	0.98
Occupation					
Professional		0.00	0.01	-0.02, 0.02	0.98
Managerial		0.00	0.01	-0.02, 0.02	0.98
Skilled		0.00	0.01	-0.02, 0.02	0.98
Unskilled		0.00	0.01	-0.02, 0.02	0.98
Religion					
Christian		0.00	0.01	-0.02, 0.02	0.98
Muslim		0.00	0.01	-0.02, 0.02	0.98
Hindu		0.00	0.01	-0.02, 0.02	0.98
Buddhist		0.00	0.01	-0.02, 0.02	0.98
Other		0.00	0.01	-0.02, 0.02	0.98
Region					
North		0.00	0.01	-0.02, 0.02	0.98
South		0.00	0.01	-0.02, 0.02	0.98
East		0.00	0.01	-0.02, 0.02	0.98
West		0.00	0.01	-0.02, 0.02	0.98
Central		0.00	0.01	-0.02, 0.02	0.98
Other		0.00	0.01	-0.02, 0.02	0.98
Time	Year	0.01	0.01	-0.01, 0.03	0.45
Time squared	Year squared	0.00	0.00	-0.00, 0.00	0.98
Time cubed	Year cubed	0.00	0.00	-0.00, 0.00	0.98
Time quart	Year quart	0.00	0.00	-0.00, 0.00	0.98
Time quint	Year quint	0.00	0.00	-0.00, 0.00	0.98
Time sext	Year sext	0.00	0.00	-0.00, 0.00	0.98
Time sept	Year sept	0.00	0.00	-0.00, 0.00	0.98
Time oct	Year oct	0.00	0.00	-0.00, 0.00	0.98
Time non	Year non	0.00	0.00	-0.00, 0.00	0.98
Time dec	Year dec	0.00	0.00	-0.00, 0.00	0.98
Time elev	Year elev	0.00	0.00	-0.00, 0.00	0.98
Time hend	Year hend	0.00	0.00	-0.00, 0.00	0.98
Time nint	Year nint	0.00	0.00	-0.00, 0.00	0.98
Time tenth	Year tenth	0.00	0.00	-0.00, 0.00	0.98
Time eleventh	Year eleventh	0.00	0.00	-0.00, 0.00	0.98
Time twelfth	Year twelfth	0.00	0.00	-0.00, 0.00	0.98
Time thirteenth	Year thirteenth	0.00	0.00	-0.00, 0.00	0.98
Time fourteenth	Year fourteenth	0.00	0.00	-0.00, 0.00	0.98
Time fifteenth	Year fifteenth	0.00	0.00	-0.00, 0.00	0.98
Time sixteenth	Year sixteenth	0.00	0.00	-0.00, 0.00	0.98
Time seventeenth	Year seventeenth	0.00	0.00	-0.00, 0.00	0.98
Time eighteenth	Year eighteenth	0.00	0.00	-0.00, 0.00	0.98
Time nineteenth	Year nineteenth	0.00	0.00	-0.00, 0.00	0.98
Time twentieth	Year twentieth	0.00	0.00	-0.00, 0.00	0.98
Time twenty-first	Year twenty-first	0.00	0.00	-0.00, 0.00	0.98
Time twenty-second	Year twenty-second	0.00	0.00	-0.00, 0.00	0.98
Time twenty-third	Year twenty-third	0.00	0.00	-0.00, 0.00	0.98
Time twenty-fourth	Year twenty-fourth	0.00	0.00	-0.00, 0.00	0.98
Time twenty-fifth	Year twenty-fifth	0.00	0.00	-0.00, 0.00	0.98
Time twenty-sixth	Year twenty-sixth	0.00	0.00	-0.00, 0.00	0.98



9.0501

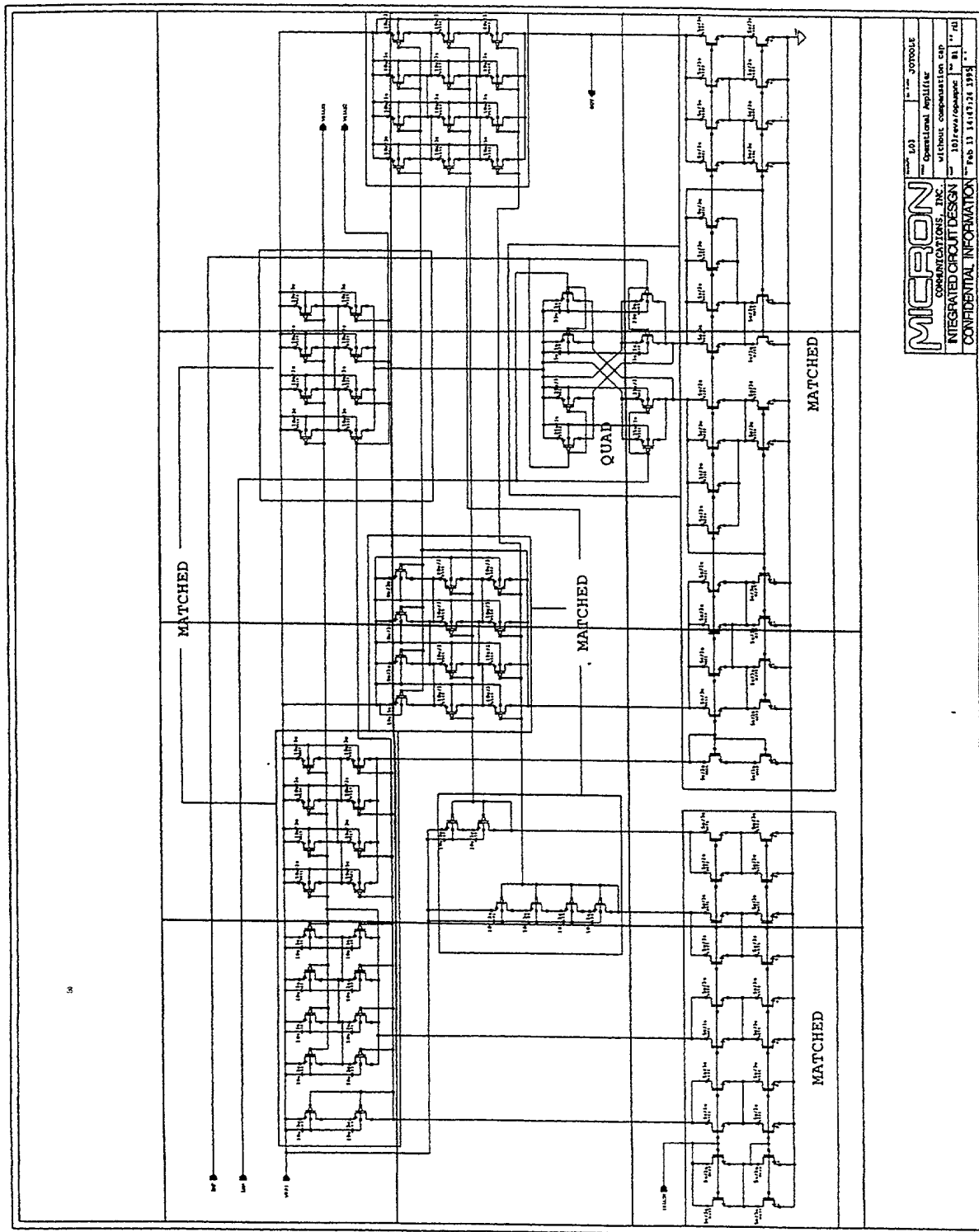


FIG. 9.0501

<b>MICRON</b>	
DESIGN	3000LS
FUNCTIONAL APPLICABLE	WITHOUT COMPENSATION CAP
INTEGRATED CIRCUIT DESIGN	10/19/80
CONFIDENTIAL INFORMATION	PAGE 13 14(07)81 1980

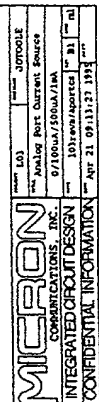
9.06AA	9.06AB	9.06AC	9.06AD	9.06AE
9.06BA	9.06BB	9.06BC	9.06BD	9.06BE
9.06CA	9.06CB	9.06CC	9.06CD	
9.06DA	9.06DB	9.06DC	9.06DD	

JEFF GILES

Fig. 9.06



9.07AA	9.07AB	9.07AC	9.07AD	9.07AE	9.07AF	9.07AG	9.07AH	9.07AI
9.07BA	9.07BB	9.07BC	9.07BD	9.07BE	9.07BF	9.07BG	9.07BH	9.07BI
9.07CA	9.07CB	9.07CC	9.07CD	9.07CE	9.07CF	9.07CG	9.07CH	
9.07DA	9.07DB	9.07DC	9.07DD	9.07DE	9.07DF	9.07DG		
9.07EA	9.07EB	9.07EC	9.07ED	9.07EE	9.07EF	9.07EG		





9.08AA 9.08AB 9.08AC  
9.08BA 9.08BB 9.08BC  
9.08CA 9.08CB 9.08CC

9.08AA	9.08AB	9.08AC
9.08BA	9.08BB	9.08BC
9.08CA	9.08CB	9.08CC

9.08 9.08

SECRET

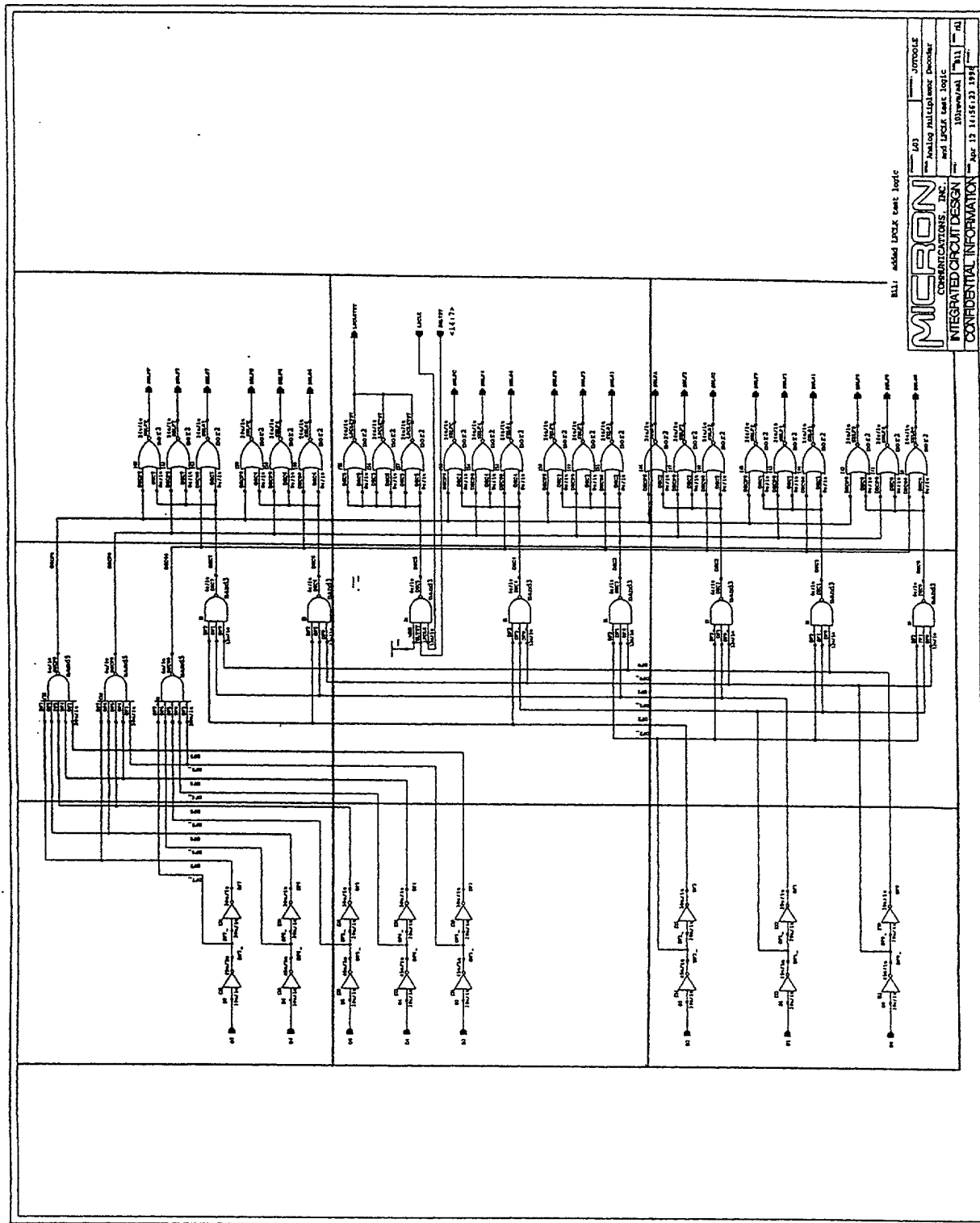


Fig. 9.08

9.09AA	9.09AB
9.09BA	9.09BB

<b>MICRON</b>		NAME	LOJ	DATE	JOTPOLE
CORPORATIONS, INC.		COMPANY	Random Clock Generator		
INTEGRATED CIRCUIT DESIGN		ITEM	1037rev/rog	83	all
CONFIDENTIAL INFORMATION		DATE	Jan 24, 09:37:38 1986		

2nd: added 22 Indus

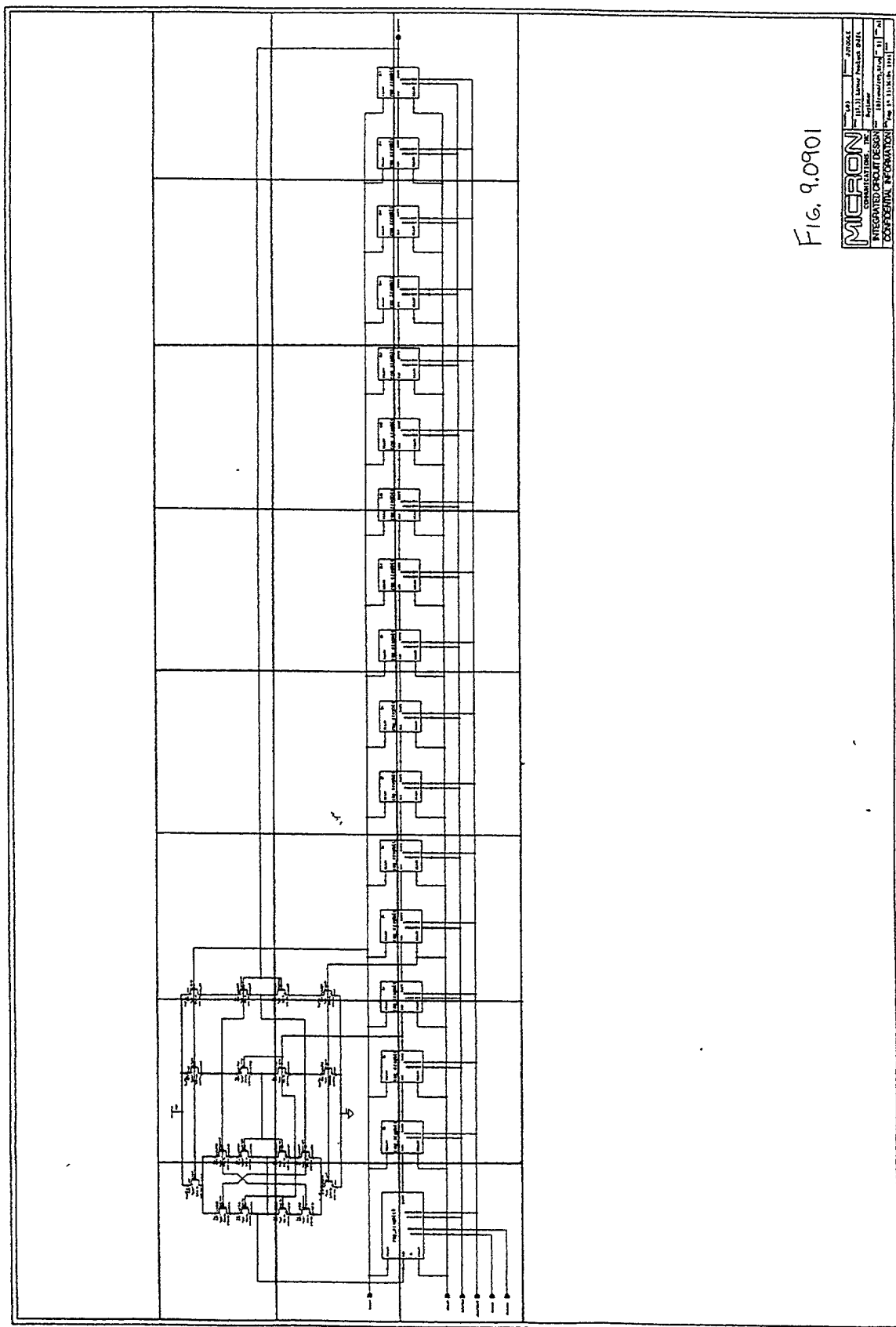
[illegible]

M140-030

9.0901AA	9.0901AB	9.0901AC	9.0901AD	9.0901AE	9.0901AF	9.0901AG	9.0901AH
9.0901BA	9.0901BB	9.0901BC	9.0901BD	9.0901BE	9.0901BF	9.0901BG	9.0901BH
9.0901CA	9.0901CB	9.0901CC	9.0901CD	9.0901CE	9.0901CF	9.0901CG	9.0901CH

11 07 1950

Table 1. Demographic characteristics of the study population	
Age (years)	Mean (SD)
Male	45.2 (10.5)
Female	46.8 (11.2)
Marital status	
Married	78.5%
Single	12.3%
Divorced	8.2%
Widowed	1.0%
Education level	
High school or less	65.4%
College	34.6%
Income (USD/month)	
< 1000	25.3%
1000-2000	45.7%
> 2000	29.0%
Occupation	
Professional	32.1%
Managerial	18.5%
Service	22.3%
Skilled	15.2%
Unskilled	11.9%
Health status	
Good	72.5%
Fair	18.7%
Poor	8.8%
Smoking status	
Smoker	28.4%
Non-smoker	71.6%
Alcohol consumption	
Regular	15.6%
Occasional	32.1%
Never	52.3%



9.090101AA	9.090101AB	9.090101AC
9.090101BA	9.090101BB	9.090101BC
9.090101CA	9.090101CB	9.090101CC

EX-101 9.090101

FIG. 9.090101



9.090102AA	9.090102AB
9.090102BA	9.090102BB

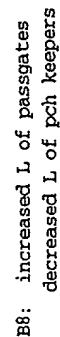
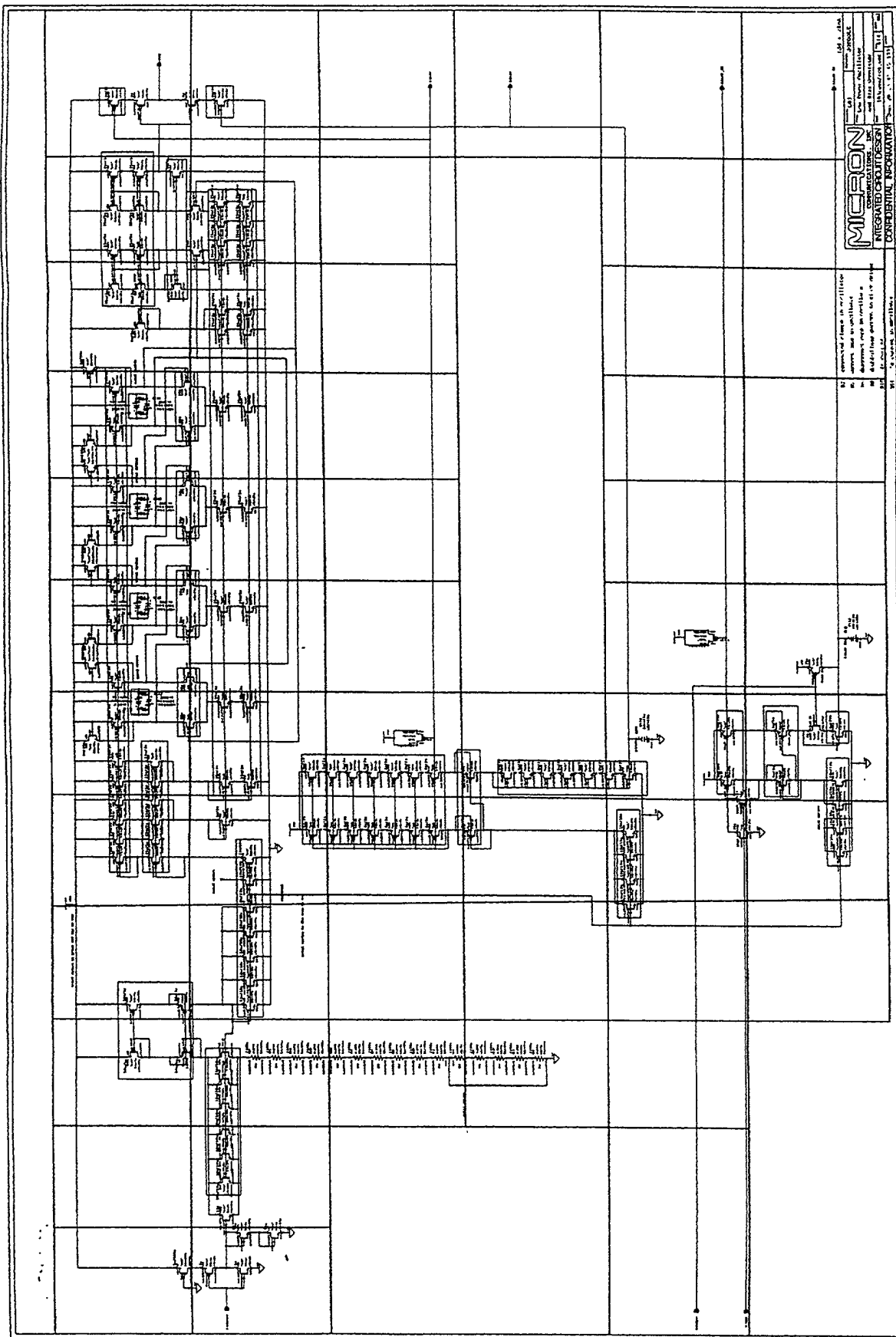


FIG. 9.090102

<b>MICRON</b>		PROJECT: L03	DESIGN: J0700LE
		TITLE: RCG Shift Register Bit	
NAME: j03reva/rcg_sregbit		REV: B8	SIZE: A
DATE: Jan 25 13:25:16 1996		SHEET:	

9.0902AA	9.0902AB	9.0902AC	9.0902AD	9.0902AE	9.0902F	9.0902AG	9.0902AH	9.0902AI	9.0902AJ	9.0902AK	9.0902AL
9.0902BA	9.0902BB	9.0902BC	9.0902BD	9.0902BE	9.0902F	9.0902BG	9.0902BH	9.0902BI	9.0902BJ	9.0902BK	9.0902BL
		9.0902CC	9.0902CD	9.0902CE	9.0902F	9.0902CG	9.0902CH	9.0902CI	9.0902CJ	9.0902CK	9.0902CL
		9.0902DC	9.0902DD	9.0902DE	9.0902F						9.0902DL
9.0902EA	9.0902EB	9.0902EC	9.0902ED	9.0902EE	9.0902F	9.0902EG	9.0902EH	9.0902EI	9.0902EJ	9.0902EK	9.0902EL
			9.0902FD	9.0902FE	9.0902F	9.0902FG	9.0902FH	9.0902FI	9.0902FJ	9.0902FK	9.0902FL

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68000  
MICROPROCESSOR  
CONFIDENTIAL INFORMATION

Fig. 9.0902A-F

9.0903AA 9.0903AB 9.0903AC

9.0903AA	9.0903AB	9.0903AC
9.0903BA	9.0903BB	9.0903BC
9.0903CA	9.0903CB	9.0903CC

9.0903

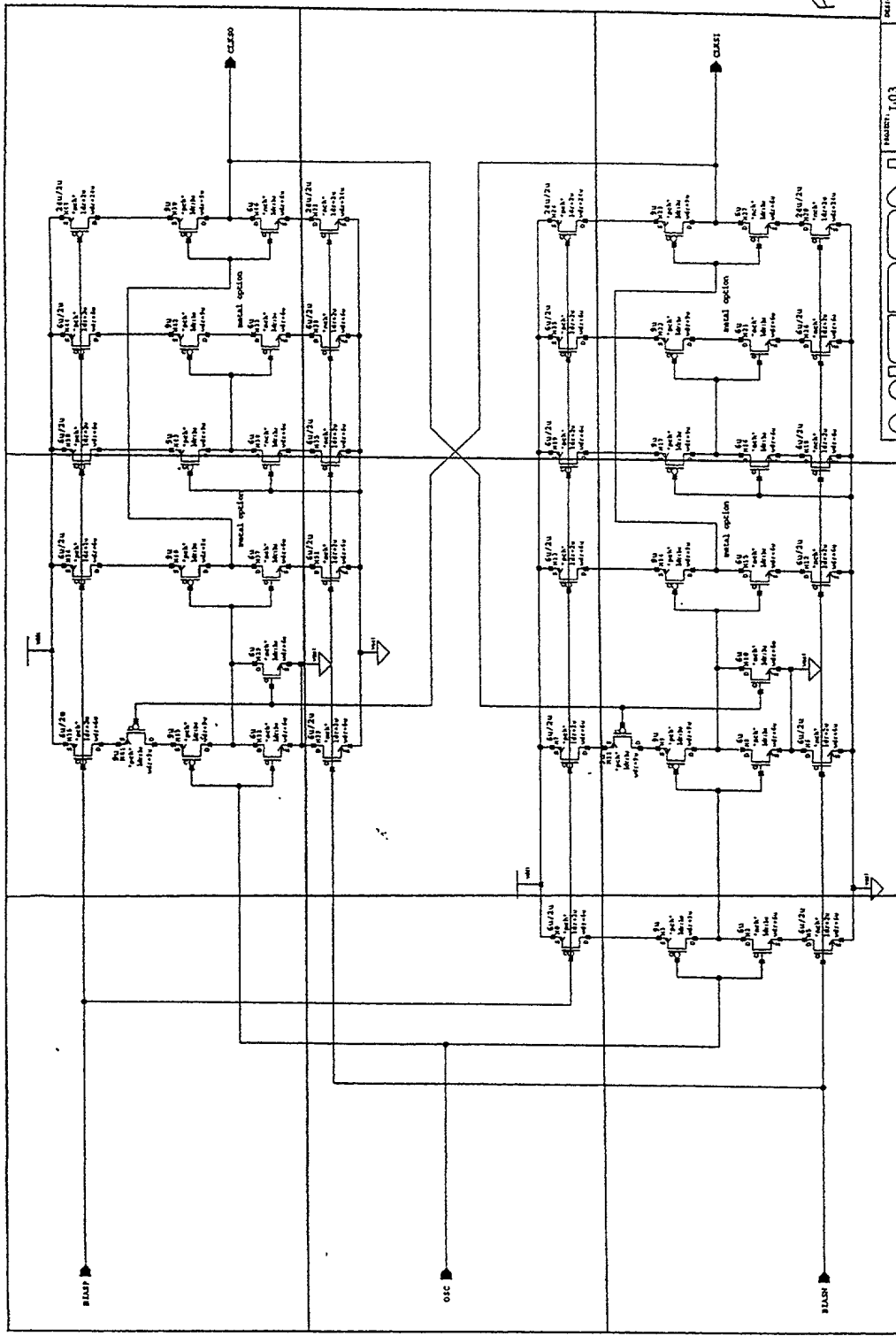


Fig. 9.0903

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

INTEGRATED CIRCUIT	DESIGNER	JOTPOOLE
NAME	103revn/rcg.clkgen	REV B8
DATE	Jan 24 09:56:43 1996	FILE

B8: wired cross-couples to ground

10AA	10AB	10AC	10AD
10BA	10BB	10BC	10BD
10CA	10CB	10CC	10CD
10DA	10DB	10DC	10DD

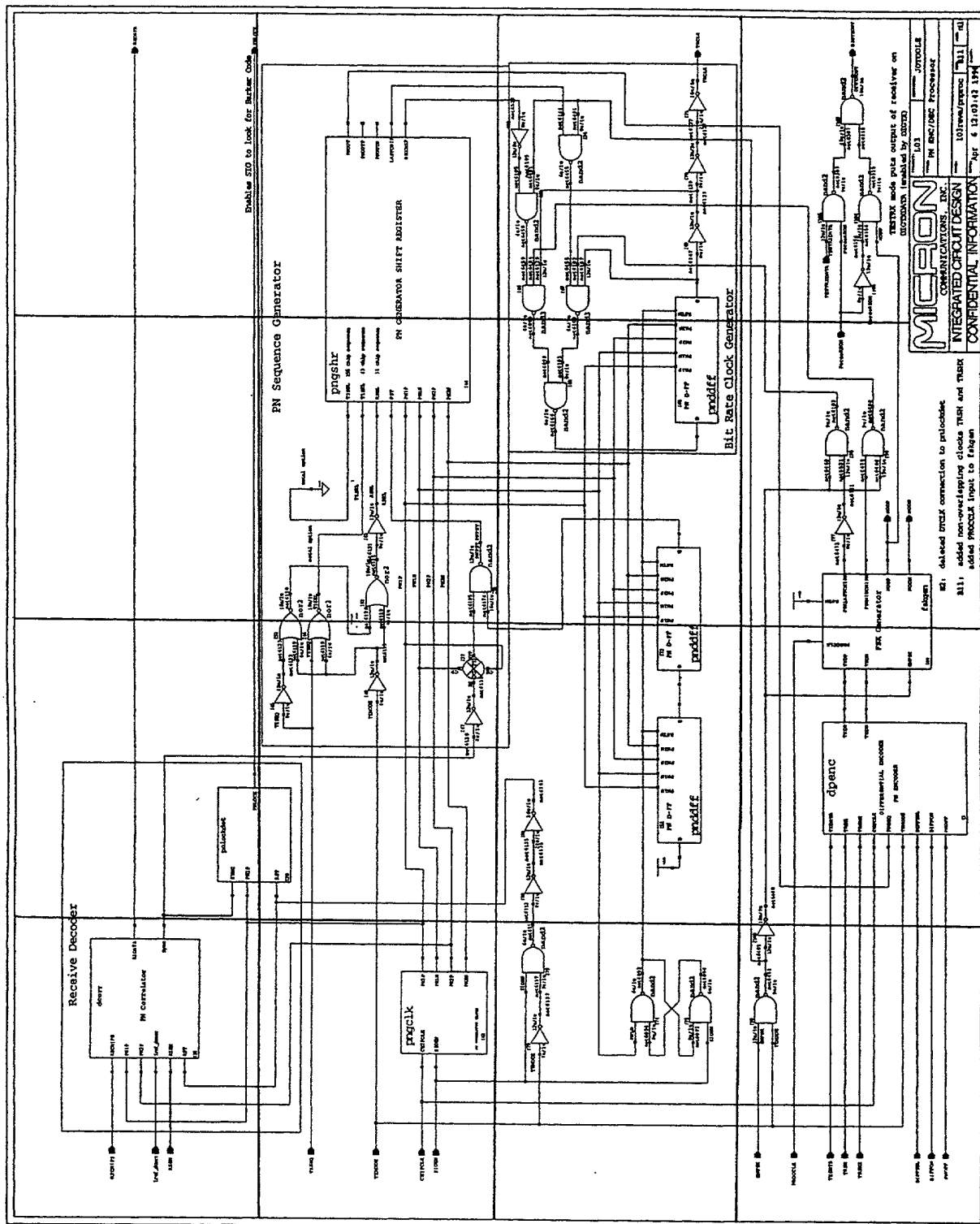
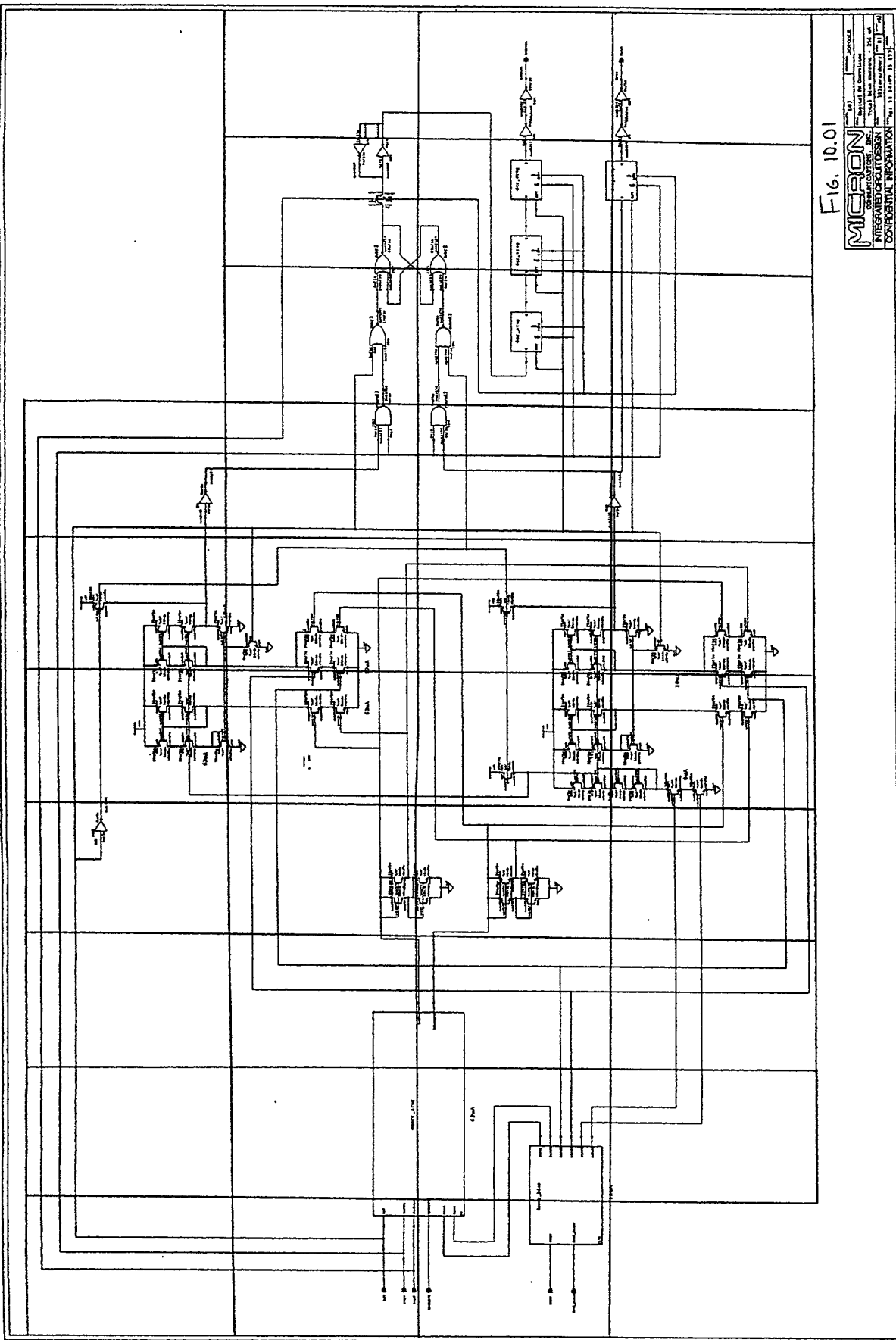


Fig. 10





SECRET



Il n'y a rien de plus

Fig. 10.0101

MICRON CORPORATION  
INTEGRATED CIRCUITS  
FIG. 10.0101  
CONFIDENTIAL INFORMATION

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

FORM 101  
REV. 11-17-1994

DATE: 11-17-1994  
TIME: 11:43:19

USER: JY00004

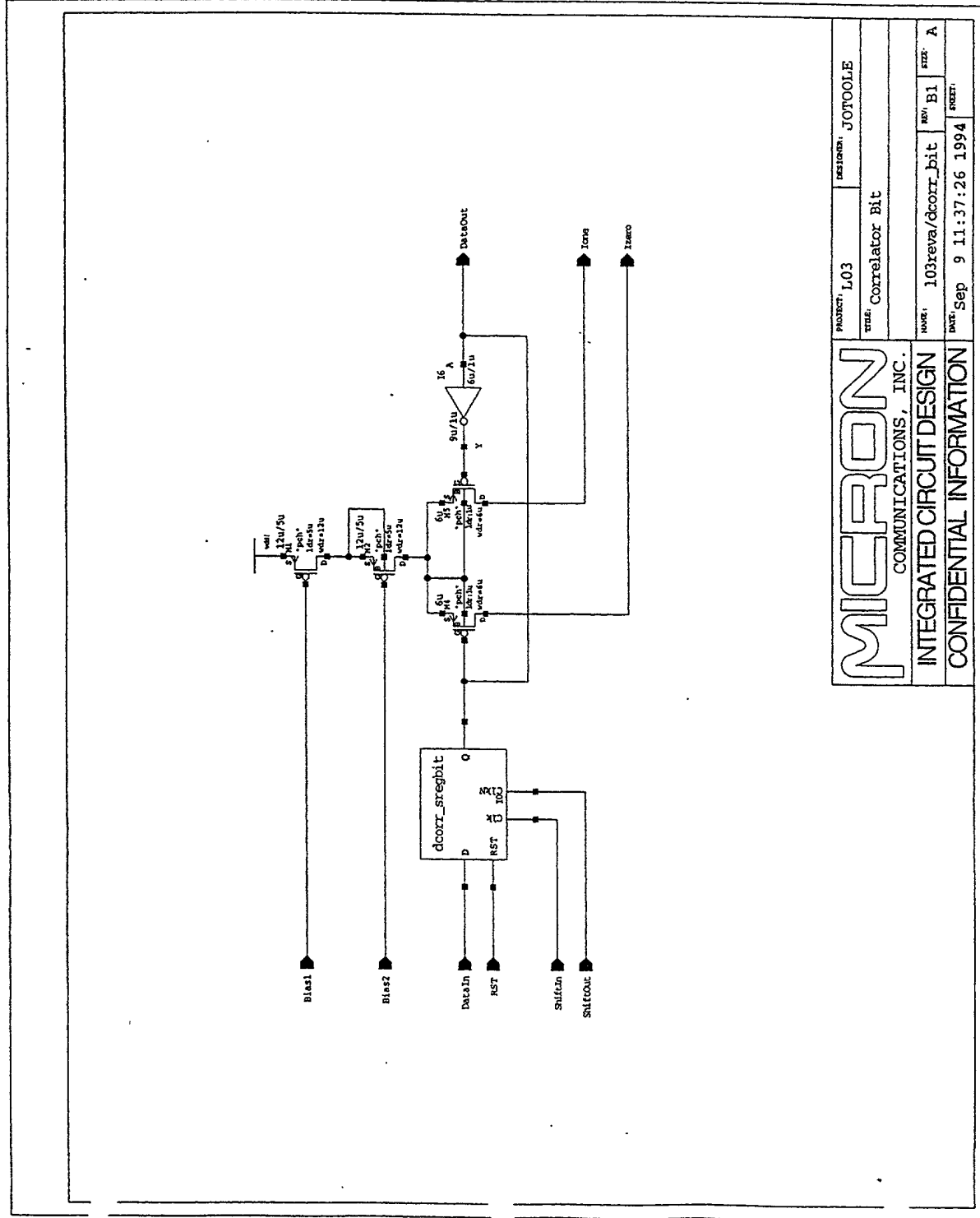


FIG. 10.010101

PROJECT: L03		DESIGNER: JOTOOLE	
TYPE: Correlator Bit		REV: B1	
NAME: 103reva/dcorr_bit		SIZE: A	
DATE: Sep 9 11:37:26 1994		SHEET: 1	

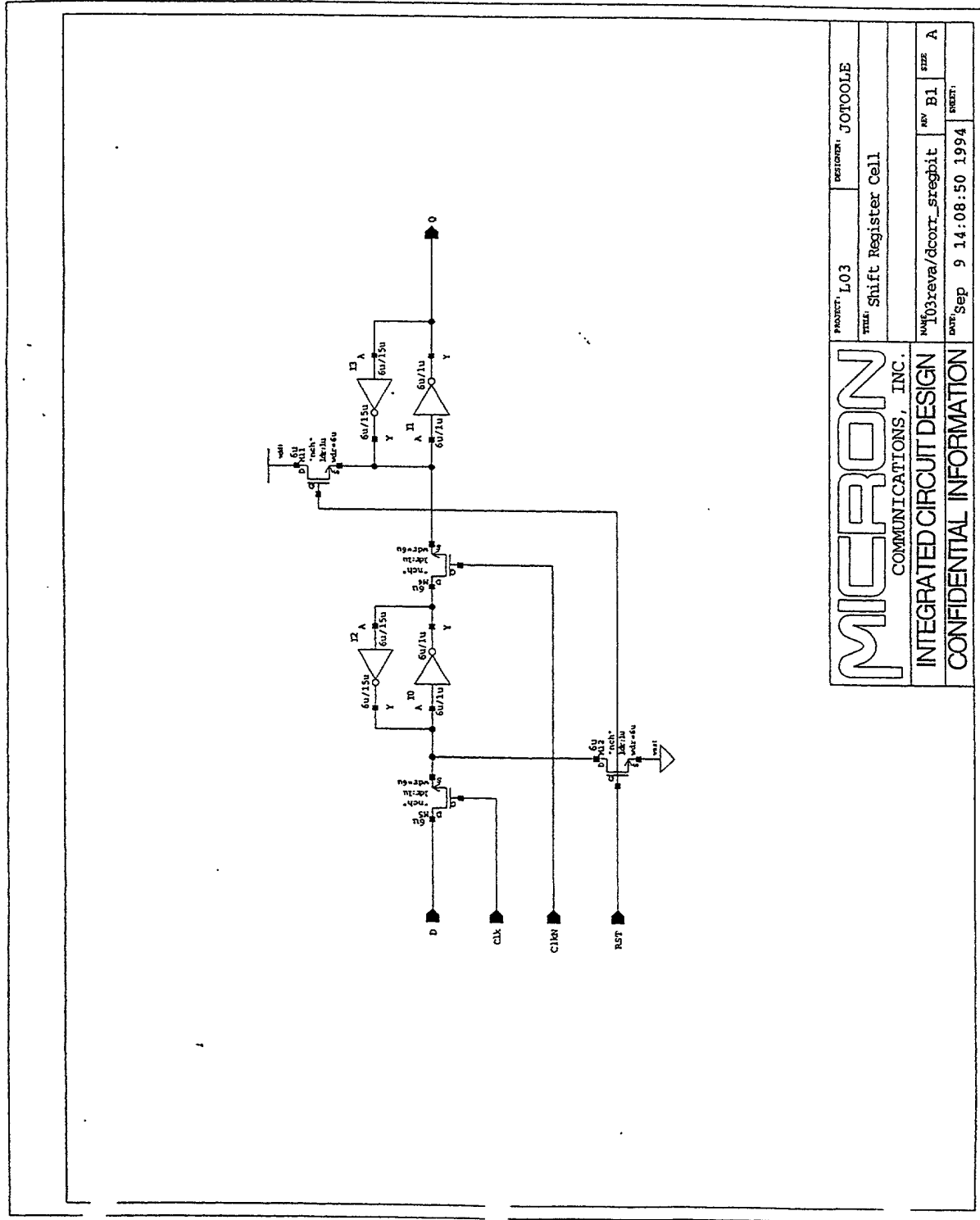
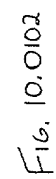


FIG. 10.01010101

MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Shift Register Cell	
INTEGRATED CIRCUIT DESIGN		REV: B1	SIZE: A
CONFIDENTIAL INFORMATION		DATE: Sep 9 14:08:50 1994	SHEET: 1

10.0102AA	10.0102AB	10.0102AC	10.0102AD	10.0102AE	10.0102AF	10.0102AG	10.0102AH	10.0102AI	10.0102AJ	10.0102AK	10.0102AL	10.0102AM	10.0102AN
10.0102BA	10.0102BB	10.0102BC	10.0102BD	10.0102BE	10.0102BF	10.0102BG	10.0102BH	10.0102BI	10.0102BJ	10.0102BK	10.0102BL	10.0102BM	10.0102BN
									10.0102CJ	10.0102CK	10.0102CL	10.0102CM	10.0102CN

2





100.000 100.000

Detects preamble by counting 4 consecutive 0's

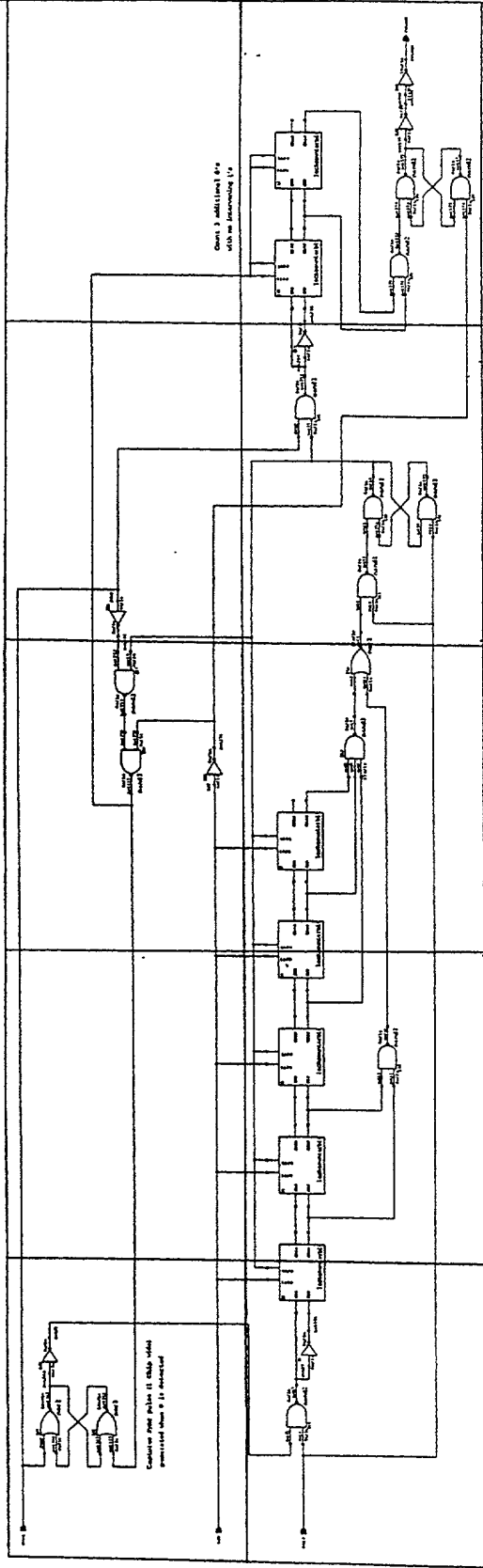
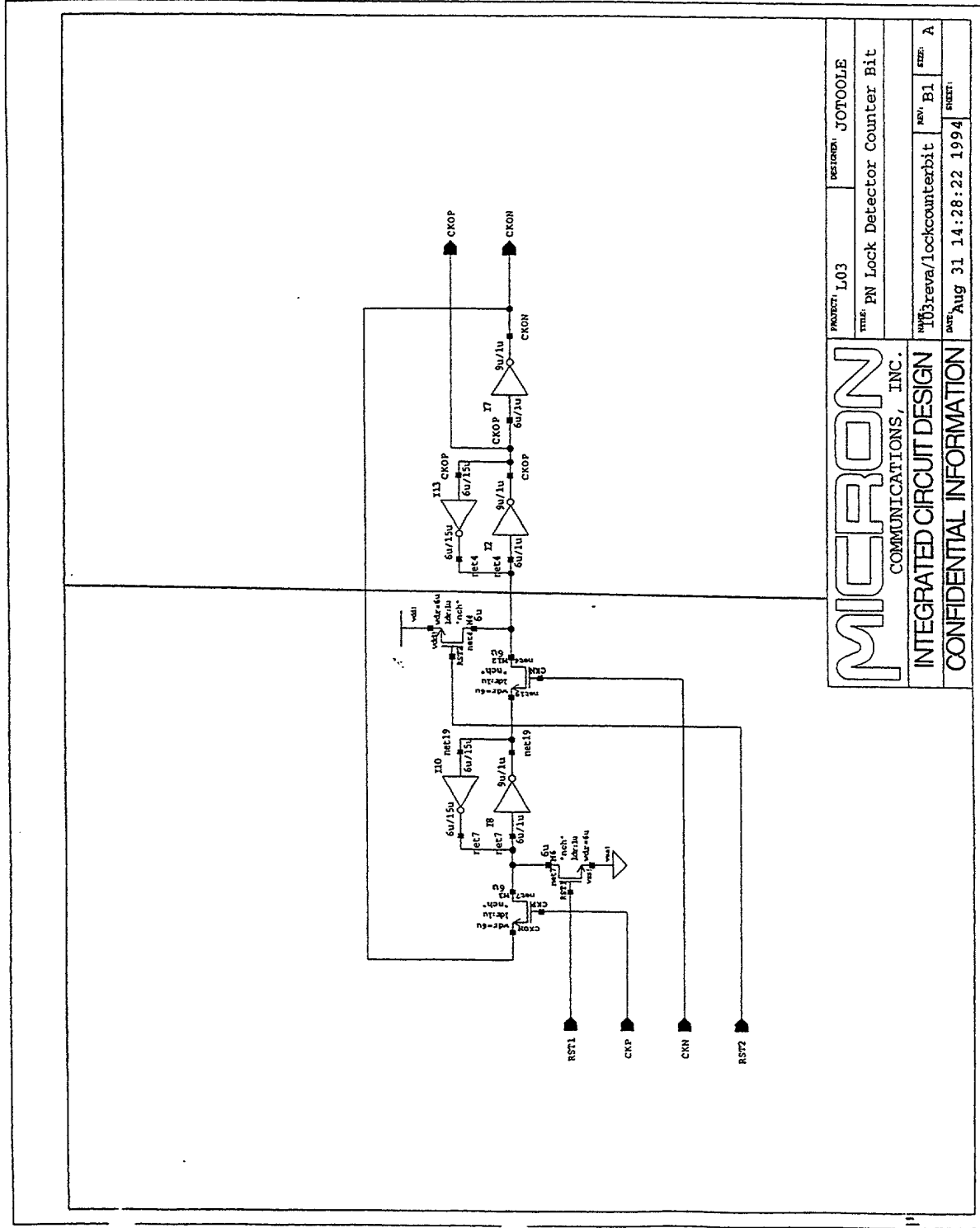


FIG. 10.02

10.0201AB

10.0201AA

10.0201



MICRON		DESIGNER	JOTOOLE
COMMUNICATIONS, INC.		PROJECT	L03
INTEGRATED CIRCUIT DESIGN		TYPE	PN Lock Detector Counter Bit
CONFIDENTIAL INFORMATION		REV	B1
		DATE	Aug 31 14:28:22 1994
		SIZE	A
		NEXT	

FIG. 10.0201

10.03AB

10.03AA

10.03 10.03

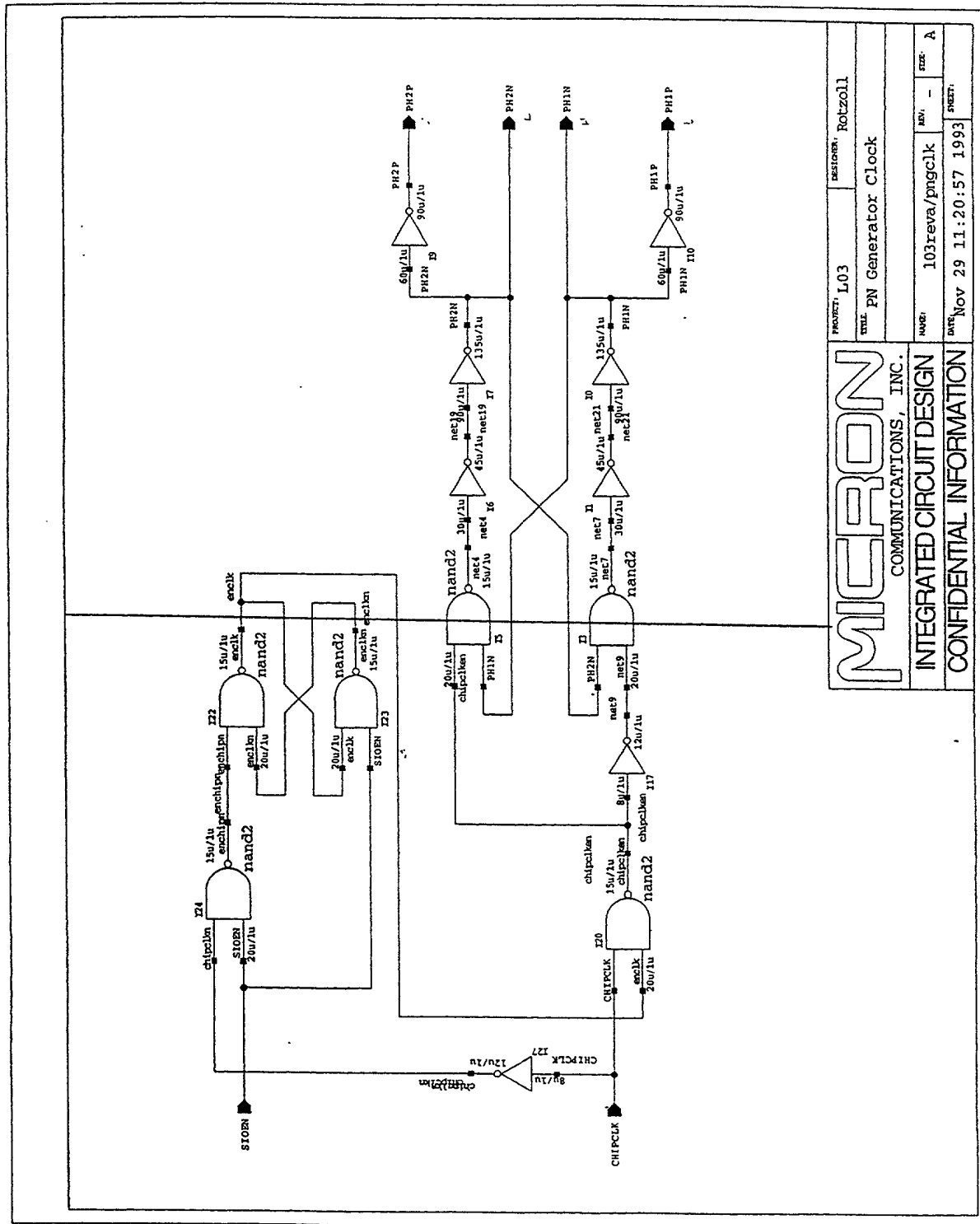


FIG. 10.03

MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: PN Generator Clock	
INTEGRATED CIRCUIT DESIGN		NUMBER: 103reva/pnclk	REV: -
CONFIDENTIAL INFORMATION		DATE: Nov 29 11:20:57 1993	SIZE: A

10.04AA	10.04AB	10.04AC	10.04AD	10.04AE
10.04BA	10.04BB	10.04BC	10.04BD	10.04BE
10.04CA	10.04CB	10.04CC	10.04CD	10.04CE

10.04

SECRET

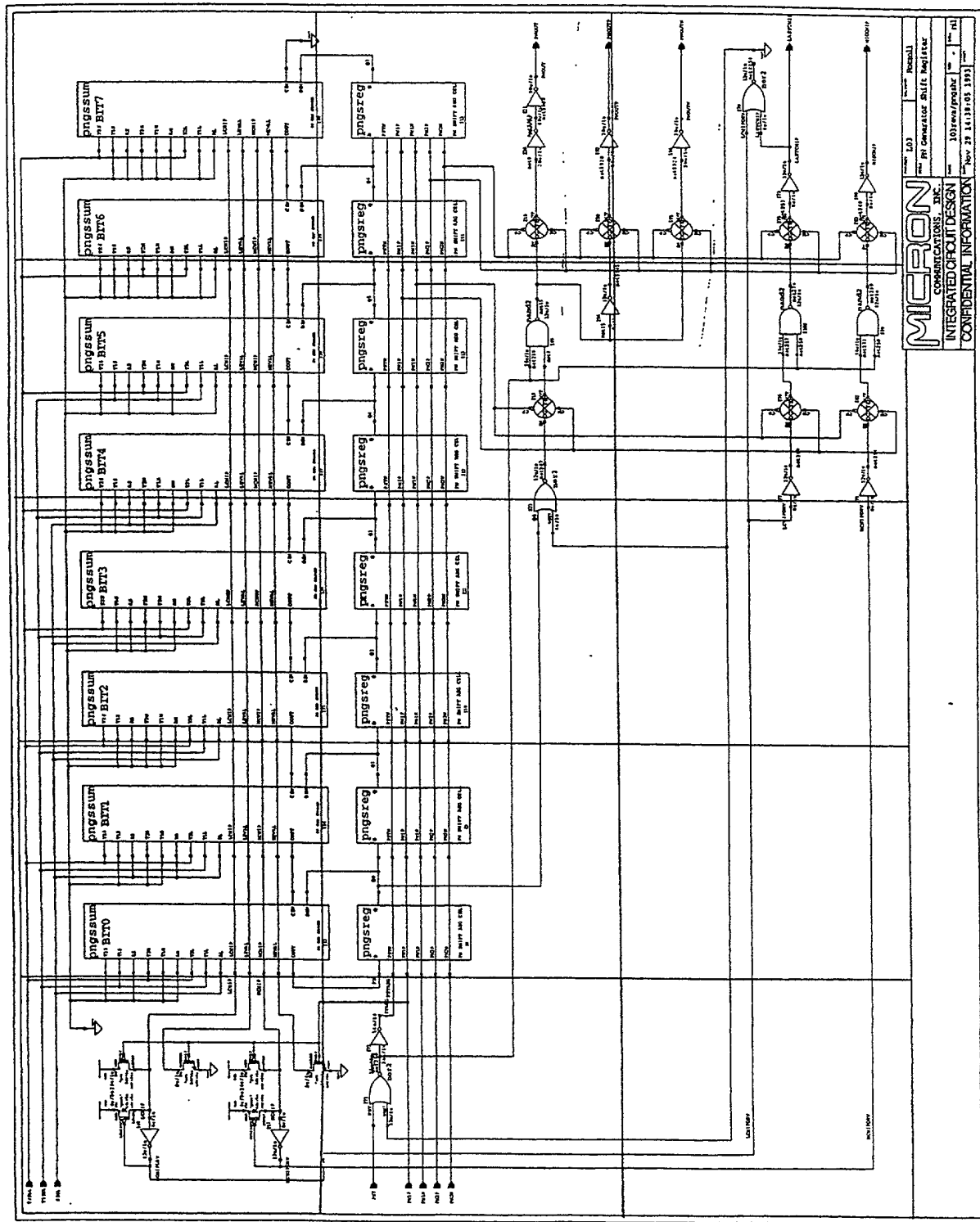


Fig. 10.04

**MICRON**  
COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

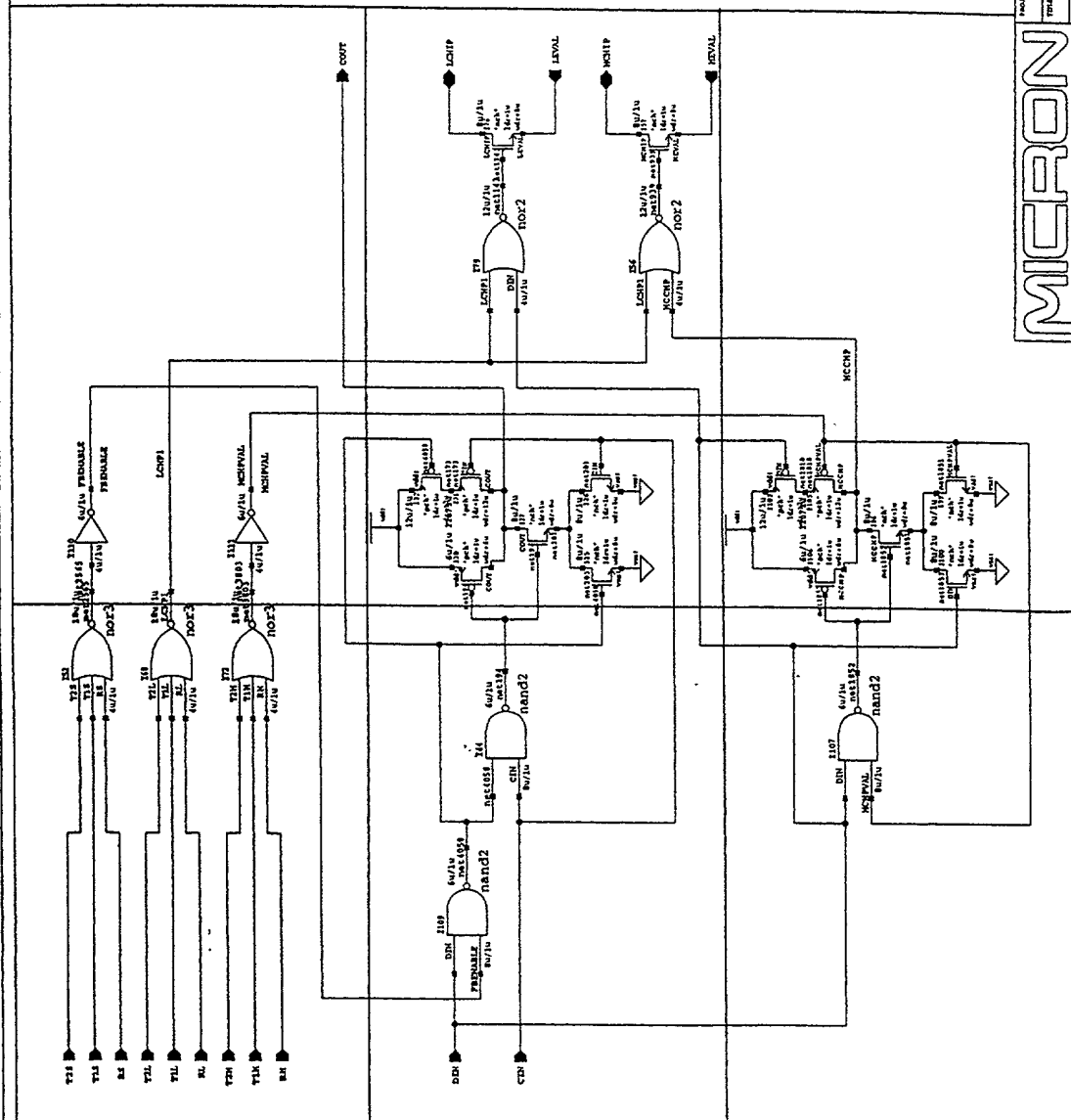
Rev. 1.03  
PR Generator Shift Register  
10/28/83  
Nov 28 14:18:03 1983





10.0402AA	10.0402AB
10.0402BA	10.0402BB
10.0402CA	10.0402CB

10.0402



# NOIR

COMMUNICATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

PROJECT: L03	DESIGNER: Rotzoll
TITLE: PN Gen Shift Reg Summer	

DATE:	Nov 20 21:23:00 1993	SHEET:	
NAME:	103reva/pngssum	NOV:	0116 ml

Parameter	Value	Unit	Source
Age	1.2	yr	1
Weight	70	kg	1
Height	1.75	m	1
Sex	Male		1
Activity	Low		1
Temperature	37	°C	1
Pressure	101.3	kPa	1
Humidity	65	%	1
Light	1000	lx	1
Sound	60	dB	1
Vibration	0.1	m/s²	1
Acceleration	0.1	m/s²	1
Angular Velocity	0.1	rad/s	1
Angular Acceleration	0.1	rad/s²	1
Linear Velocity	0.1	m/s	1
Linear Acceleration	0.1	m/s²	1
Angular Displacement	0.1	rad	1
Linear Displacement	0.1	m	1
Force	0.1	N	1
Pressure	0.1	kPa	1
Temperature	0.1	°C	1
Humidity	0.1	%	1
Light	0.1	lx	1
Sound	0.1	dB	1
Vibration	0.1	m/s²	1
Acceleration	0.1	m/s²	1
Angular Velocity	0.1	rad/s	1
Angular Acceleration	0.1	rad/s²	1
Linear Velocity	0.1	m/s	1
Linear Acceleration	0.1	m/s²	1
Angular Displacement	0.1	rad	1
Linear Displacement	0.1	m	1
Force	0.1	N	1
Pressure	0.1	kPa	1
Temperature	0.1	°C	1
Humidity	0.1	%	1
Light	0.1	lx	1
Sound	0.1	dB	1
Vibration	0.1	m/s²	1
Acceleration	0.1	m/s²	1
Angular Velocity	0.1	rad/s	1
Angular Acceleration	0.1	rad/s²	1
Linear Velocity	0.1	m/s	1
Linear Acceleration	0.1	m/s²	1
Angular Displacement	0.1	rad	1
Linear Displacement	0.1	m	1
Force	0.1	N	1
Pressure	0.1	kPa	1
Temperature	0.1	°C	1
Humidity	0.1	%	1
Light	0.1	lx	1
Sound	0.1	dB	1
Vibration	0.1	m/s²	1
Acceleration	0.1	m/s²	1
Angular Velocity	0.1	rad/s	1
Angular Acceleration	0.1	rad/s²	1
Linear Velocity	0.1	m/s	1
Linear Acceleration	0.1	m/s²	1
Angular Displacement	0.1	rad	1
Linear Displacement	0.1	m	1
Force	0.1	N	1
Pressure	0.1	kPa	1
Temperature	0.1	°C	1
Humidity	0.1	%	1
Light	0.1	lx	1
Sound	0.1	dB	1
Vibration	0.1	m/s²	1
Acceleration	0.1	m/s²	1
Angular Velocity	0.1	rad/s	1
Angular Acceleration	0.1	rad/s²	1
Linear Velocity	0.1	m/s	1
Linear Acceleration	0.1	m/s²	1
Angular Displacement	0.1	rad	1
Linear Displacement	0.1	m	1
Force	0.1	N	1
Pressure	0.1	kPa	1
Temperature	0.1	°C	1
Humidity	0.1	%	1
Light	0.1	lx	1
Sound	0.1	dB	1
Vibration	0.1	m/s²	1
Acceleration	0.1	m/s²	1
Angular Velocity	0.1	rad/s	1
Angular Acceleration	0.1	rad/s²	1
Linear Velocity	0.1	m/s	1
Linear Acceleration	0.1	m/s²	1
Angular Displacement	0.1	rad	1
Linear Displacement	0.1	m	1
Force	0.1	N	1
Pressure	0.1	kPa	1
Temperature	0.1	°C	1
Humidity	0.1	%	1
Light	0.1	lx	1
Sound	0.1	dB	1
Vibration	0.1	m/s²	1
Acceleration	0.1	m/s²	1
Angular Velocity	0.1	rad/s	1
Angular Acceleration	0.1	rad/s²	1
Linear Velocity	0.1	m/s	1
Linear Acceleration	0.1	m/s²	1
Angular Displacement	0.1	rad	1
Linear Displacement	0.1	m	1
Force	0.1	N	1
Pressure	0.1	kPa	1
Temperature	0.1	°C	1
Humidity	0.1	%	1
Light	0.1	lx	

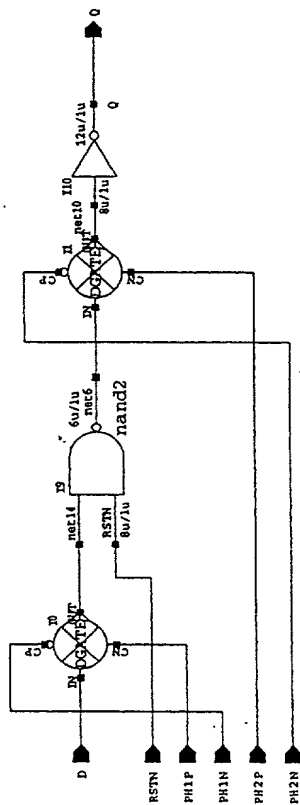


Fig. 10.05

<b>MICRON</b>	
COMMUNICATIONS, INC.	
INTEGRATED CIRCUIT DESIGN	
CONFIDENTIAL INFORMATION	
PROJECT: L03	DESIGNER: Rotzoll
TITLE: EN Controller D Flip-Flop	
NAME: 103reva/pndiff	REV: -
DATE: Nov 26 18:12:59 1993	SHEET: A

11.11.11

811 brought the accompanying clerks together and made

10.07AA 10.07AB 10.07AC 10.07AD

10.07AA	10.07AB	10.07AC	10.07AD
10.07BA	10.07BB	10.07BC	10.07BD
10.07CA	10.07CB	10.07CC	10.07CD

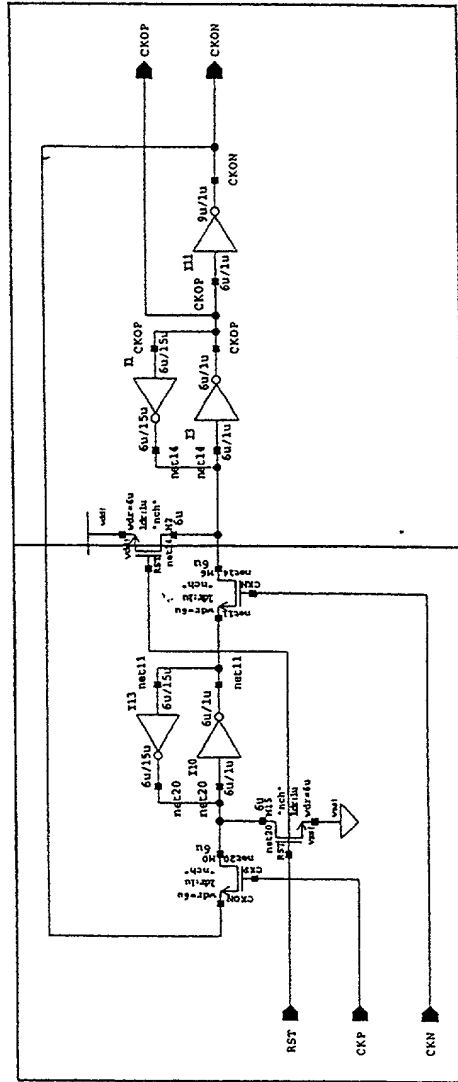
10.07 10.07 10.07 10.07





10.0701AA	10.0701AB
-----------	-----------

FIG. 10.0701



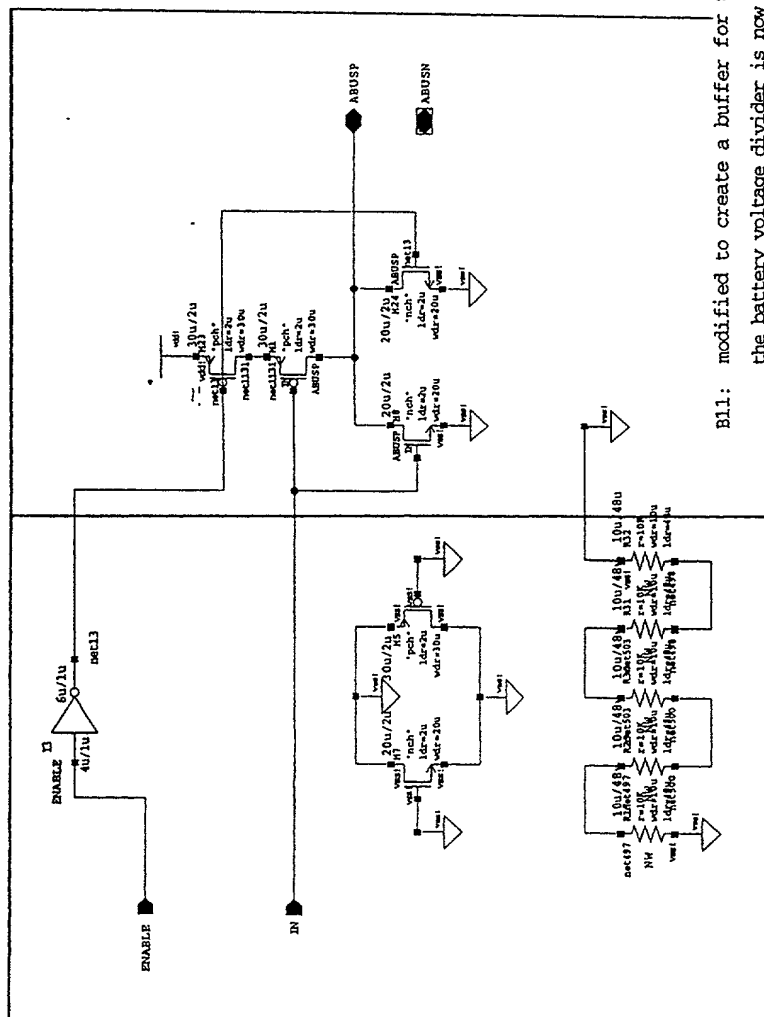
MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TYPE: FSK Counter Bit	
INTEGRATED CIRCUIT DESIGN		Same as wuabot_cbit	
CONFIDENTIAL INFORMATION		NAME: 103reva/eskcbt	REV: B1
		DATE: Apr 17 15:42:44 1995	SIZE: A
			SHEET:

FIG. 10.0701

11AB

11AA

II II II



B11: modified to create a buffer for the opamp output  
the battery voltage divider is now part of tsn

# 2002

# INTEGRATED CIRCUIT DESIGN

**CONFIDENTIAL INFORMATION**

PROJECT: L03	DESIGNER: JOTOOLE
--------------	-------------------

NAME:	Battery Analog I/O Buffer
-------	---------------------------

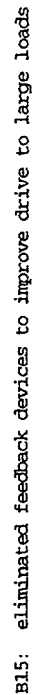
103reva/batala	REV B11	SIZE A
----------------	---------	--------

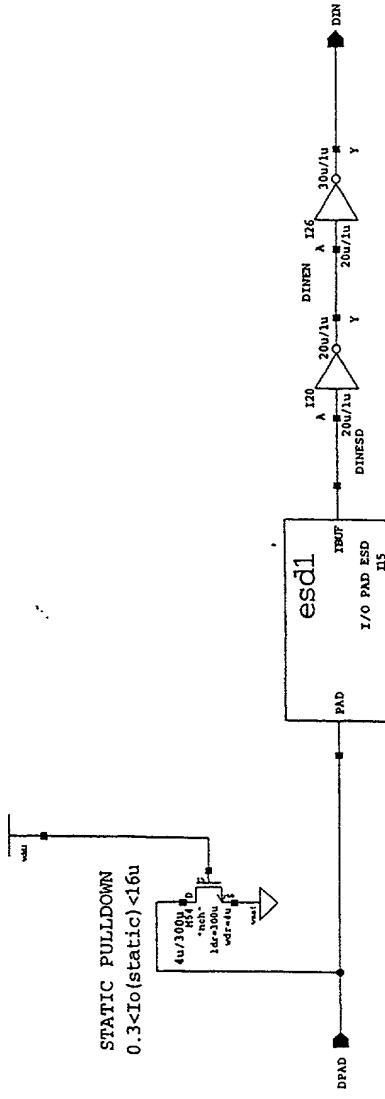
DATE: Apr 8 10:19:56 1996

12AB

12AA

1111 112

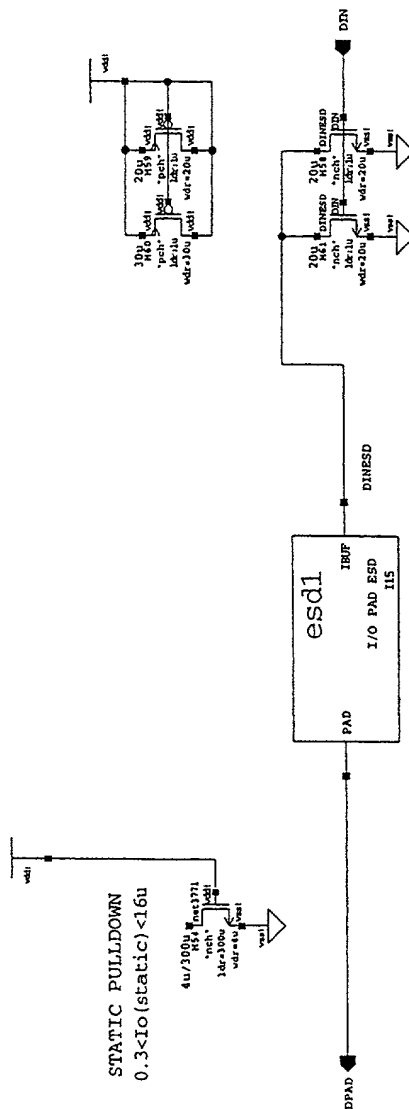




MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Digital Input Pad Buffer	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/paddigin	REV: B1
CONFIDENTIAL INFORMATION		DATE: Apr 11 11:10:35 1995	SIZE: A

FIG. 13

Variable	Mean		SD		t		p	
	Control	Case	Control	Case	Control	Case	Control	Case
Age	30.5	30.5	1.2	1.2	0.0	0.0	0.999	0.999
Gender	100	100	0	0	0.0	0.0	0.999	0.999
Education	12.5	12.5	1.0	1.0	0.0	0.0	0.999	0.999
Occupation	1.5	1.5	0.5	0.5	0.0	0.0	0.999	0.999
Marital status	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Religion	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Income	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Health status	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Family size	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental education	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental occupation	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental marital status	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental religion	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental income	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental health status	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental family size	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental education	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental occupation	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental marital status	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental religion	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental income	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental health status	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental family size	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental education	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental occupation	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental marital status	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental religion	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental income	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental health status	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental family size	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental parental education	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental parental occupation	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental parental marital status	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental parental religion	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental parental income	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental parental health status	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental parental family size	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental parental parental education	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental parental parental occupation	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental parental parental marital status	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental parental parental religion	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental parental parental income	1.0	1.0	0.0	0.0	0.0	0.0	0.999	0.999
Parental parental parental parental parental health status	1.0	1.0						



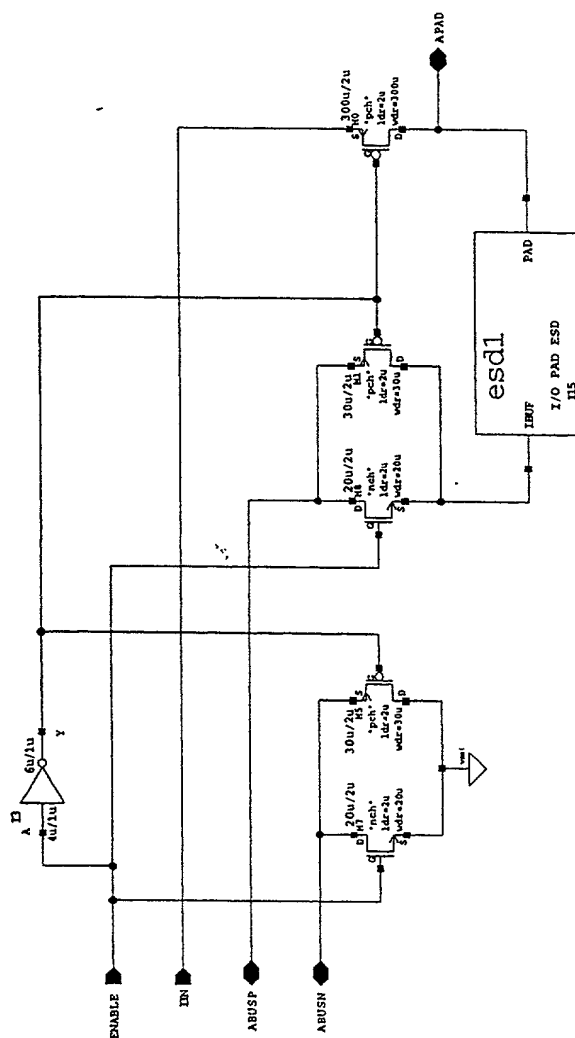
B13: new cell for WAKEUP\* output

MICRON		PROJECT: L03	DESIGNER: JOTOOLE
COMMUNICATIONS, INC.		TITLE: Digital Input Pad Buffer	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/paddigin2	REV: B13
CONFIDENTIAL INFORMATION		DATE: May 24 18:28:29 1996	

Fig 13.5



Variable	Mean	Standard deviation	Minimum	Maximum
Age	34.5	10.2	22	55
Gender	0.5	0.5	0	1
Marital status	0.6	0.5	0	1
Education	12.5	1.5	10	15
Income	1500	500	1000	2500
Health status	0.8	0.2	0	1
Smoking status	0.3	0.5	0	1
Alcohol consumption	0.2	0.4	0	1
Exercise frequency	0.5	0.5	0	1
Stress level	0.7	0.3	0	1
Sleep quality	0.6	0.4	0	1
Work satisfaction	0.5	0.5	0	1
Life satisfaction	0.7	0.3	0	1
Depression score	0.4	0.5	0	1
Anxiety score	0.3	0.4	0	1
Resilience score	0.6	0.4	0	1
Optimism score	0.5	0.5	0	1
Gratitude score	0.6	0.4	0	1
Forgiveness score	0.5	0.5	0	1
Compassion score	0.4	0.4	0	1
Kindness score	0.5	0.5	0	1
Generosity score	0.4	0.4	0	1
Patience score	0.5	0.5	0	1
Self-control score	0.4	0.4	0	1
Emotional stability score	0.6	0.4	0	1
Empathy score	0.5	0.5	0	1
Empowerment score	0.4	0.4	0	1
Self-efficacy score	0.5	0.5	0	1
Resilience score	0.6	0.4	0	1
Optimism score	0.5	0.5	0	1
Gratitude score	0.6	0.4	0	1
Forgiveness score	0.5	0.5	0	1
Compassion score	0.4	0.4	0	1
Kindness score	0.5	0.5	0	1
Generosity score	0.4	0.4	0	1
Patience score	0.5	0.5	0	1
Self-control score	0.4	0.4	0	1
Emotional stability score	0.6	0.4	0	1
Empathy score	0.5	0.5	0	1
Empowerment score	0.4	0.4	0	1
Self-efficacy score	0.5	0.5	0	1
Resilience score	0.6	0.4	0	1
Optimism score	0.5	0.5	0	1
Gratitude score	0.6	0.4	0	1
Forgiveness score	0.5	0.5	0	1
Compassion score	0.4	0.4	0	1
Kindness score	0.5	0.5	0	1
Generosity score	0.4	0.4	0	1
Patience score	0.5	0.5	0	1
Self-control score	0.4	0.4	0	1
Emotional stability score	0.6	0.4	0	1
Empathy score	0.5	0.5	0	1
Empowerment score	0.4	0.4	0	1
Self-efficacy score	0.5	0.5	0	1
Resilience score	0.6	0.4	0	1
Optimism score	0.5	0.5	0	1
Gratitude score	0.6	0.4	0	1
Forgiveness score	0.5	0.5	0	1
Compassion score	0.4	0.4	0	1
Kindness score	0.5	0.5	0	1
Generosity score	0.4	0.4	0	1
Patience score	0.5	0.5	0	1
Self-control score	0.4	0.4	0	1
Emotional stability score	0.6	0.4	0	1
Empathy score	0.5	0.5	0	1
Empowerment score	0.4	0.4	0	1
Self-efficacy score	0.5	0.5	0	1
Resilience score	0.6	0.4	0	1
Optimism score	0.5	0.5	0	1
Gratitude score	0.6	0.4	0	1
Forgiveness score	0.5	0.5	0	1
Compassion score	0.4	0.4	0	1
Kindness score	0.5	0.5	0	1
Generosity score	0.4	0.4	0	1
Patience score	0.5	0.5	0	1
Self-control score	0.4	0.4	0	1
Emotional stability score	0.6	0.4	0	1
Empathy score	0.5	0.5	0	1
Empowerment score	0.4	0.4	0	1
Self-efficacy score	0.5	0.5	0	1
Resilience score	0.6	0.4	0	1
Optimism score	0.5	0.5	0	1
Gratitude score	0.6	0.4	0	1
Forgiveness score	0.			

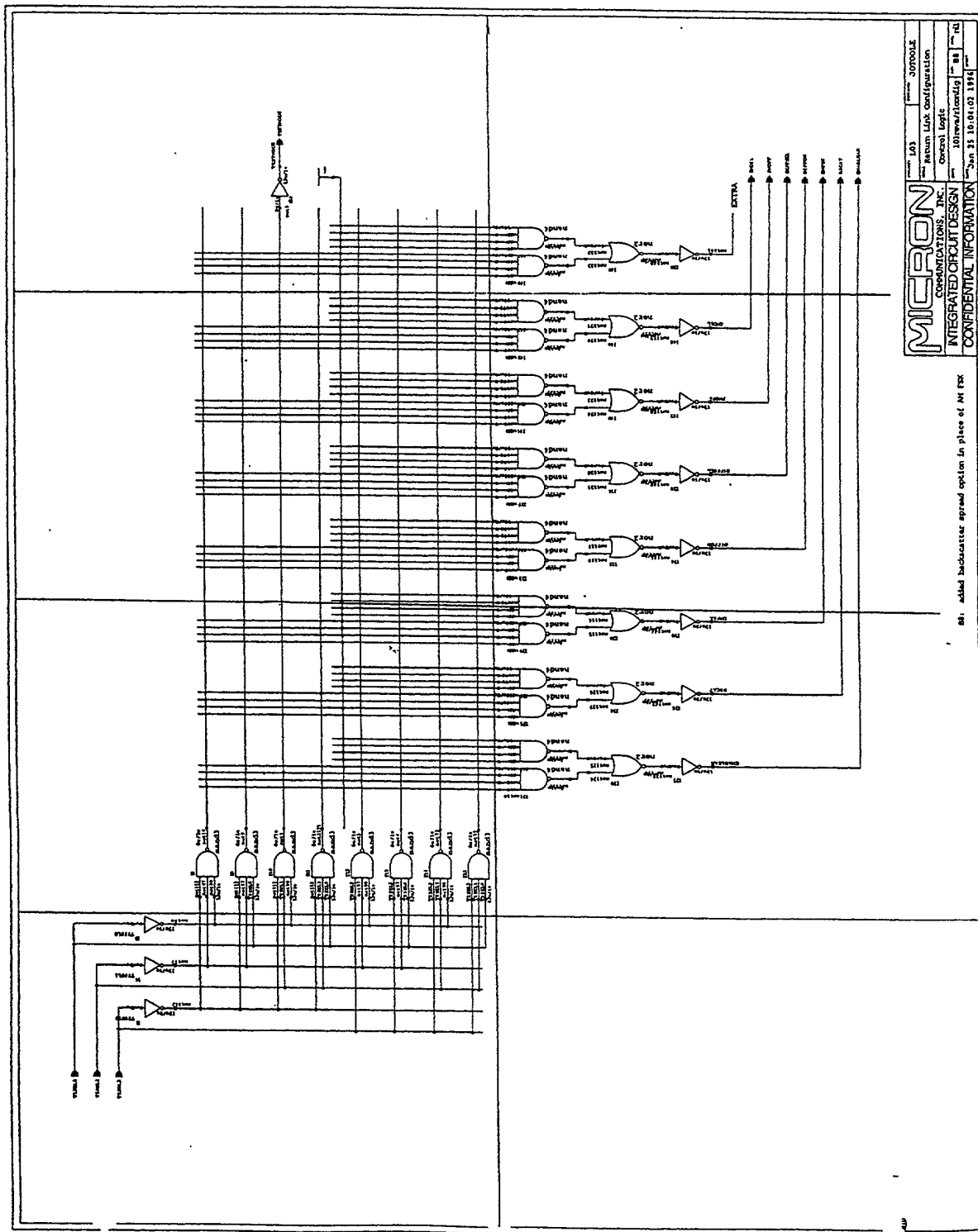


MICRON		PROJECT: L03	DESIGNER: Rotzoll
COMMUNICATIONS, INC.		TITLE: Analog I/O Pad Buffer	
INTEGRATED CIRCUIT DESIGN		NAME: 103reva/padalg	REV: -
CONFIDENTIAL INFORMATION		DATE: Dec 12 21:55:41 1993	SHEET: A

F16.19

15AA	15AB	15AC	15AD	15BC
	15BA	15BB		

CONFIDENTIAL



MICRON  
COMPUTATIONS, INC.  
INTEGRATED CIRCUIT DESIGN  
CONFIDENTIAL INFORMATION

101 1070000  
Return Line Configuration  
Control Logic  
1070000000  
Date 10/04/03 1994

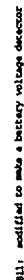
add: added backwater spread option in place of AMR

FIG. 15

16AA	16AB	16AC	16AD	16AE	16AF	16AG	16AH
16BA	16BB	16BC	16BD	16BE	16BF	16BG	16BH
16CA	16CB	16CC	16CD	16CE	16CF	16CG	16CH
16DA	16DB	16DC	16DD	16DE	16DF	16DG	16DH
16EA	16EB	16EC	16ED	16EE	16EF	16EG	16EH



Fig. 16

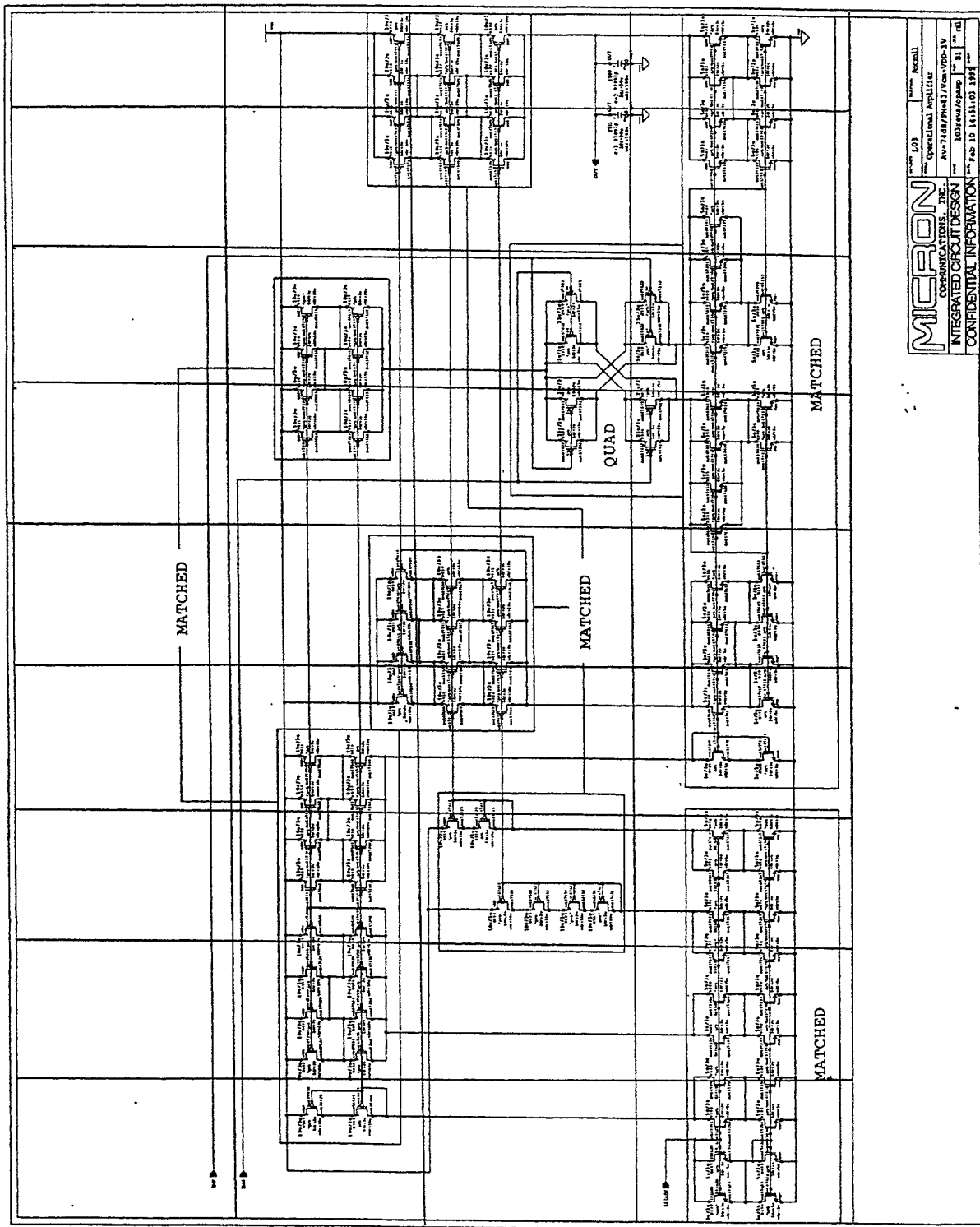


“Code” entered

16.01AA	16.01AB	16.01AC	16.01AD	16.01AE	16.01AF	16.01AG		
16.01BA	16.01BB	16.01BC	16.01BD	16.01BE	16.01BF	16.01BG	16.01BH	16.01BI
16.01CA	16.01CB	16.01CC	16.01CD	16.01CE	16.01CF	16.01CG	16.01CH	16.01CI
16.01DA	16.01DB	16.01DC	16.01DD	16.01DE	16.01DF	16.01DG	16.01DH	16.01DI

II II II II II II

Fig. 16.01



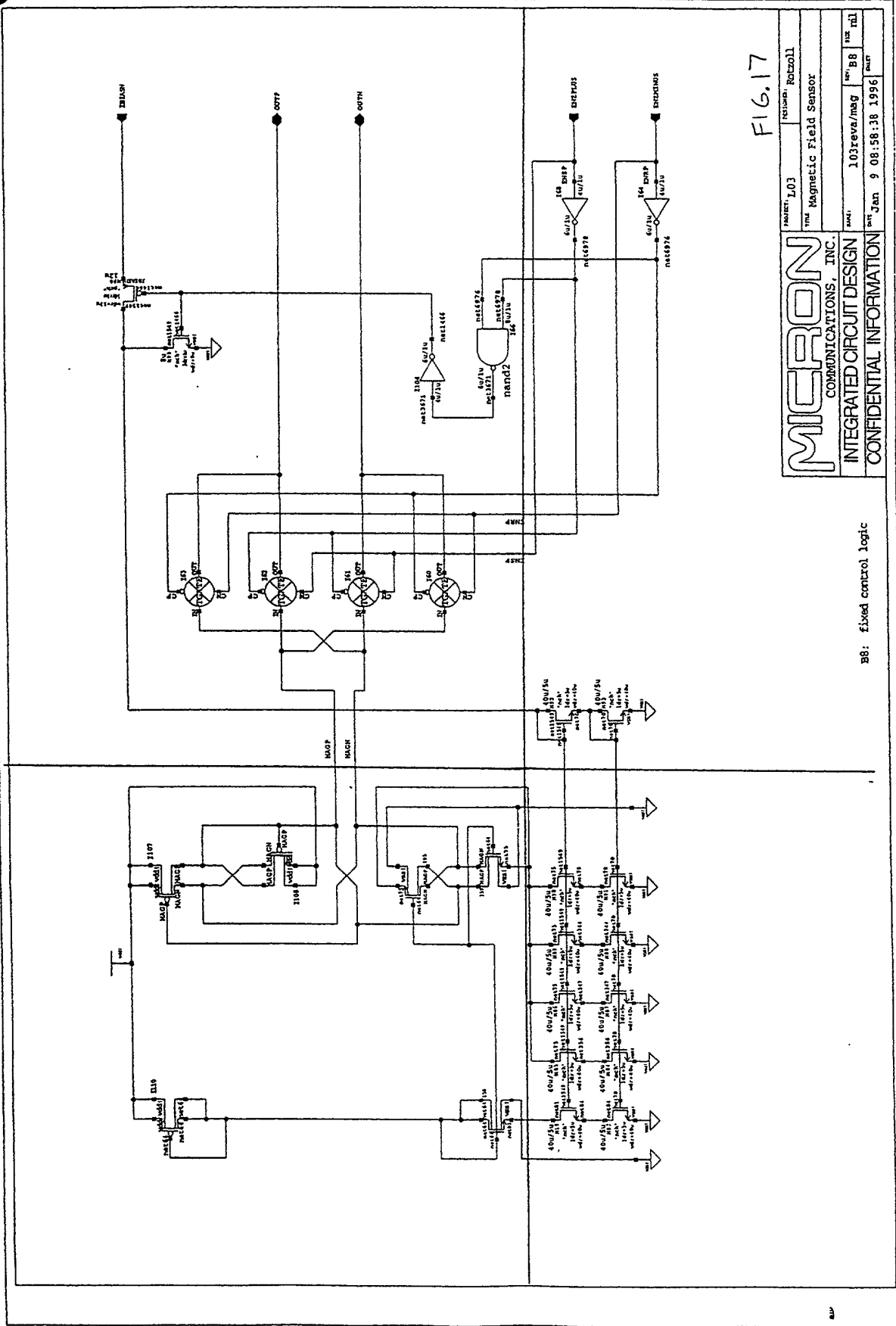
ALL INFORMATION CONTAINED  
HEREIN IS UNCLASSIFIED

17AA	17AB
17BA	17BB

SECRET



FIG. 17



MICRON		INTEGRATED CIRCUIT DESIGN	CONFIDENTIAL INFORMATION	
MODEL	L03	TYPE	Magnetic Field Sensor	
REV	103revA/mag	DATE	Jan 9 08:58:38 1996	
REV	B8	DATE	Jan 9 08:58:38 1996	

18AB

18AA

EX-18B

600220 23022200

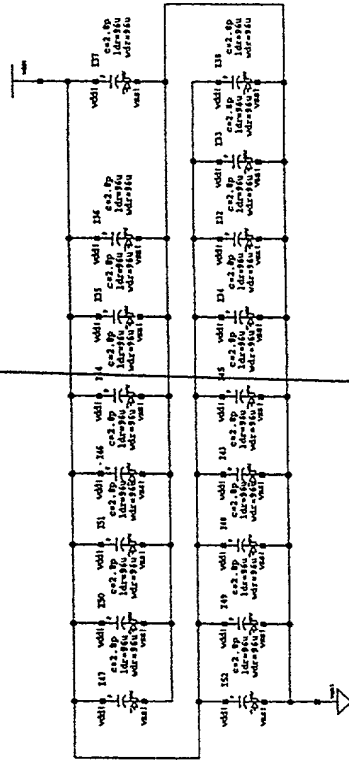


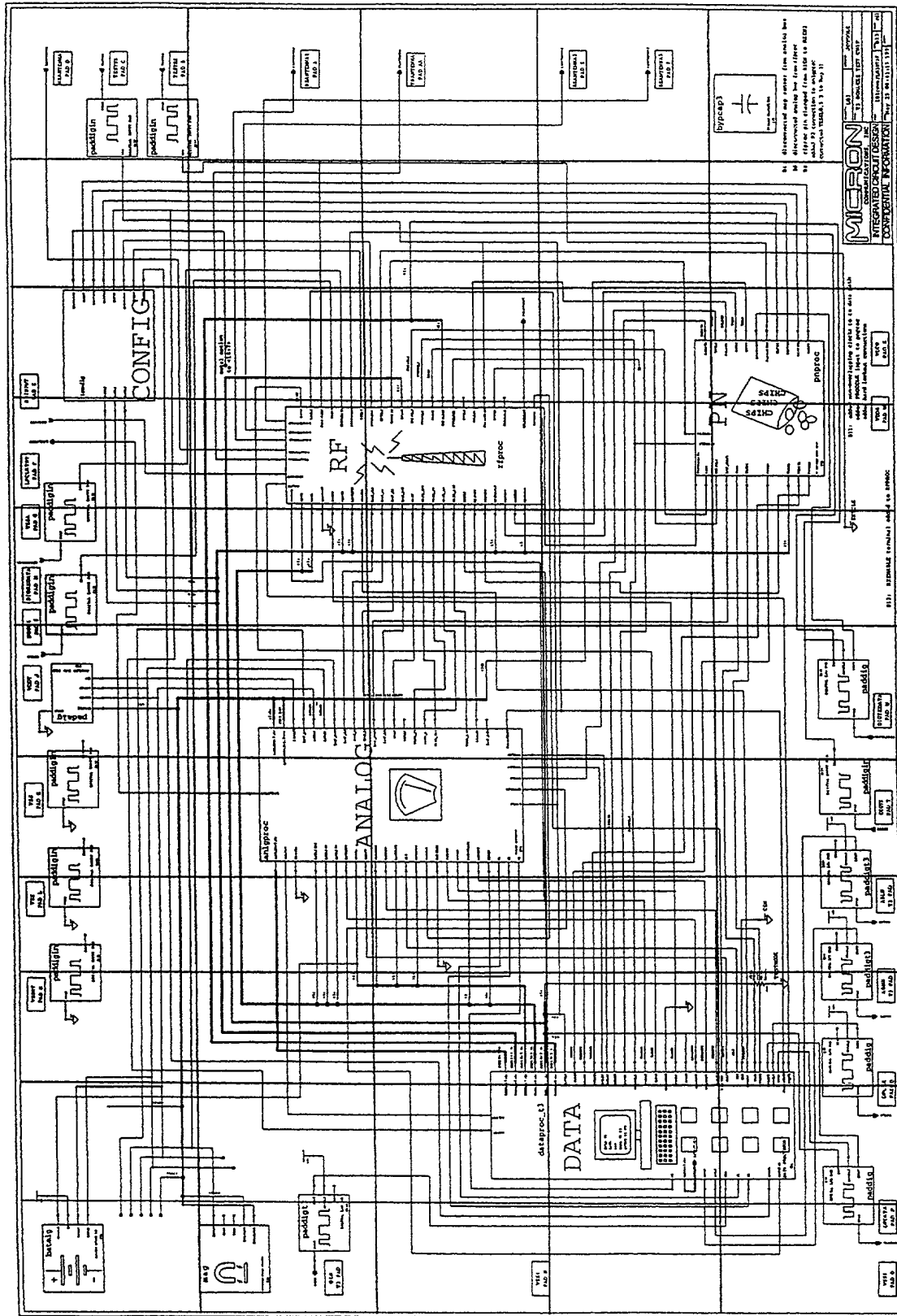
FIG. 18

MICRON		PROJECT	103	NATIONAL	JOTOOLE
COMMUNICATIONS, INC.		TYPE	Chip Bypass Capacitor		
INTEGRATED CIRCUIT DESIGN		CT=	pF		
CONFIDENTIAL INFORMATION		DATE	103rev2/bypcap3	REV.	B2
		DATE	Jul 28 17:43:25 1995	DATE	

B2: deleted one cap

Ex II 61

FIG. 19.AA-EK

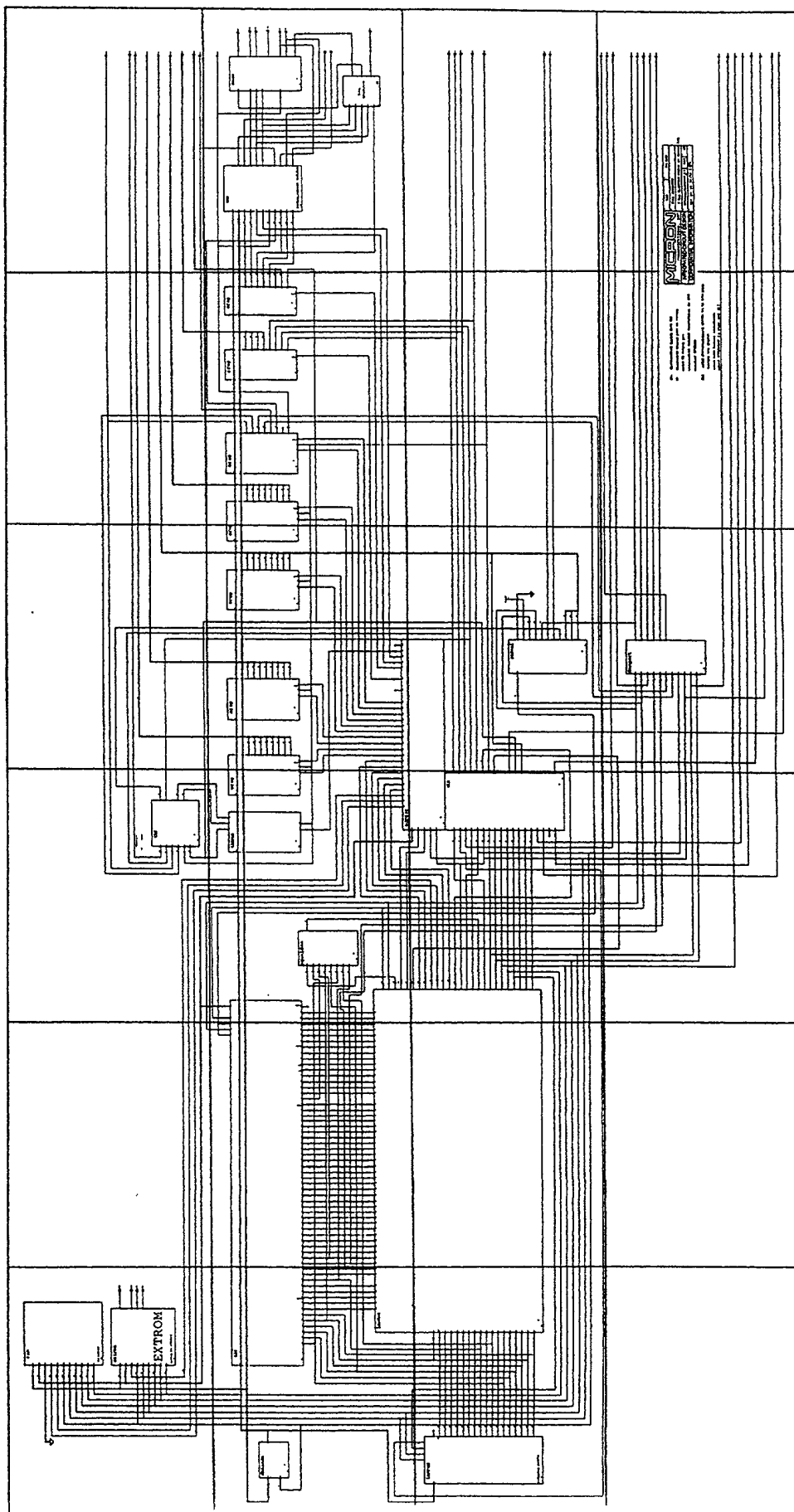


20AA	20AB	20AC	20AD	20AE	20AF
20BA	20BB	20BC	20BD	20BE	20BF
20CA	20CB	20CC	20CD	20CE	20CF
		20DC	20DD	20DE	20DF

IT 11 09 2000

2000 09 11 11 09 2000

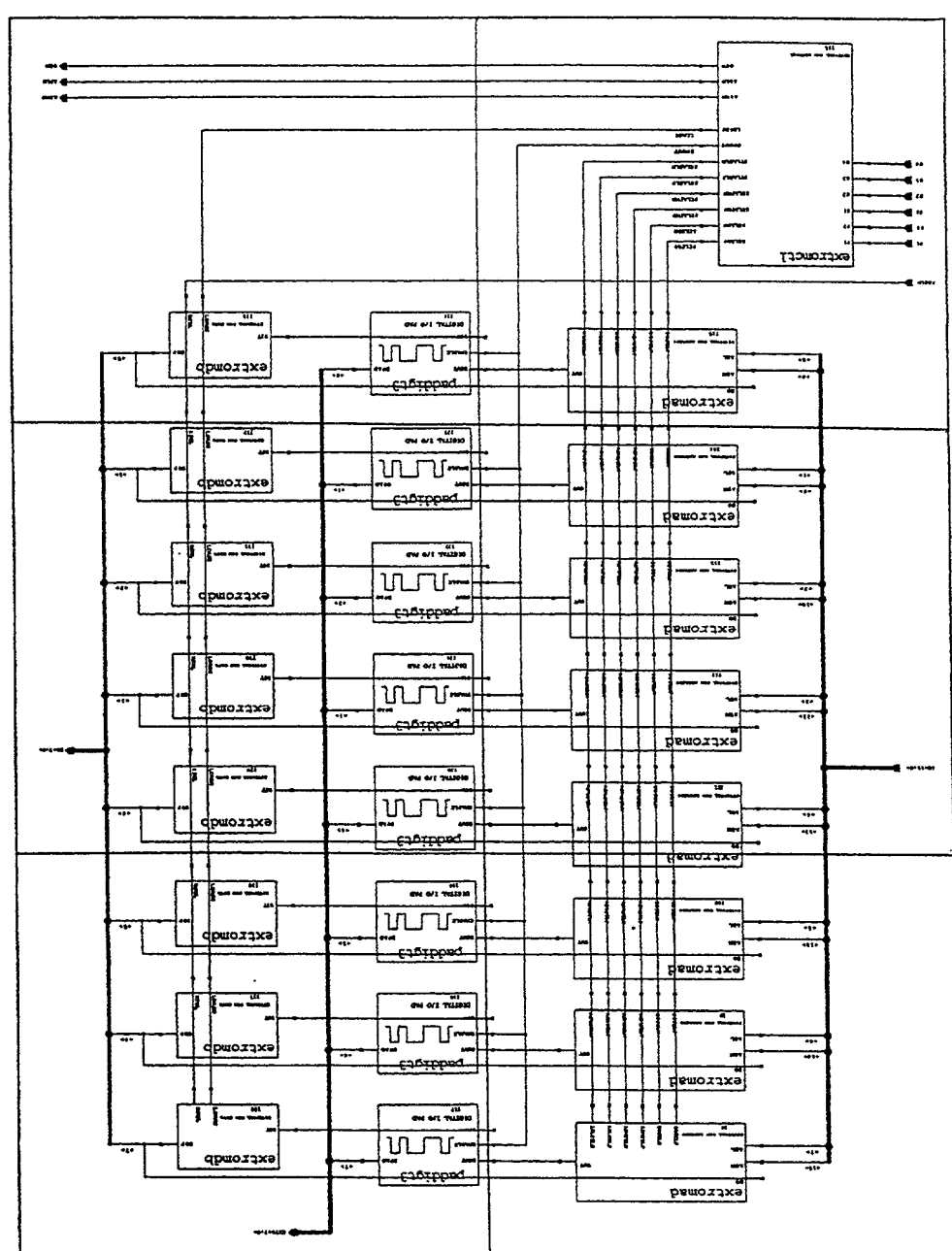
FIG. 20



20.01AA	20.01AB
20.01BA	20.01BB
20.01CA	20.01CB

II 20.01





Year	Total population		Urban population		Rural population		Total population		Urban population		Rural population	
	Population	Density	Population	Density	Population	Density	Population	Density	Population	Density	Population	Density
1950	10,000,000	100	5,000,000	100	5,000,000	100	10,000,000	100	5,000,000	100	5,000,000	100
1955	11,000,000	110	5,500,000	110	5,500,000	110	11,000,000	110	5,500,000	110	5,500,000	110
1960	12,000,000	120	6,000,000	120	6,000,000	120	12,000,000	120	6,000,000	120	6,000,000	120
1965	13,000,000	130	6,500,000	130	6,500,000	130	13,000,000	130	6,500,000	130	6,500,000	130
1970	14,000,000	140	7,000,000	140	7,000,000	140	14,000,000	140	7,000,000	140	7,000,000	140
1975	15,000,000	150	7,500,000	150	7,500,000	150	15,000,000	150	7,500,000	150	7,500,000	150
1980	16,000,000	160	8,000,000	160	8,000,000	160	16,000,000	160	8,000,000	160	8,000,000	160
1985	17,000,000	170	8,500,000	170	8,500,000	170	17,000,000	170	8,500,000	170	8,500,000	170
1990	18,000,000	180	9,000,000	180	9,000,000	180	18,000,000	180	9,000,000	180	9,000,000	180
1995	19,000,000	190	9,500,000	190	9,500,000	190	19,000,000	190	9,500,000	190	9,500,000	190
2000	20,000,000	200	10,000,000	200	10,000,000	200	20,000,000	200	10,000,000	200	10,000,000	200
2005	21,000,000	210	10,500,000	210	10,500,000	210	21,000,000	210	10,500,000	210	10,500,000	210
2010	22,000,000	220	11,000,000	220	11,000,000	220	22,000,000	220	11,000,000	220	11,000,000	220
2015	23,000,000	230	11,500,000	230	11,500,000	230	23,000,000	230	11,500,000	230	11,500,000	230
2020	24,000,000	240	12,000,000	240	12,000,000	240	24,000,000	240	12,000,000	240	12,000,000	240
2025	25,000,000	250	12,500,000	250	12,500,000	250	25,000,000	250	12,500,000	250	12,500,000	250
2030	26,000,000	260	13,000,000	260	13,000,000	260	26,000,000	260	13,000,000	260	13,000,000	260
2035	27,000,000	270	13,500,000	270	13,500,000	270	27,000,000	270	13,500,000	270	13,500,000	270
2040	28,000,000	280	14,000,000	280	14,000,000	280	28,000,000	280	14,000,000	280	14,000,000	280
2045	29,000,000	290	14,500,000	290	14,500,000	290	29,000,000	290	14,500,000	290	14,500,000	290
2050	30,000,000	300	15,000,000	300	15,000,000	300	30,000,000	300	15,000,000	300	15,000,000	300
2055	31,000,000	310	15,500,000	310	15,500,000	310	31,000,000	310	15,500,000	310	15,500,000	310
2060	32,000,000	320	16,000,000	320	16,000,000	320	32,000,000	320	16,000,000	320	16,000,000	320
2065	33,000,000	330	16,500,000	330	16,500,000	330	33,000,000					

20.0101AA 20.0101AB

20.0101AA	20.0101AB
20.0101BA	20.0101BB

20.0101AA 20.0101AB

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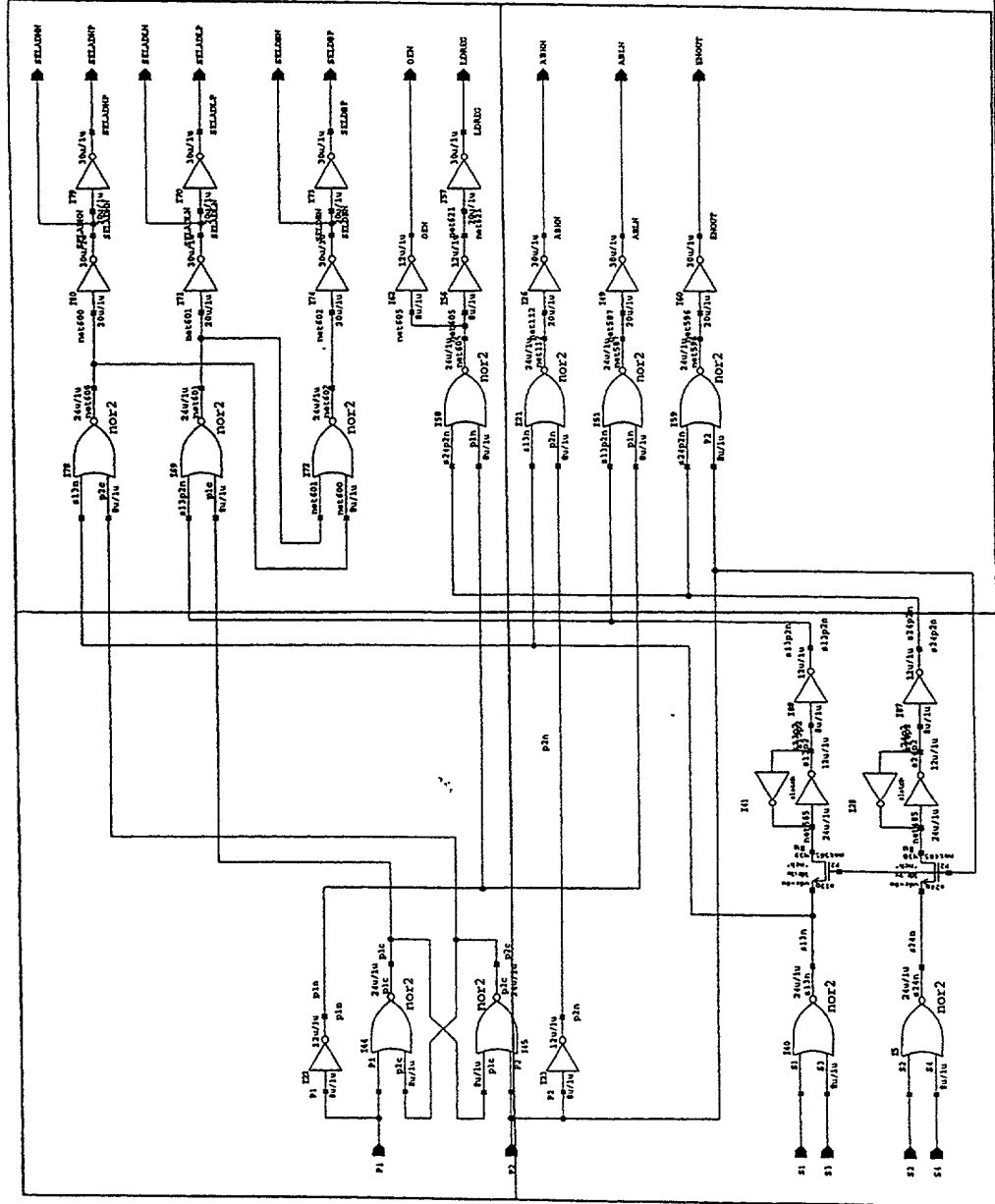


Fig. 20.0101

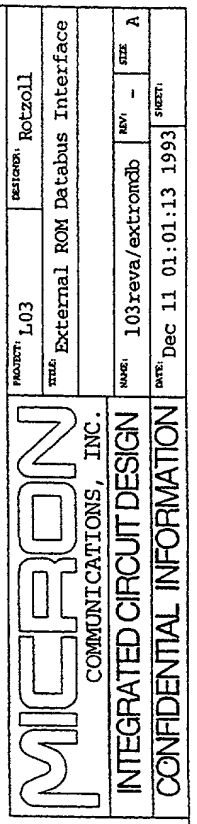
MICRON	
INTEGRATED CIRCUIT DESIGN	
COMMUNICATIONS, INC.	
CONFIDENTIAL INFORMATION	
PRODUCT	ROM
TYPE	External ROM Control Logic
DATE	10/28/93
REV	1
FILE	20.0101



20.0103AA	20.0103AB	20.0103AC
-----------	-----------	-----------



FIG. 20.0104



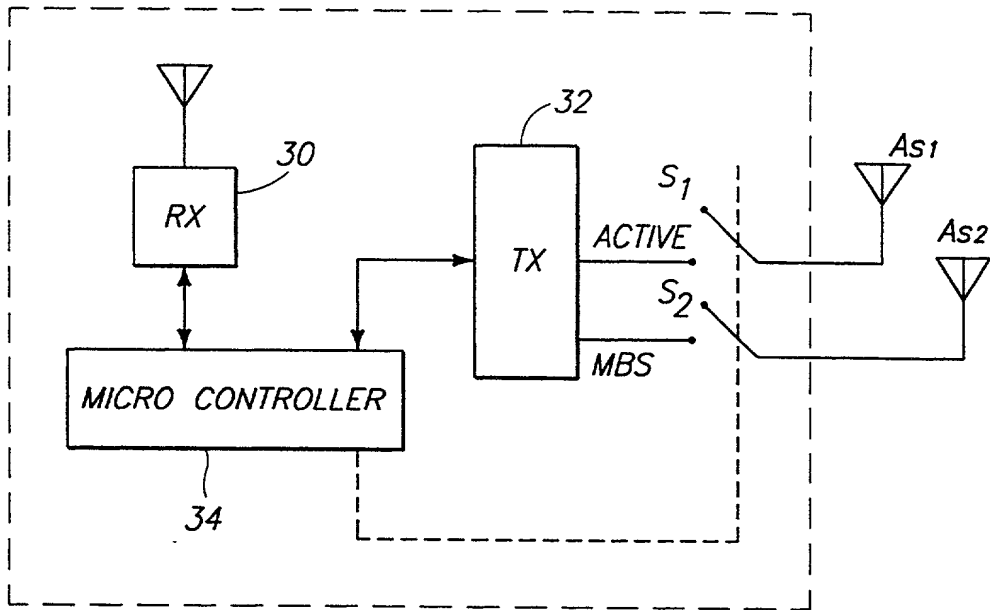


Fig. 2

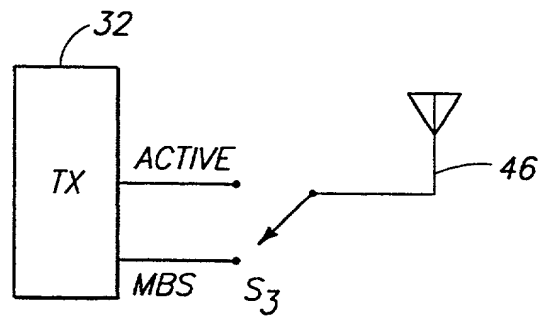
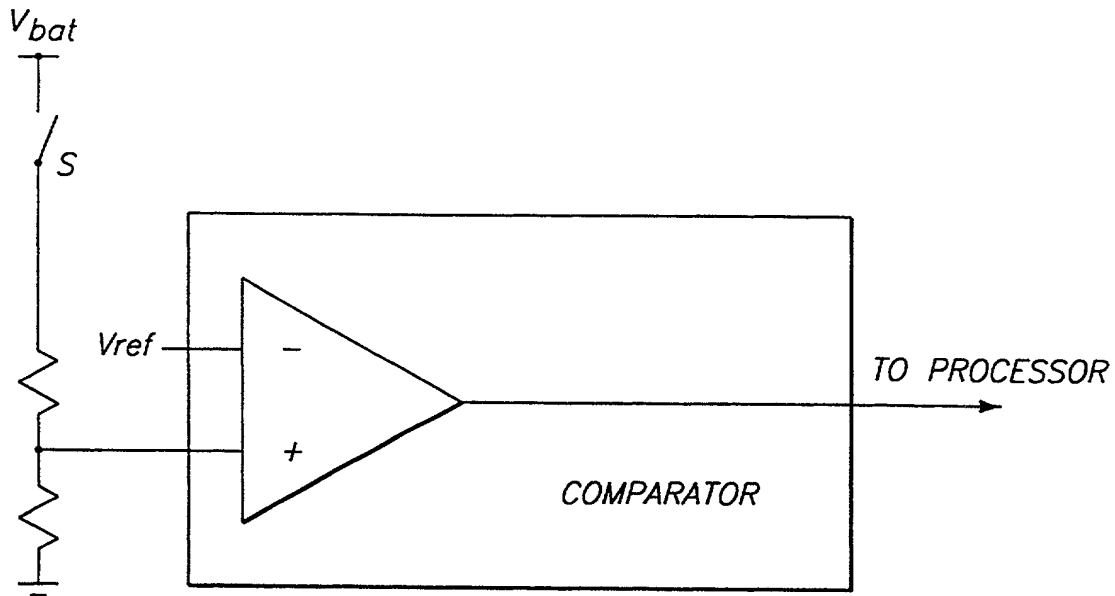


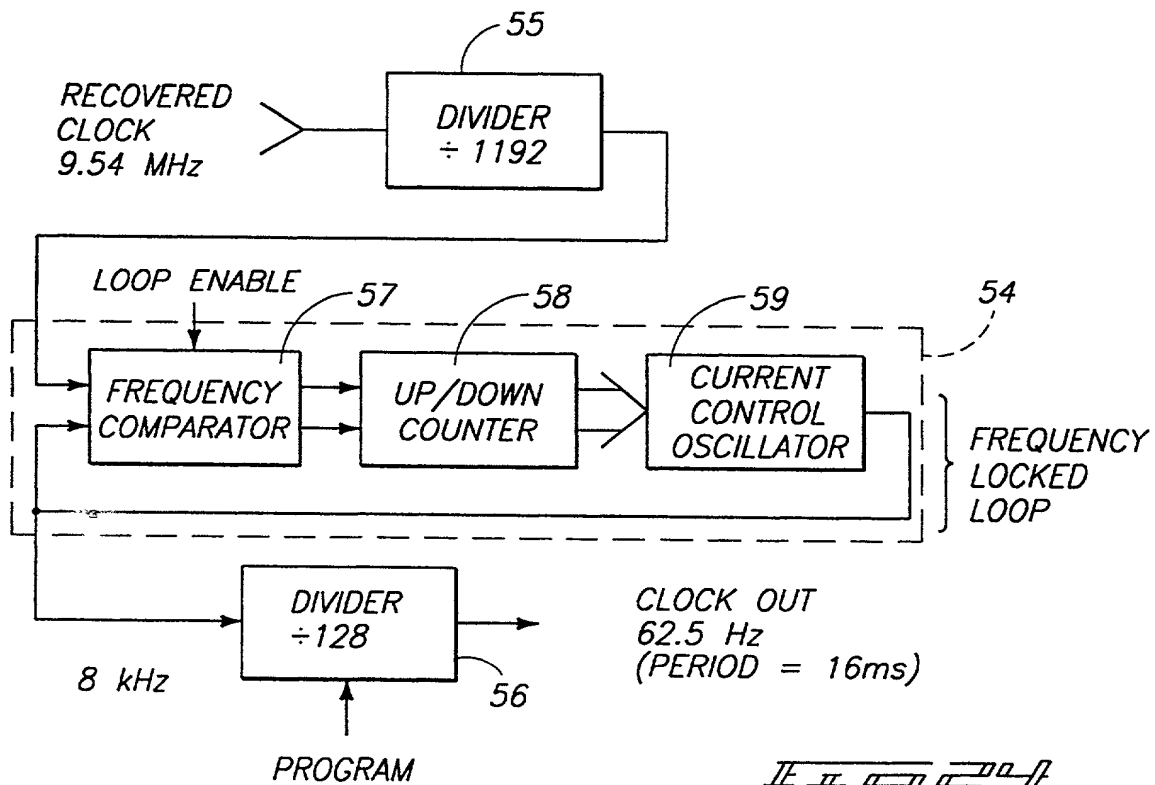
Fig. 3



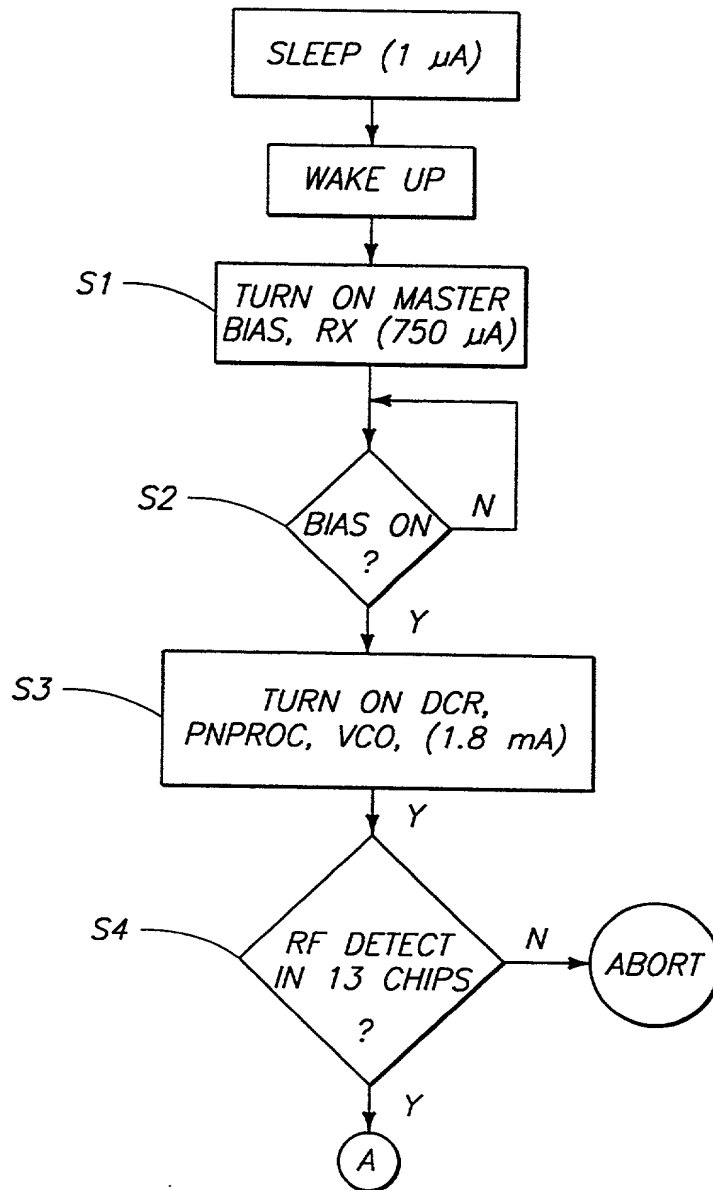


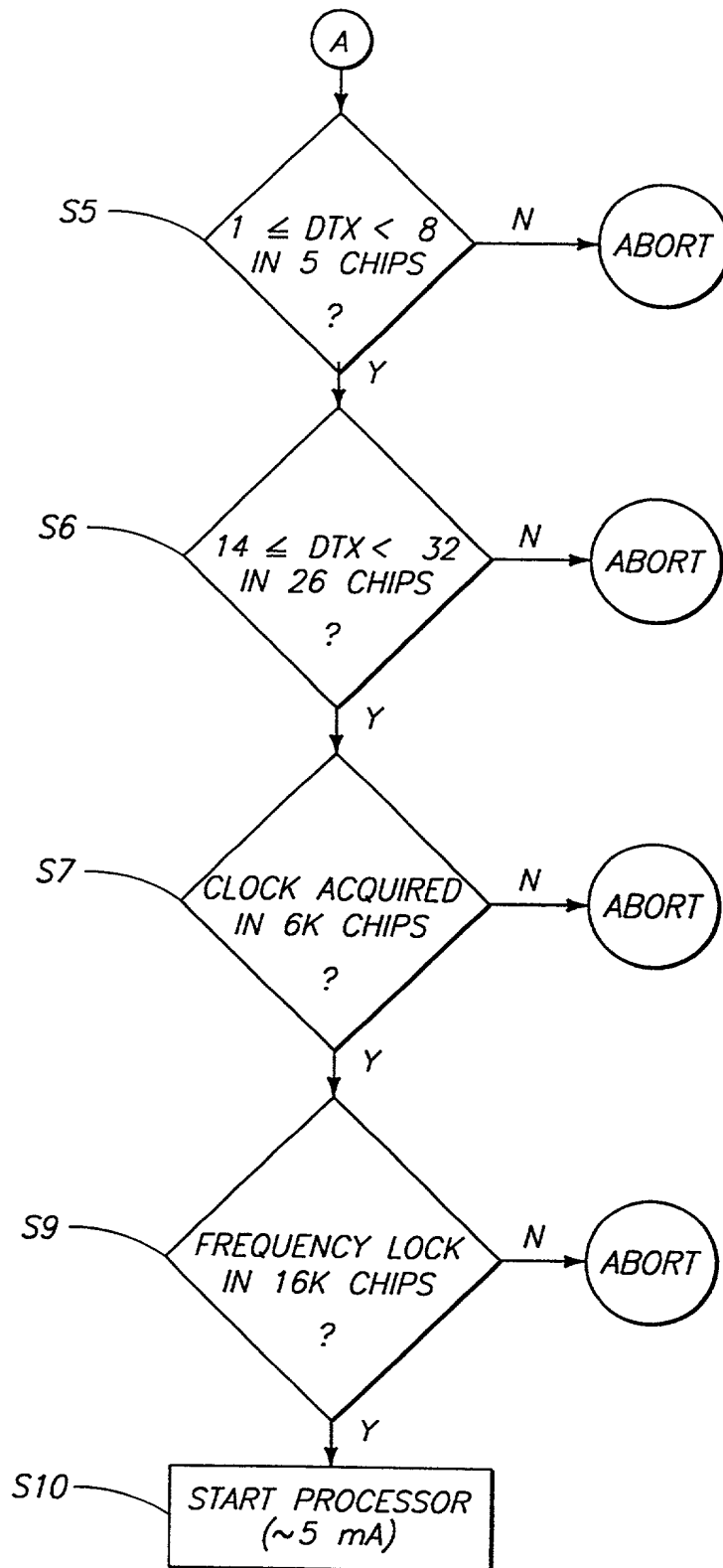
$V_{ref}$  = bandgap voltage  $\approx 1.2$  V for silicon

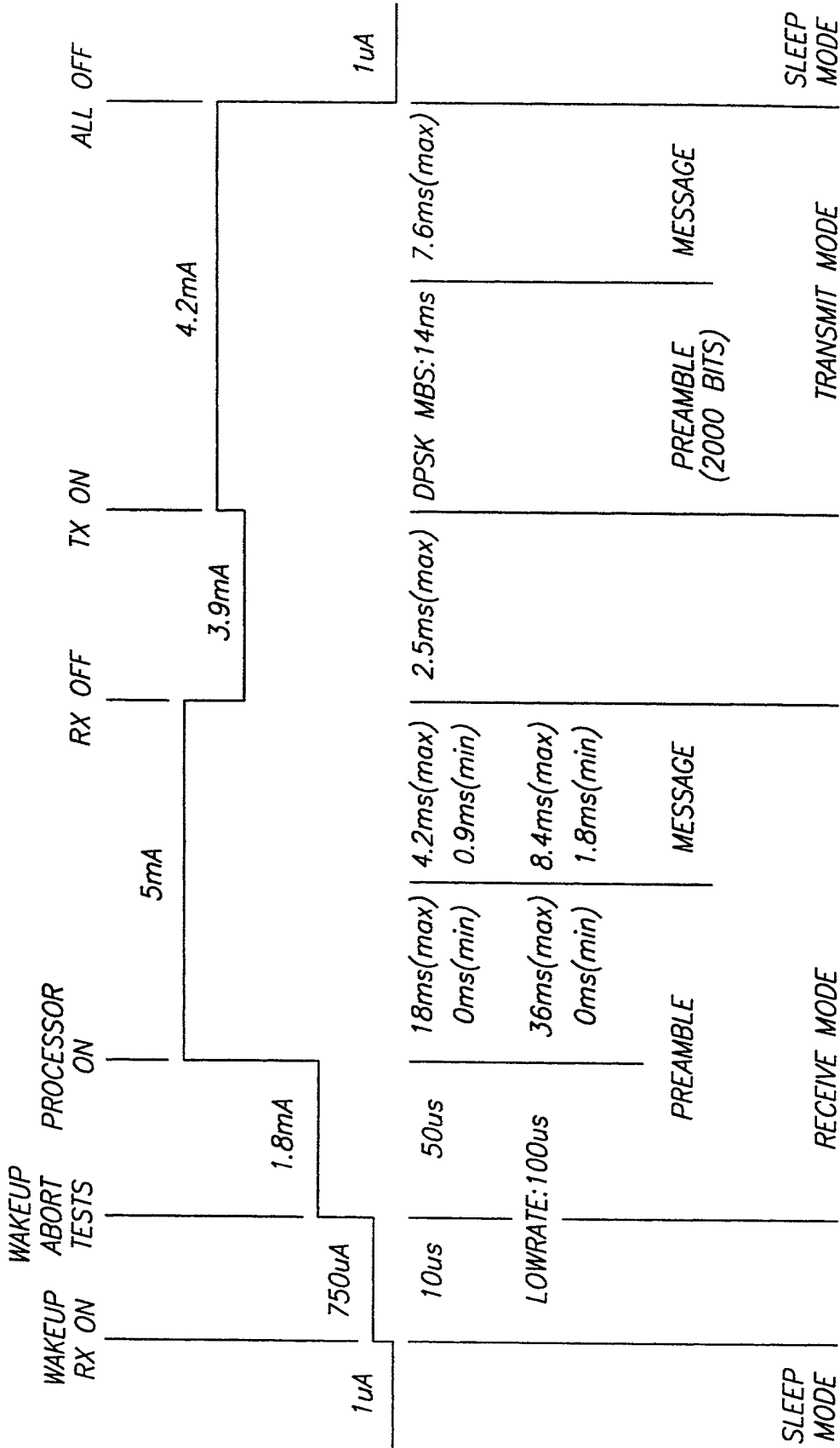
*II II II II*

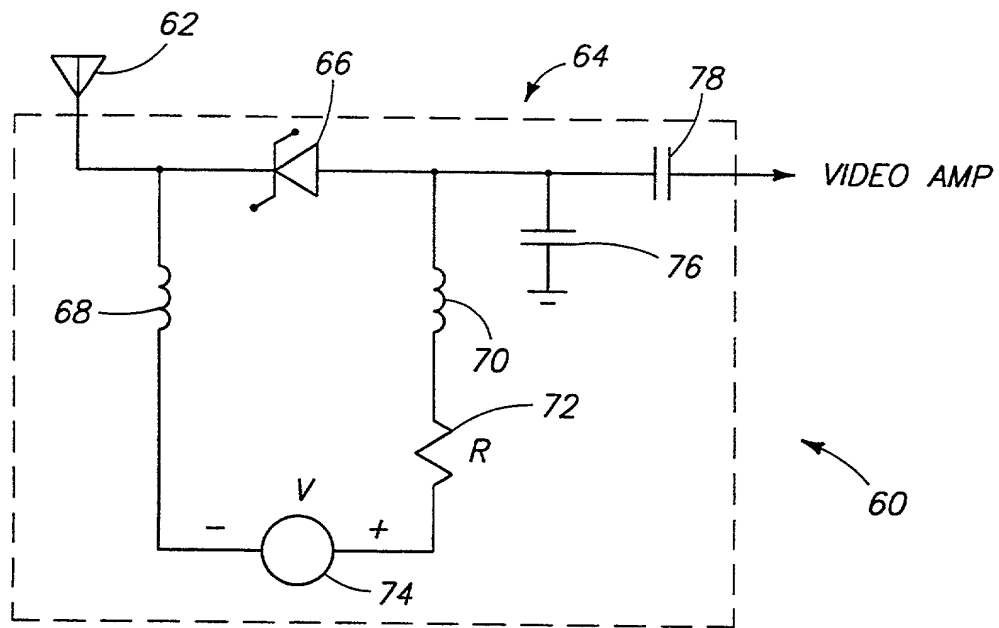


*II II II II*

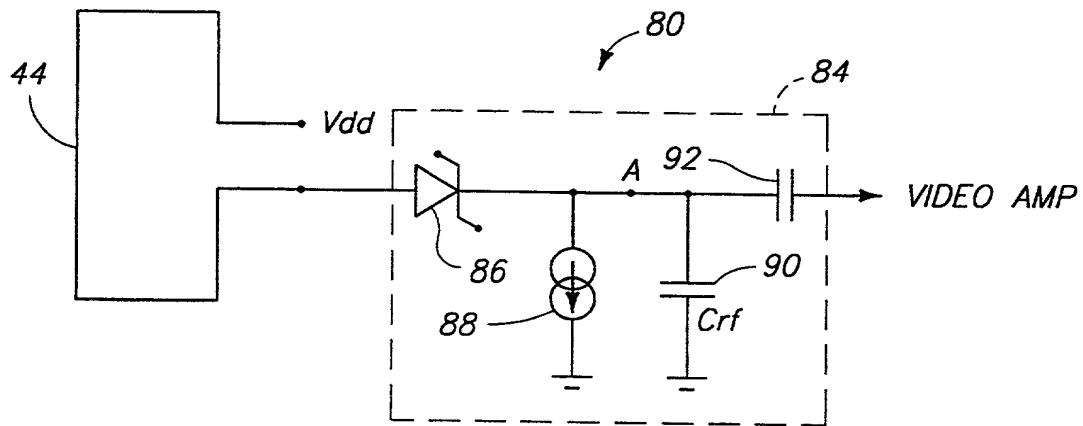
WAKEUP SEQUENCE



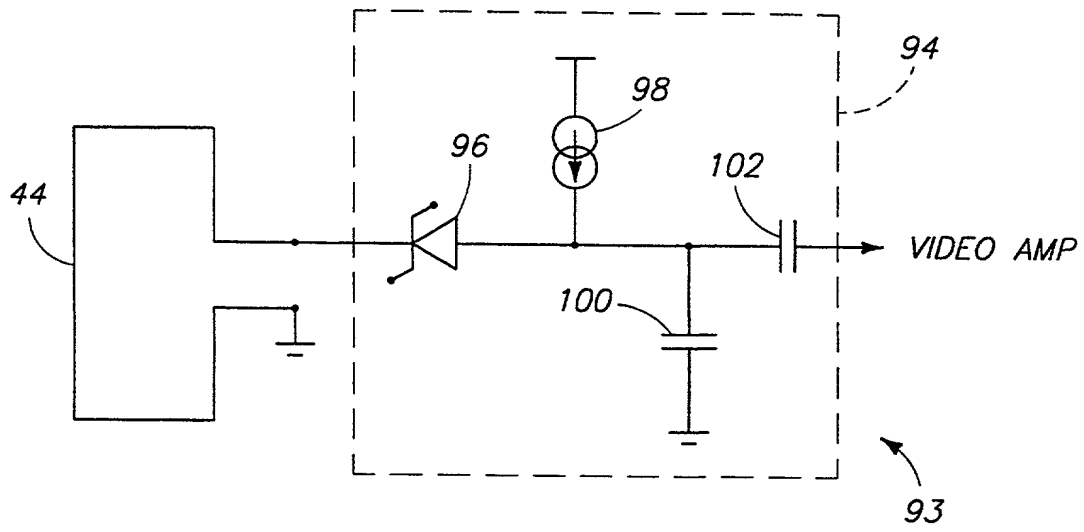




II II II II II



II II II II II

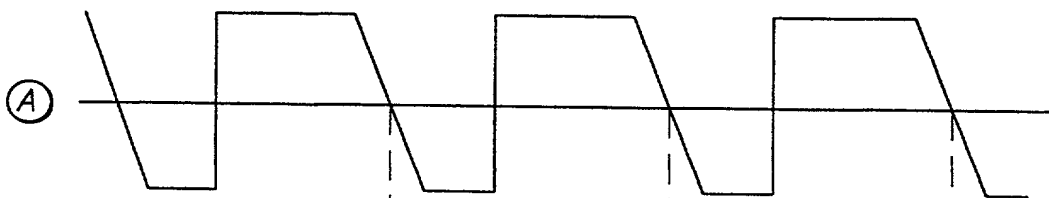


*II II II II*

LOW POWER



HIGH POWER



AMPLIFIED  
DIGITAL  
SIGNAL



*II II II II*

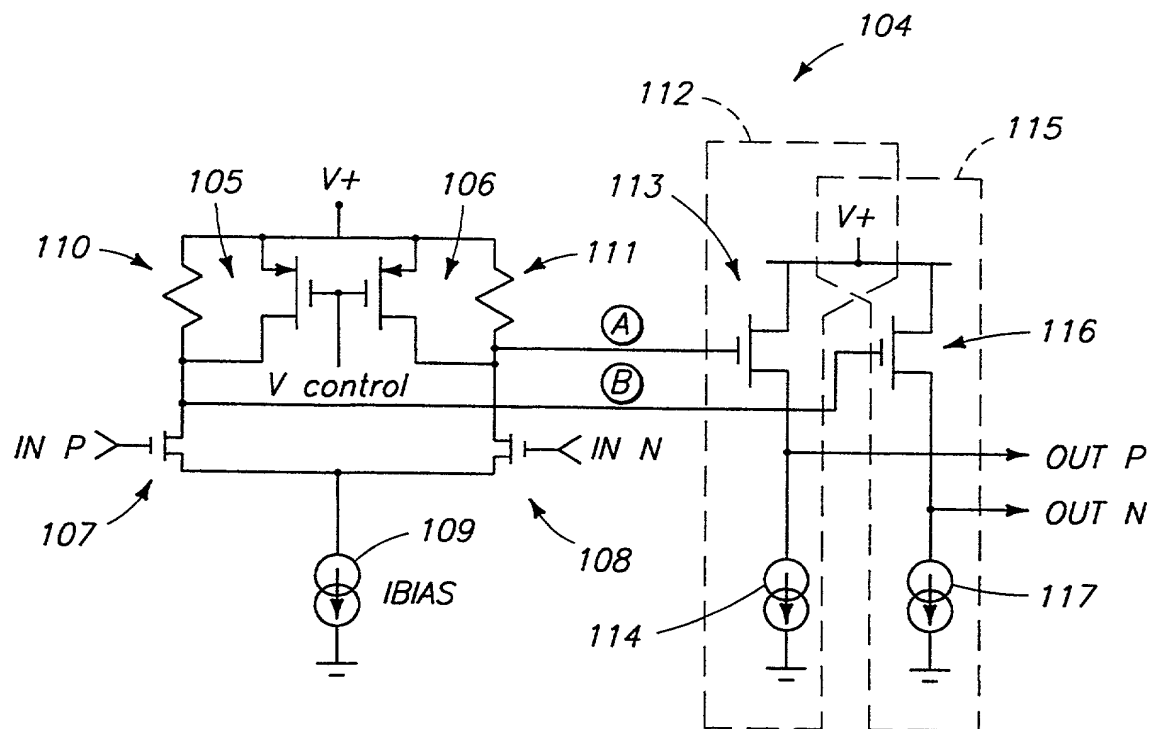


FIG. 32

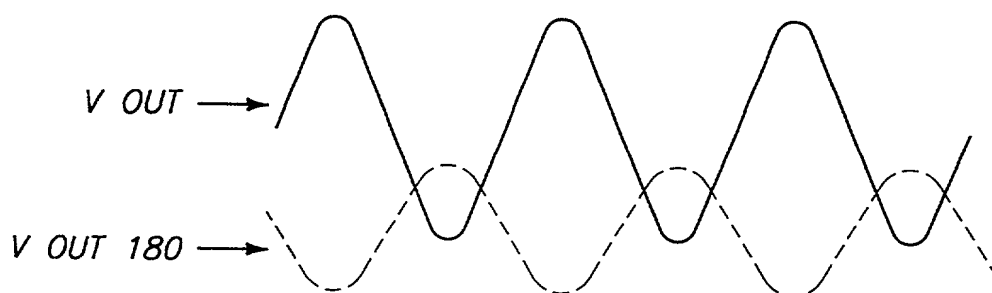
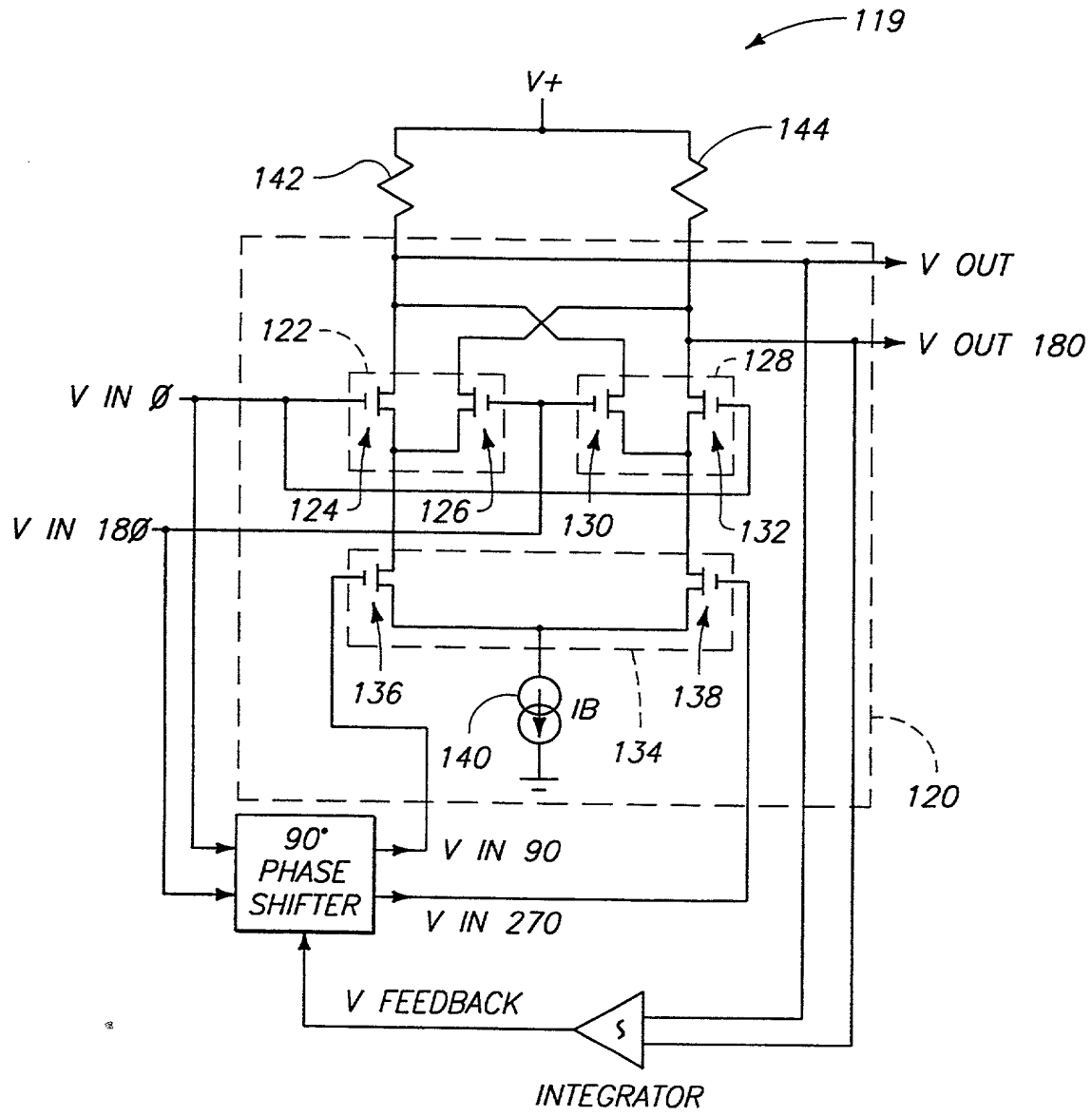
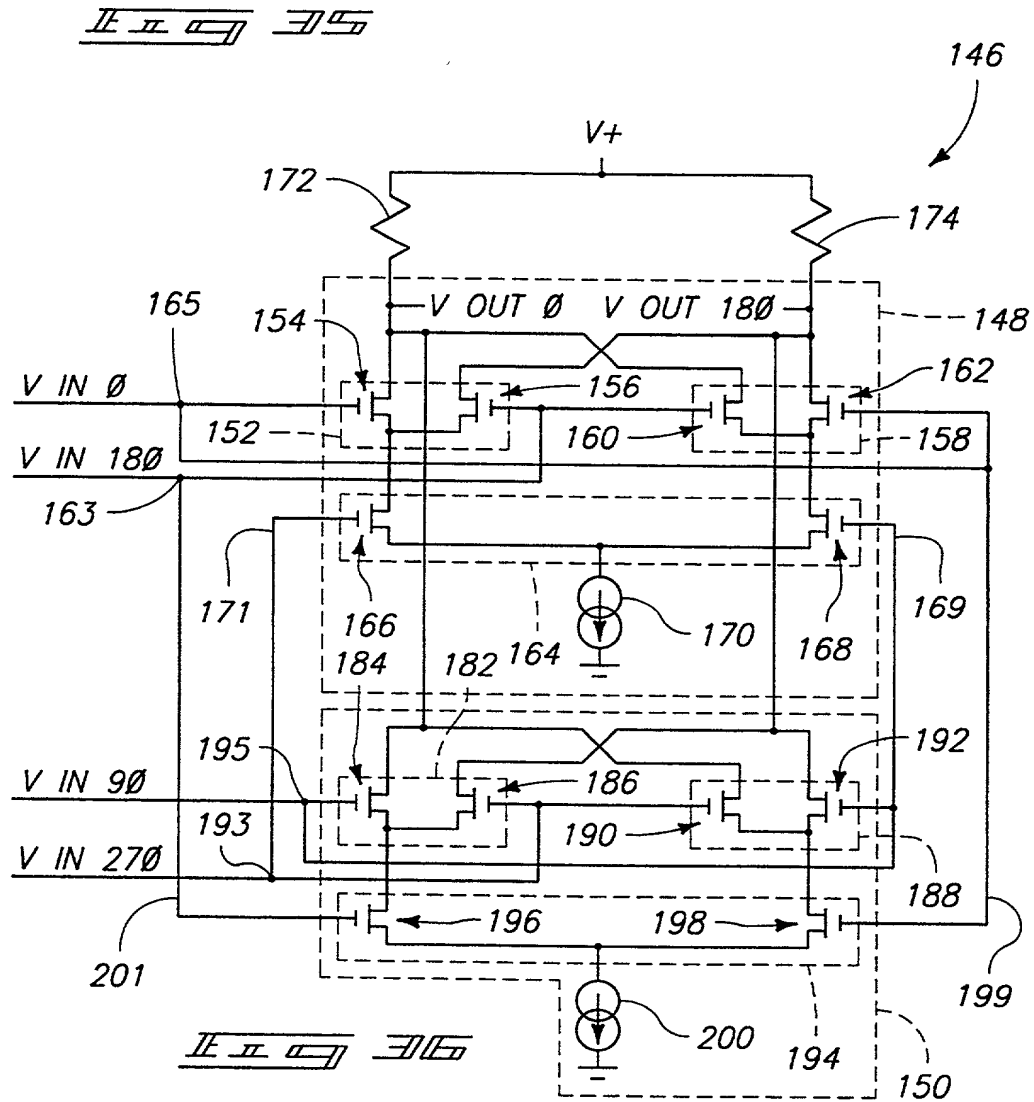
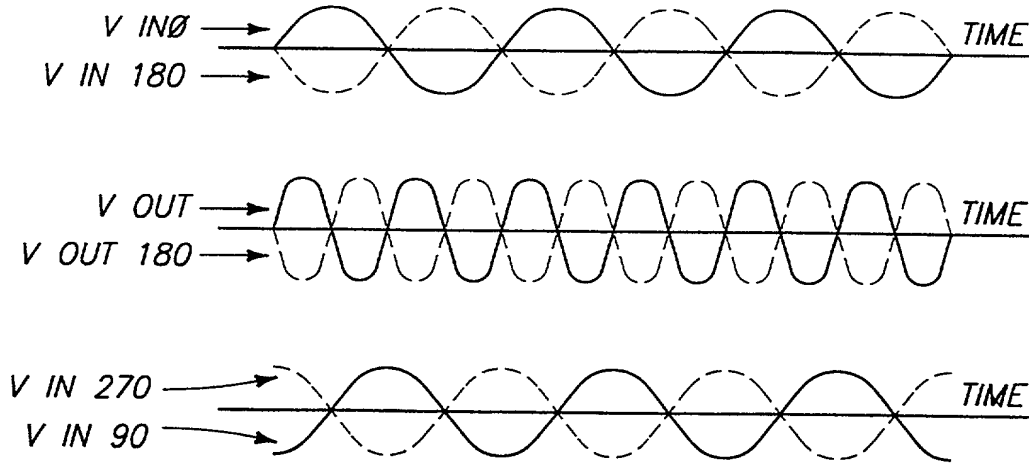


FIG. 33



II II II II II





40020 250250

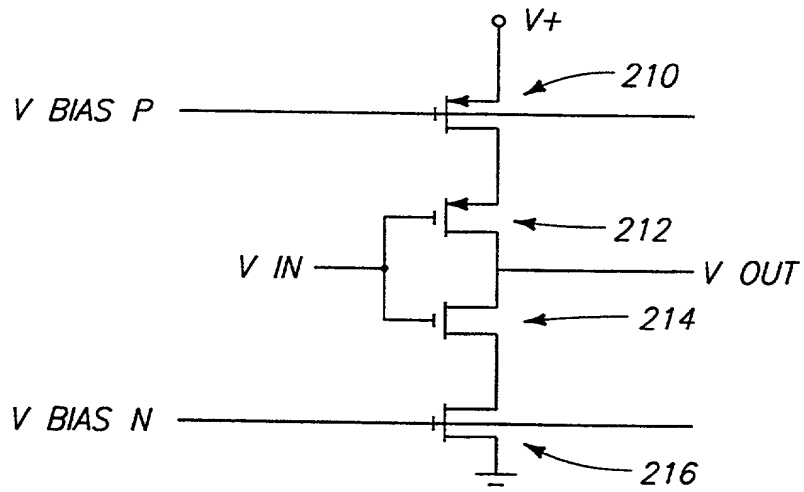


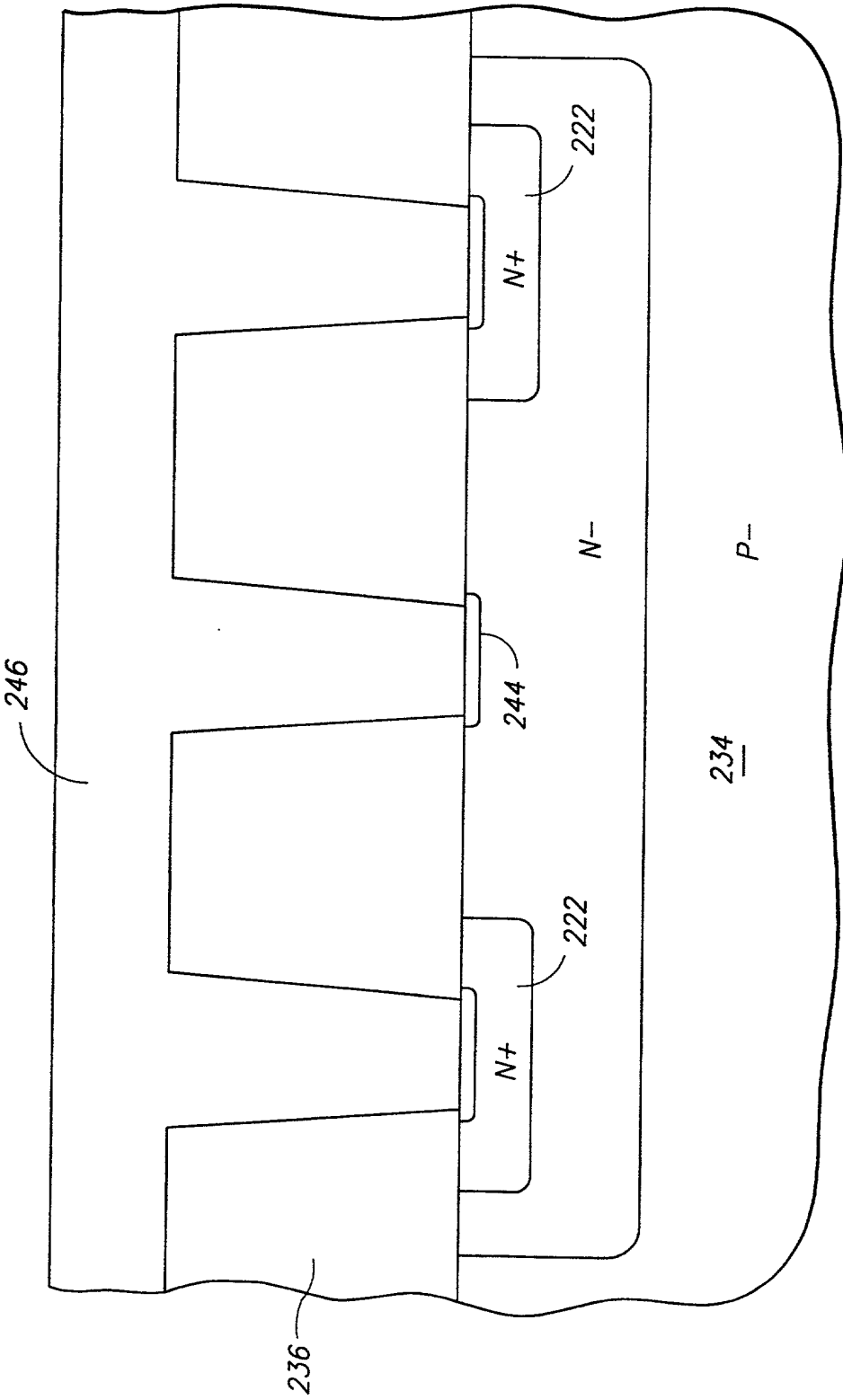
FIG. 3

ALL INFORMATION CONTAINED  
HEREIN IS UNCLASSIFIED

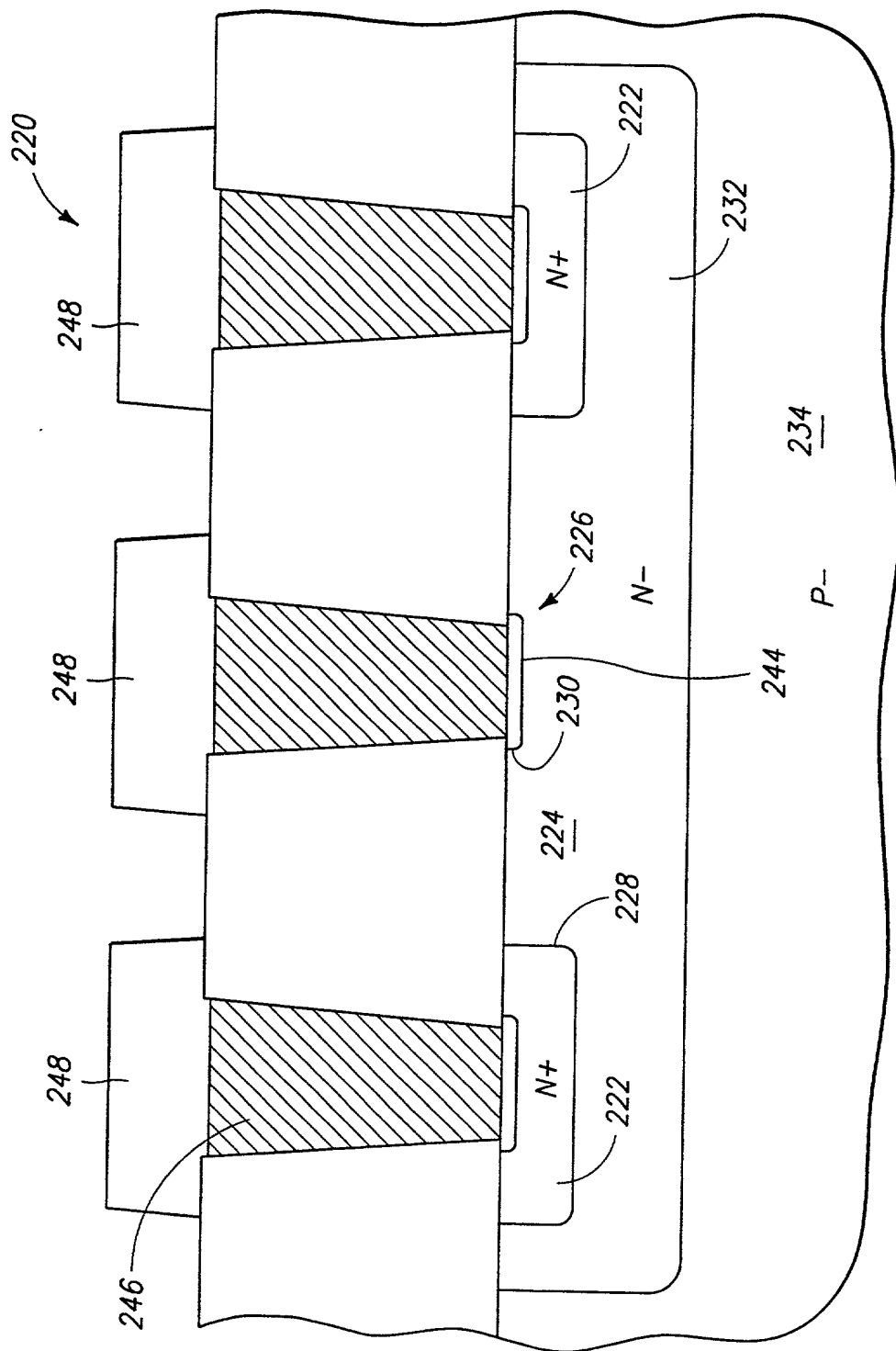
<u>236</u>	
<u>232</u>	N-
<u>234</u>	P-

FILE 3188





MI40-030



II II II II II II

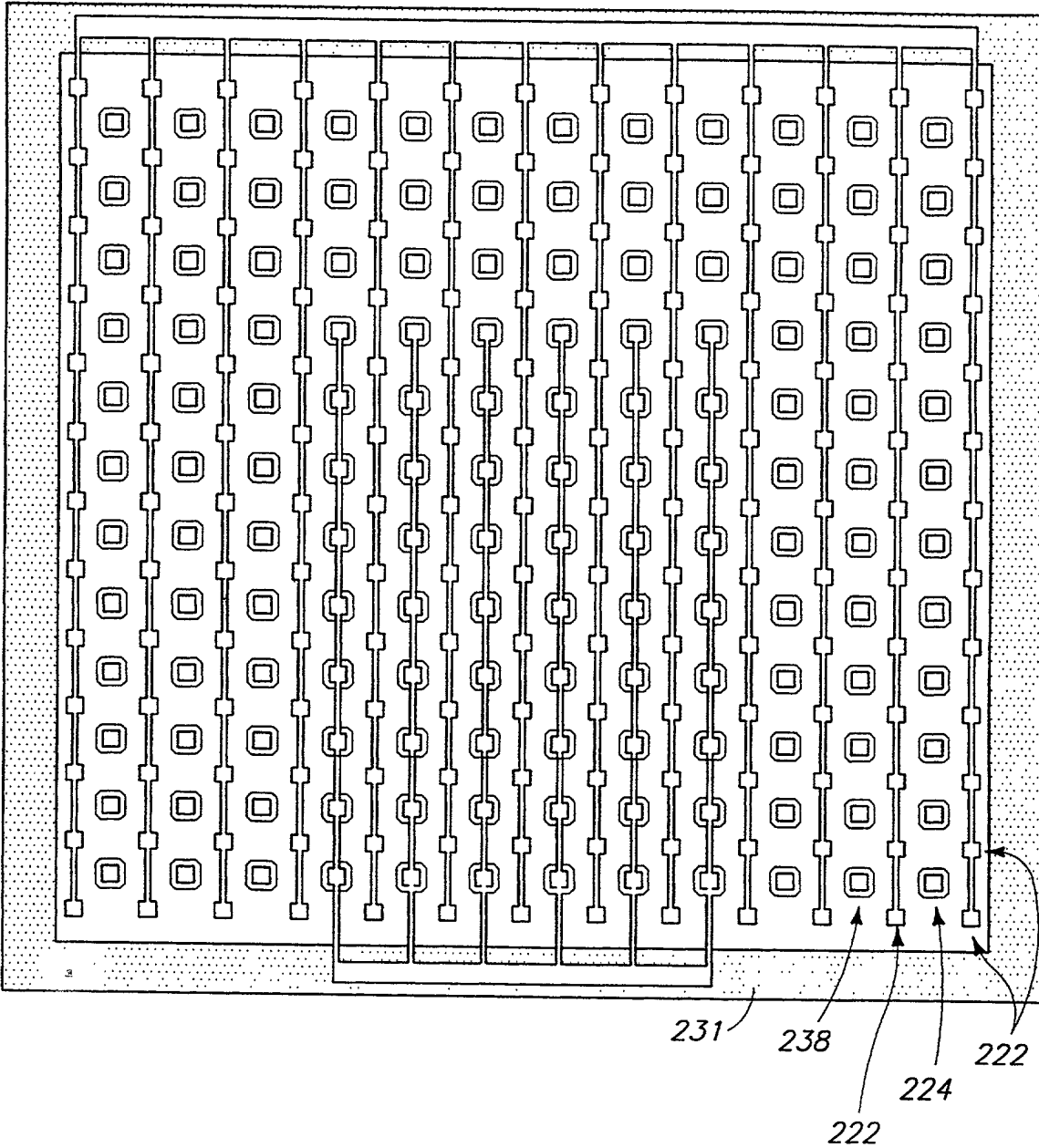


Fig. 4

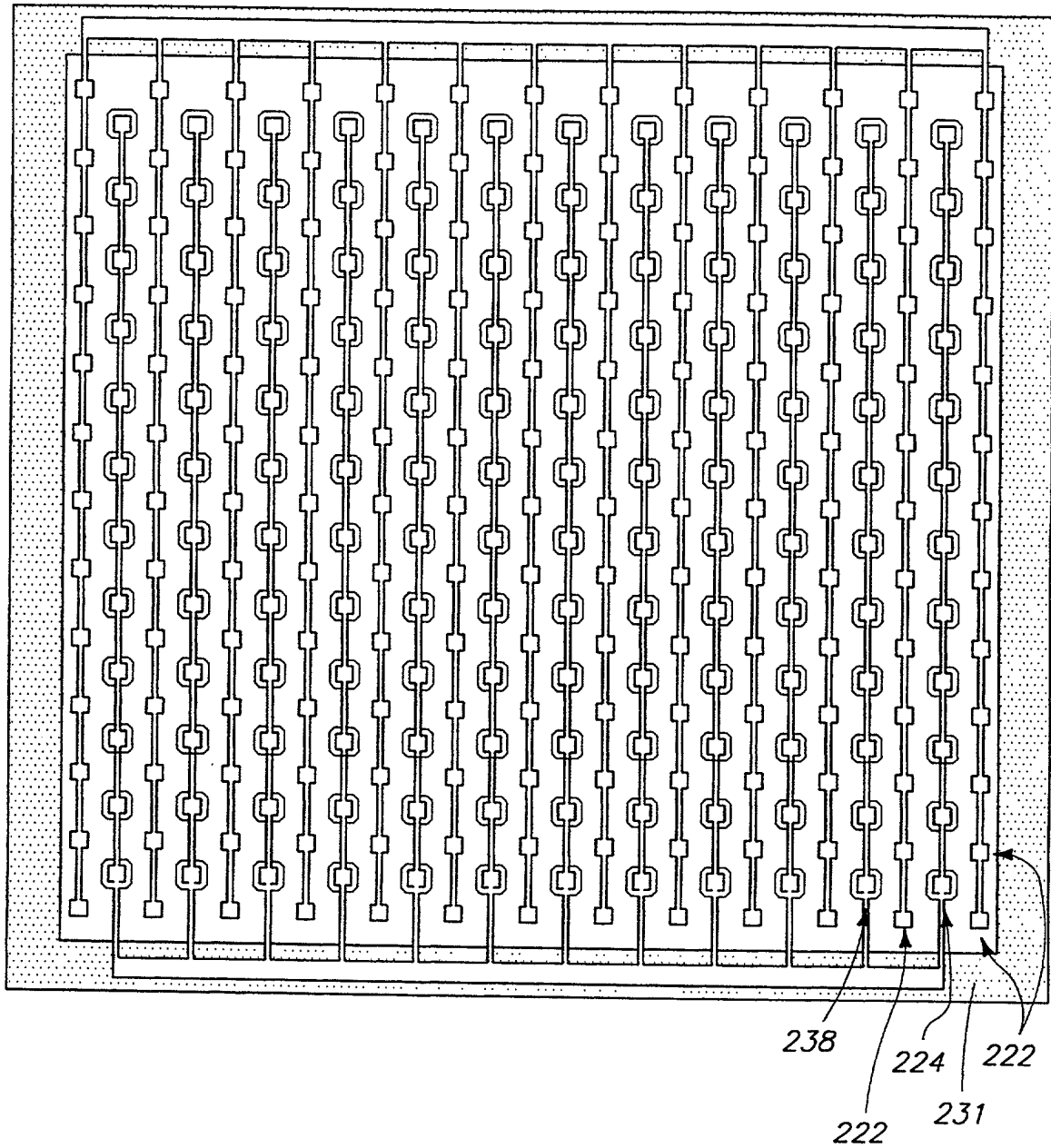
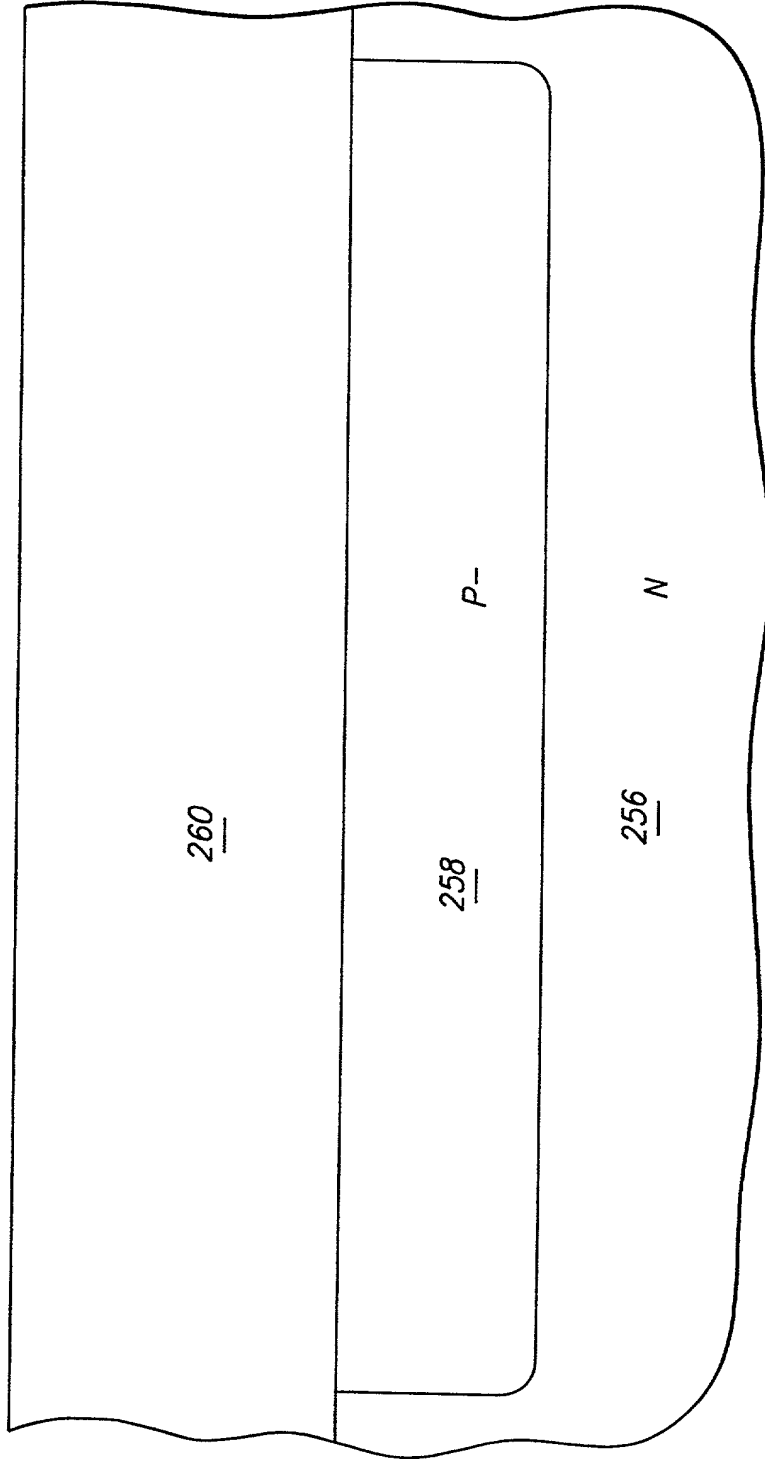


FIG. 4

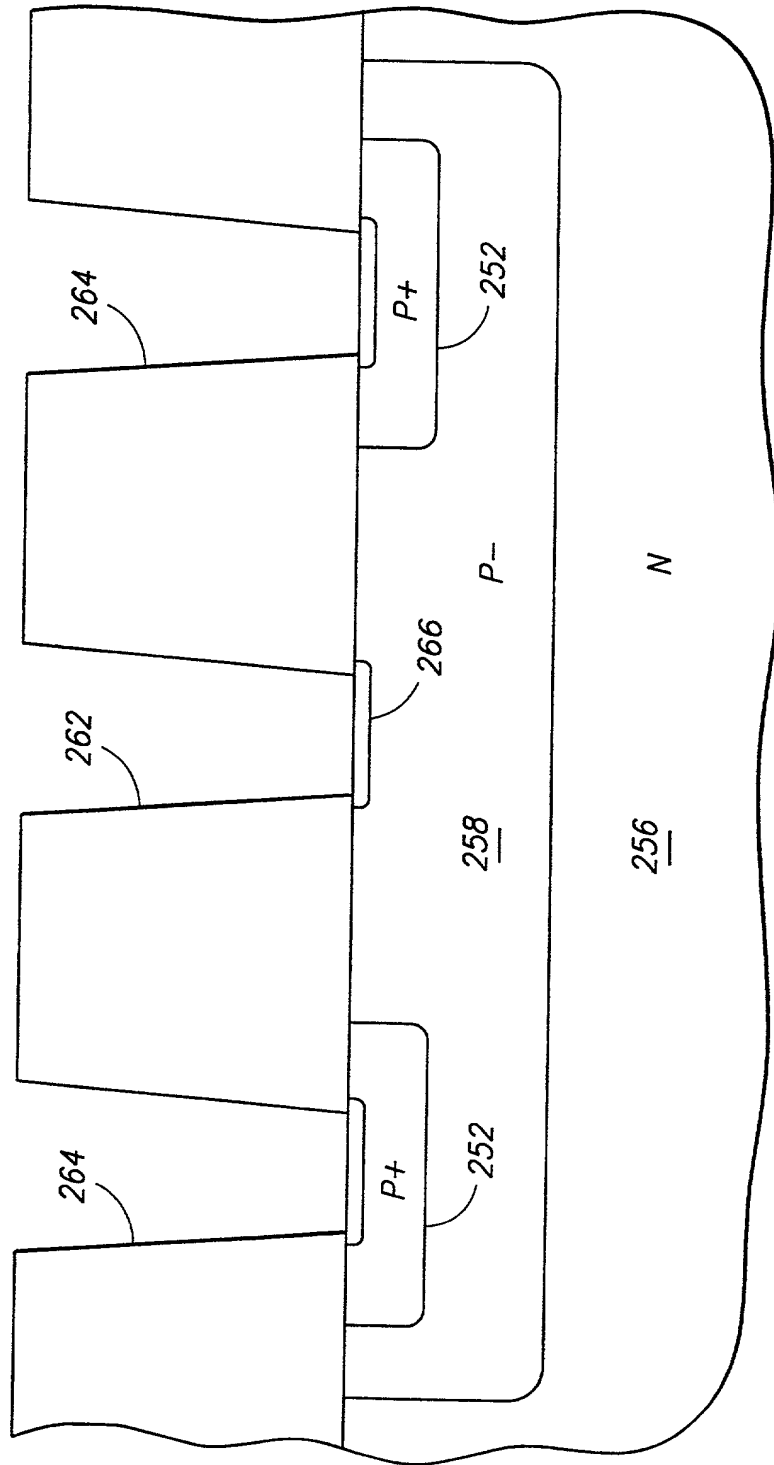


MI40-030

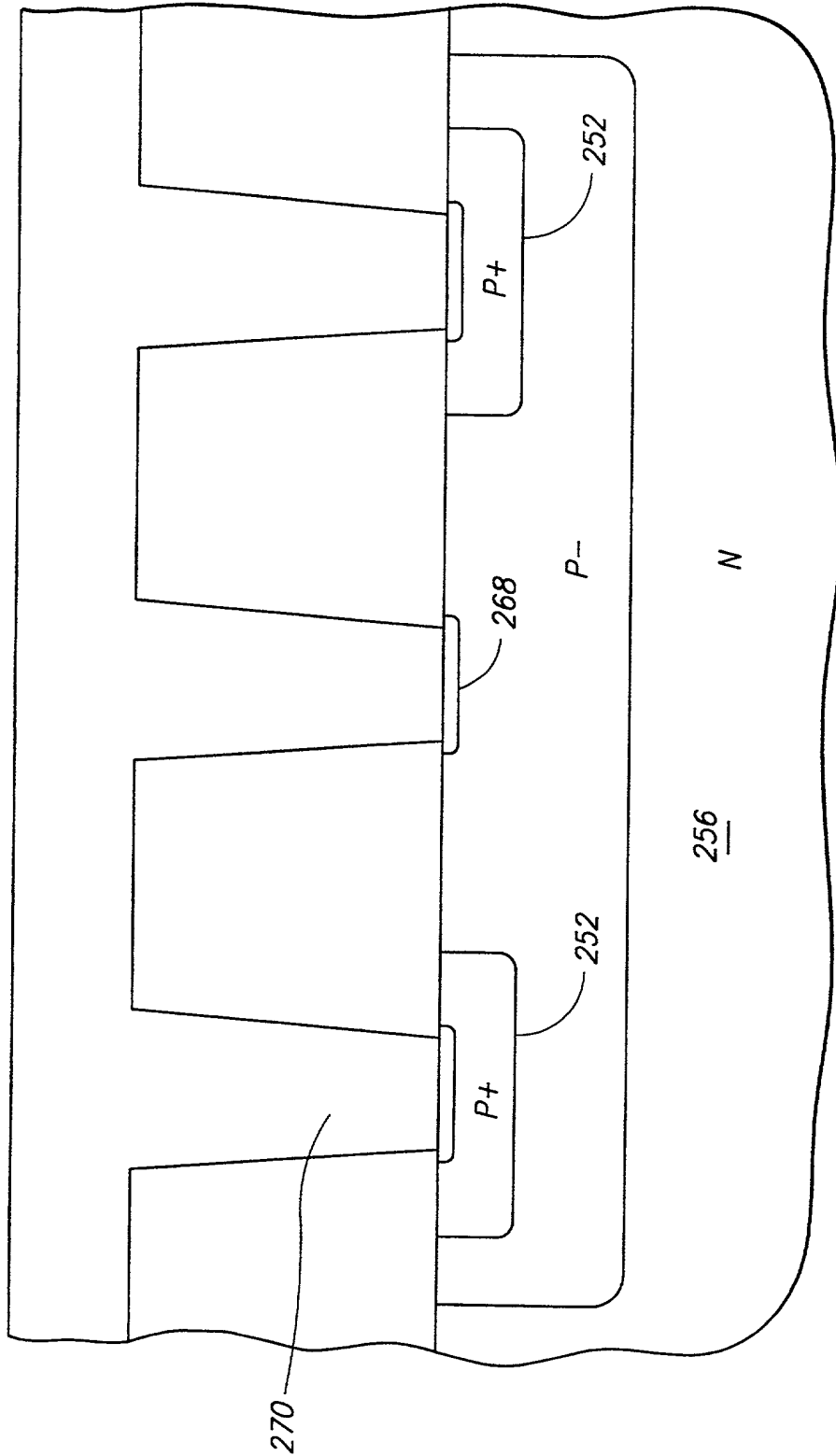
MI40-030



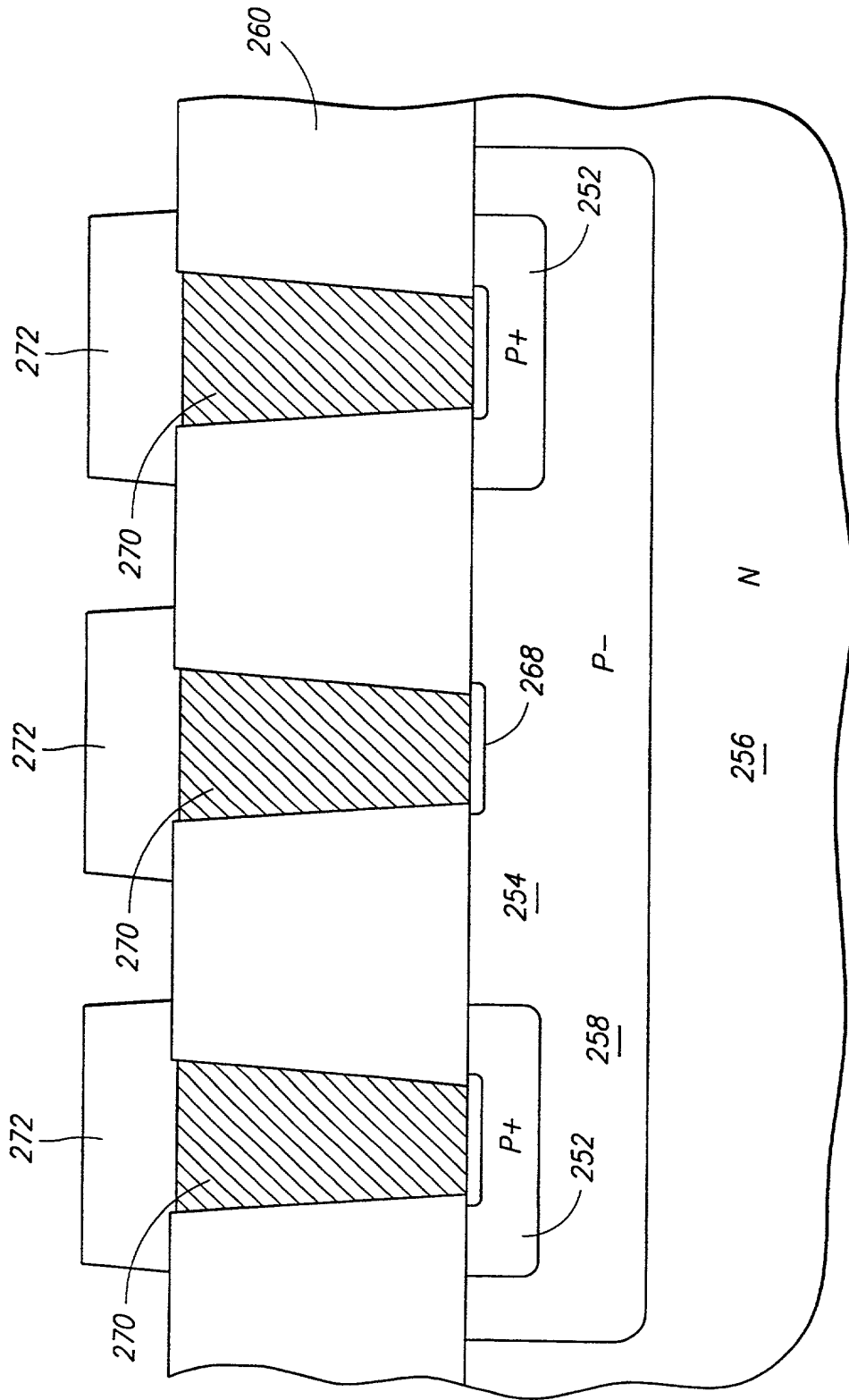
IT 11 07 0707



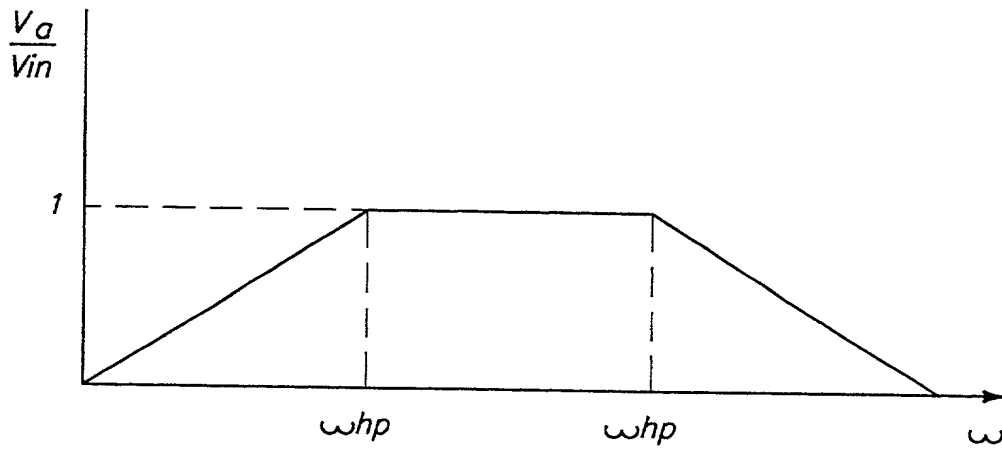
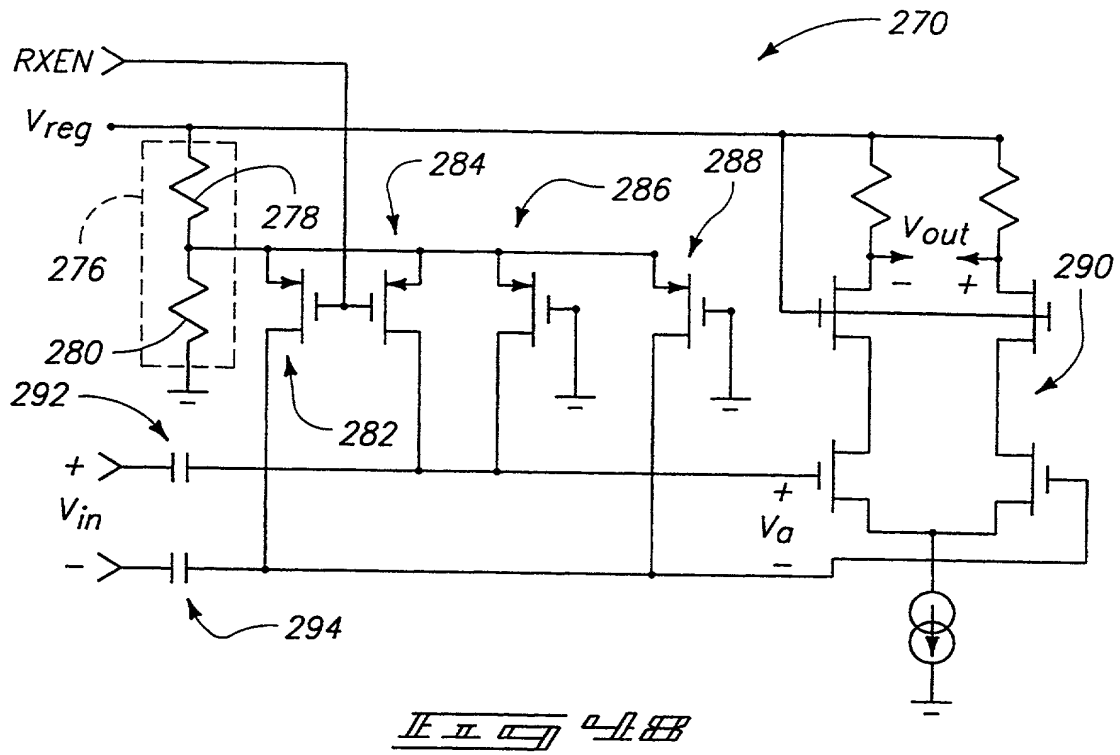
MI 40



MI 40-030



Il x a 211



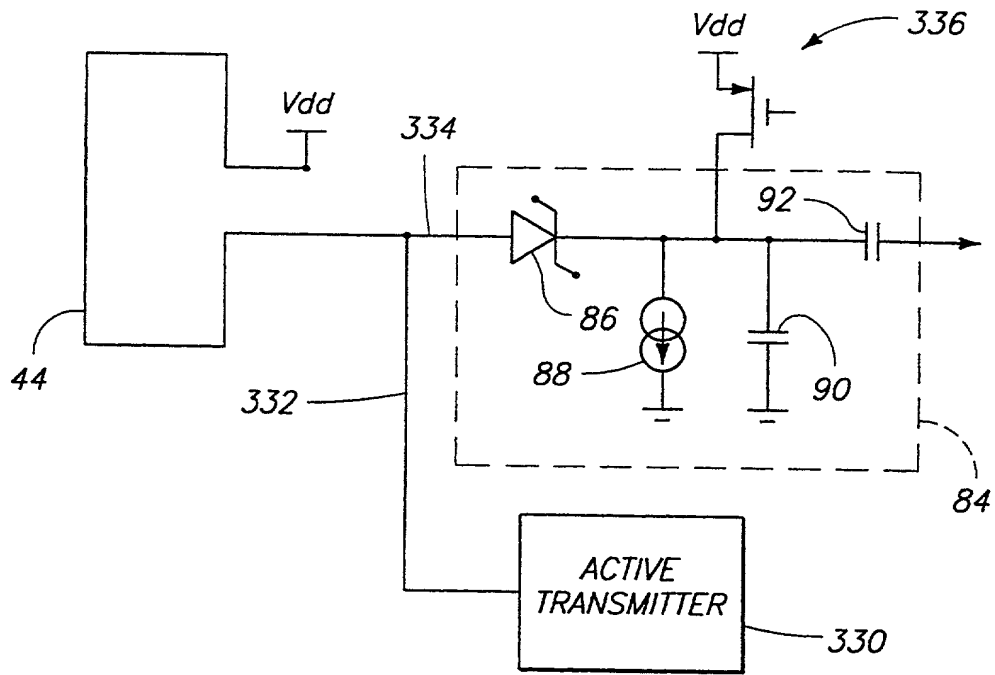


FIG. 50

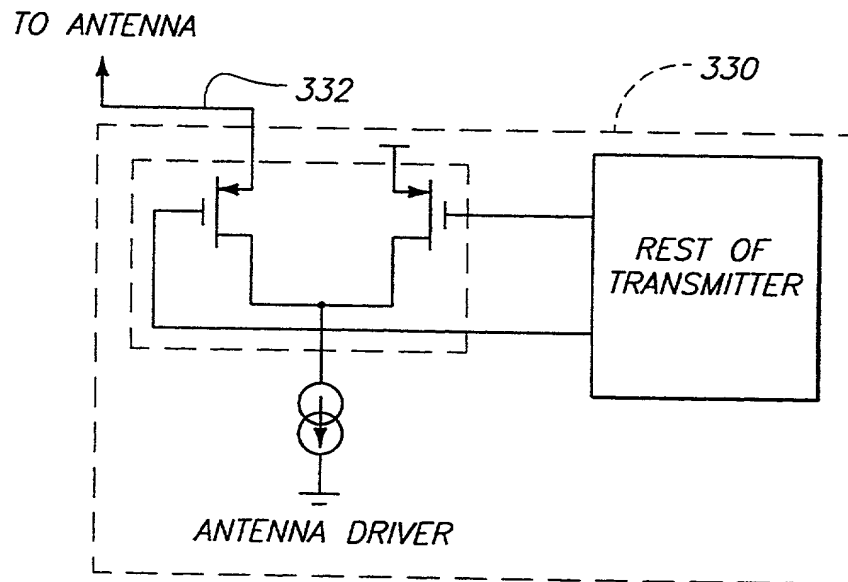


FIG. 51

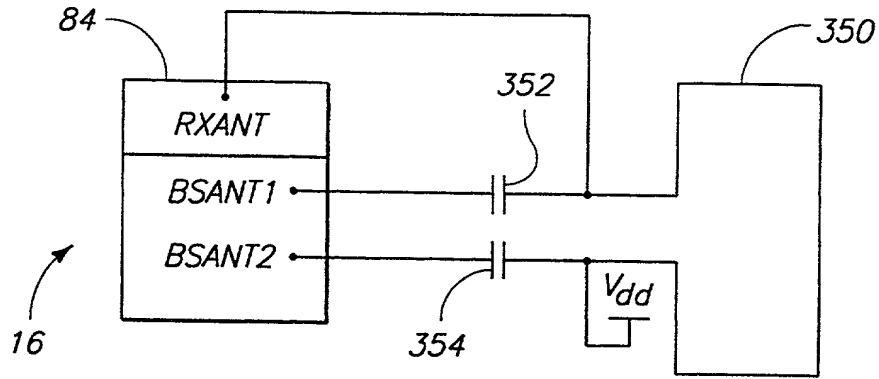


FIG. 5B

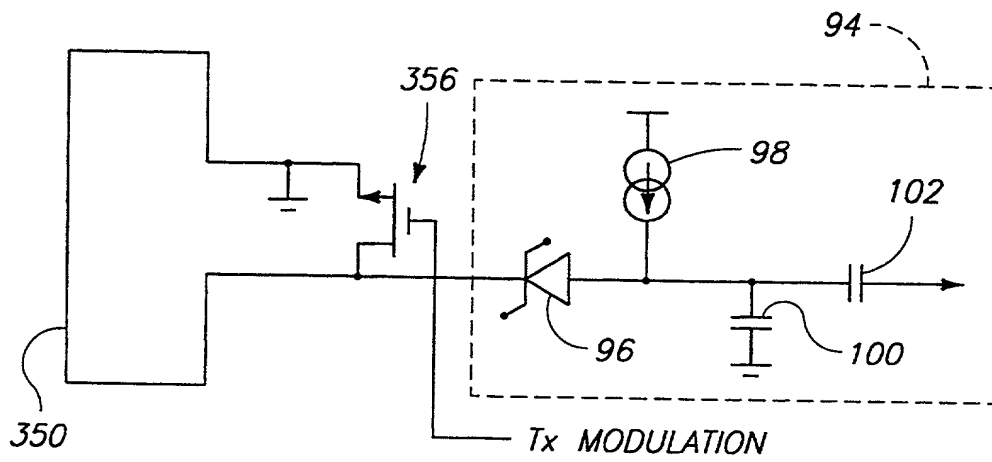


FIG. 5C

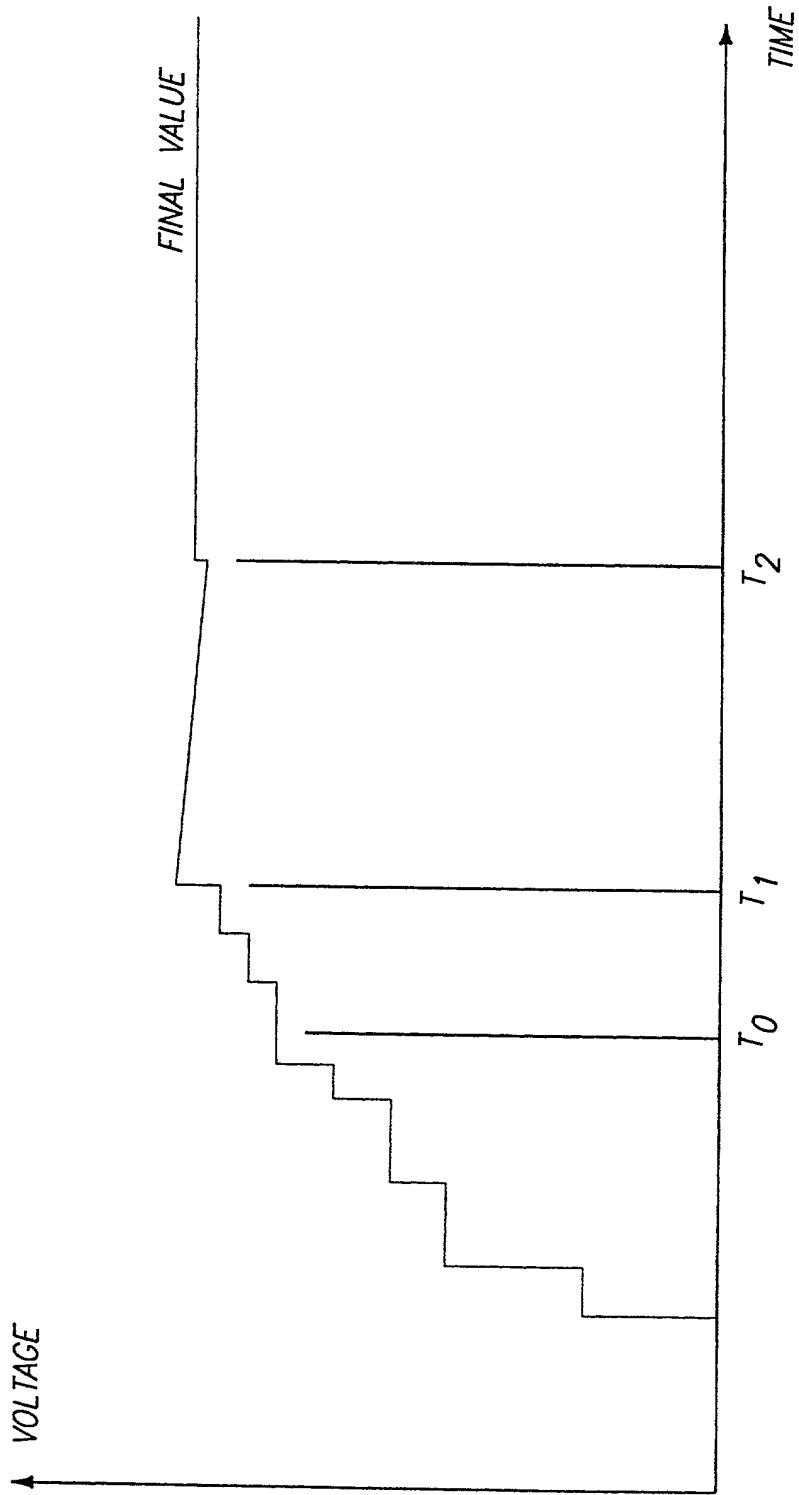
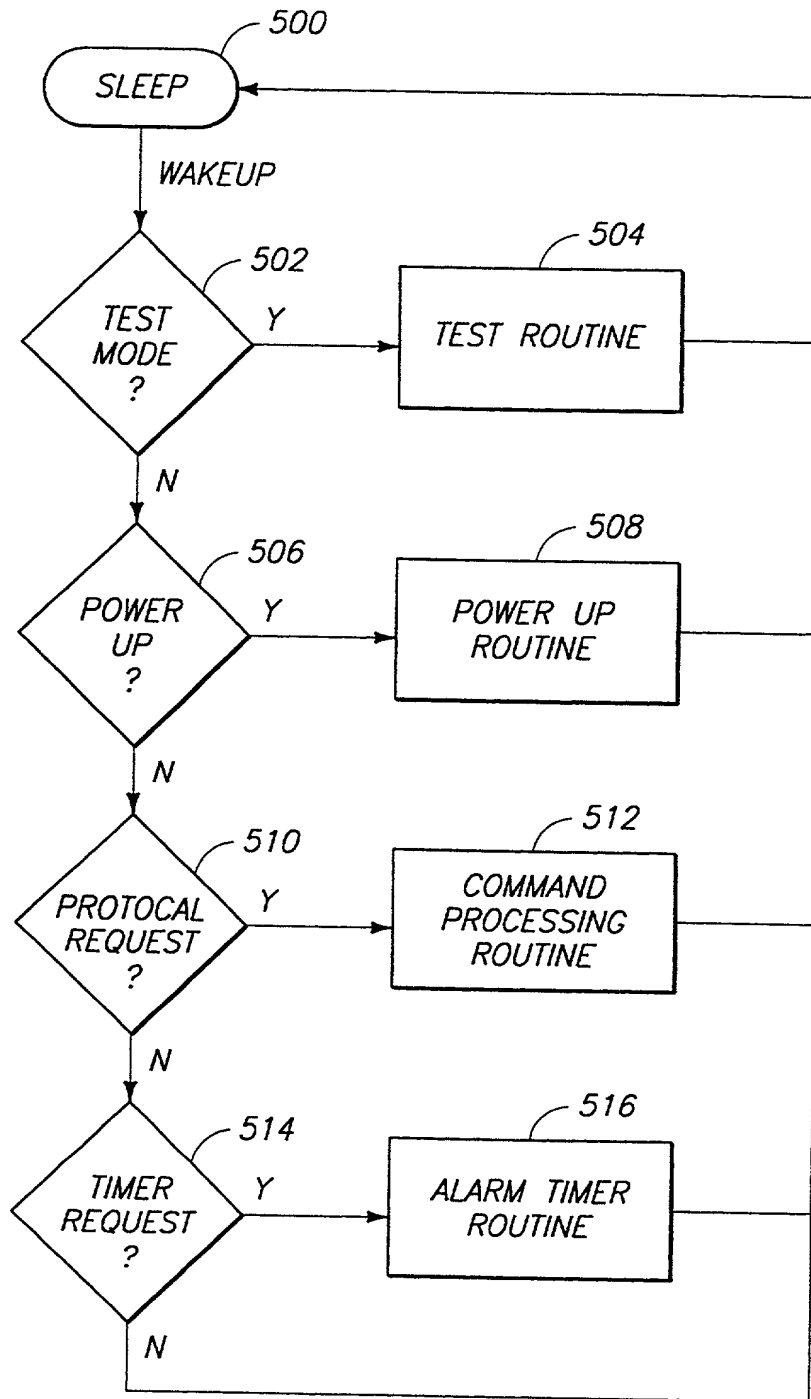
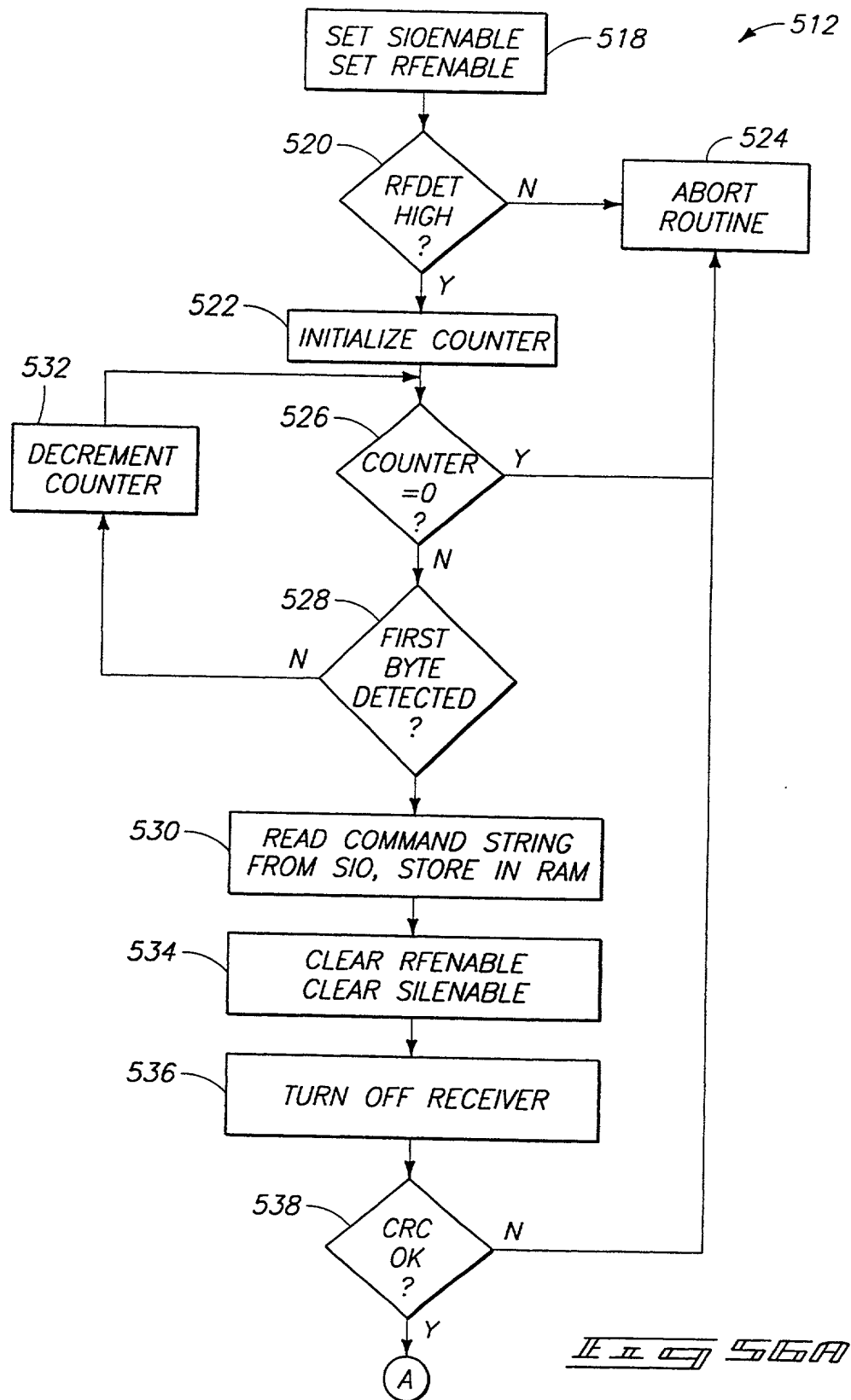
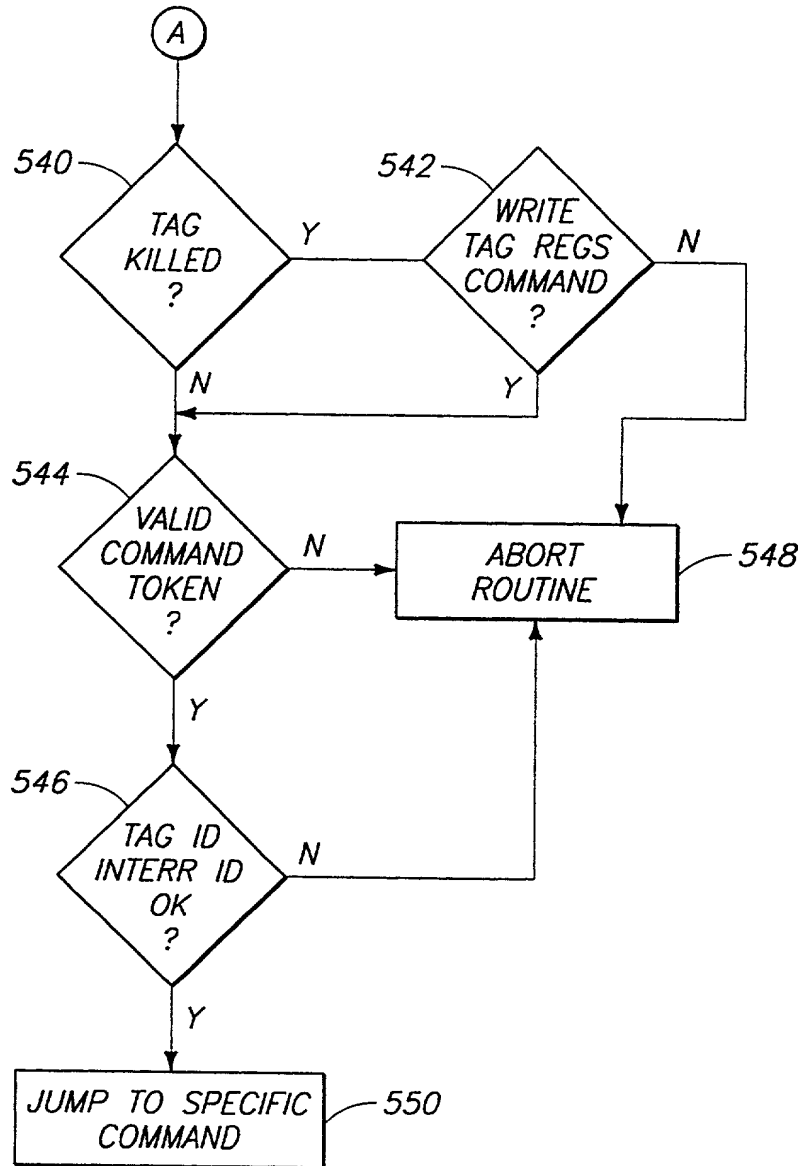


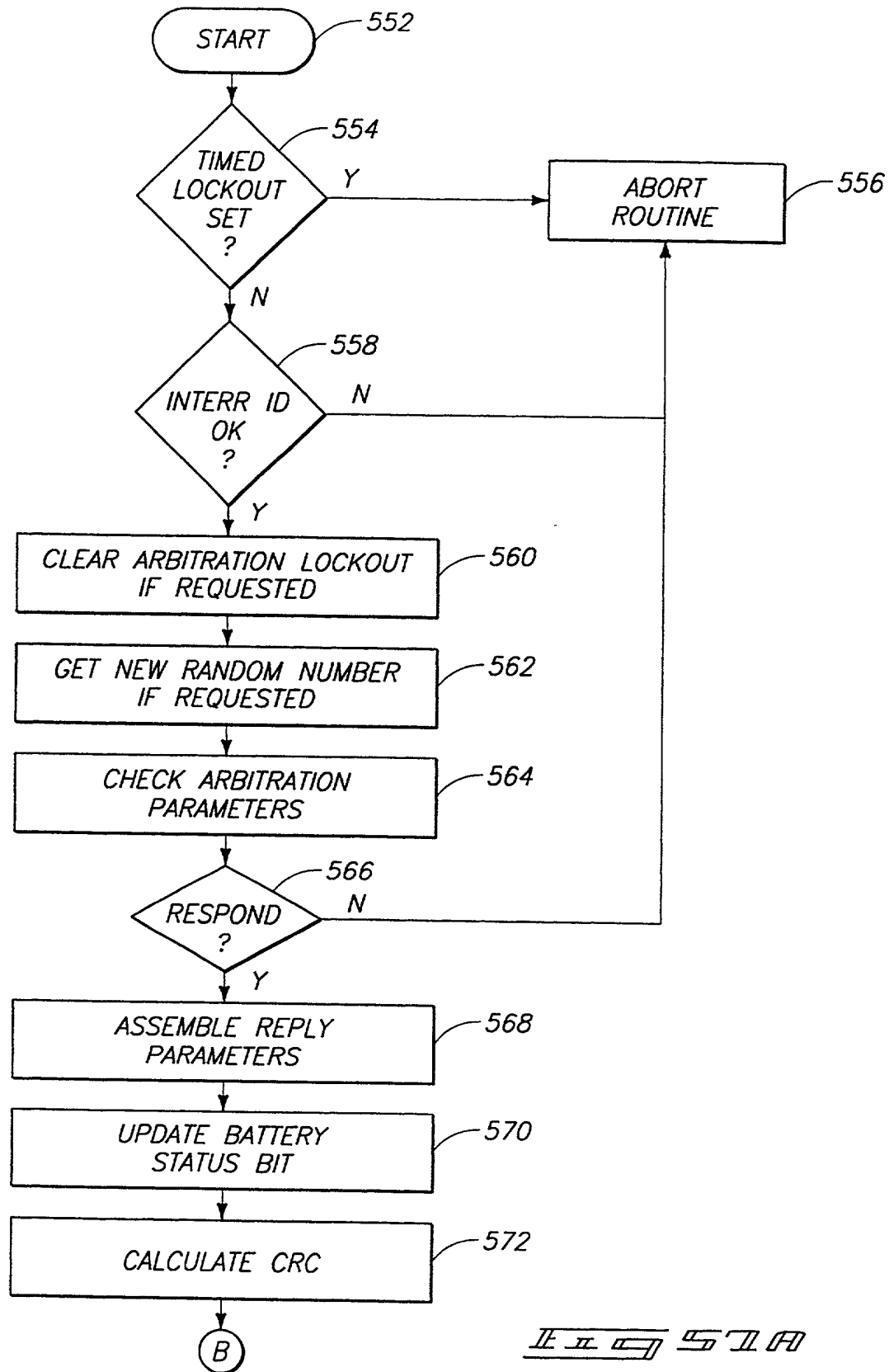
Figure 1

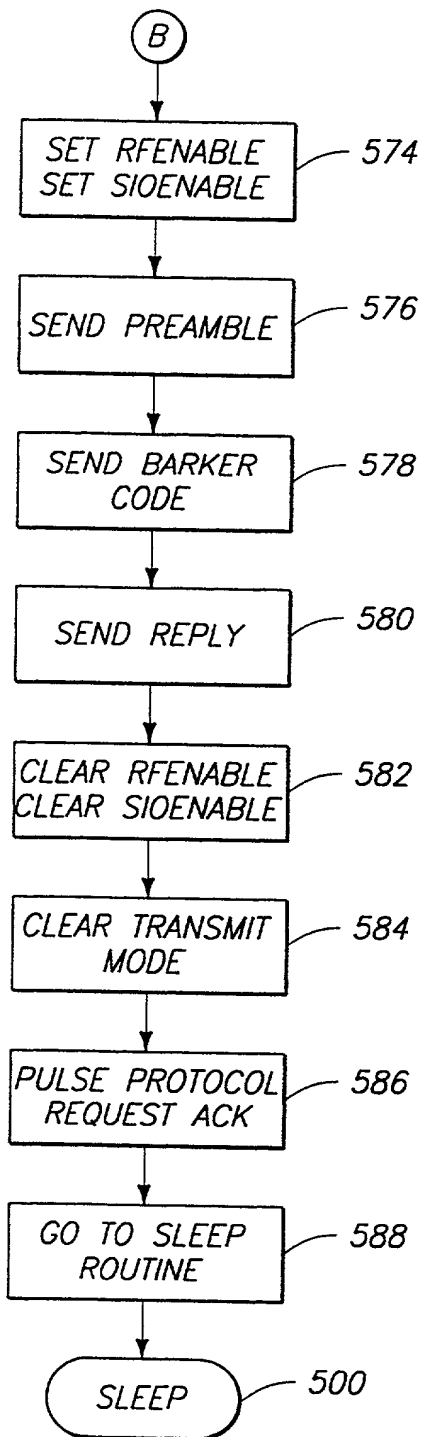


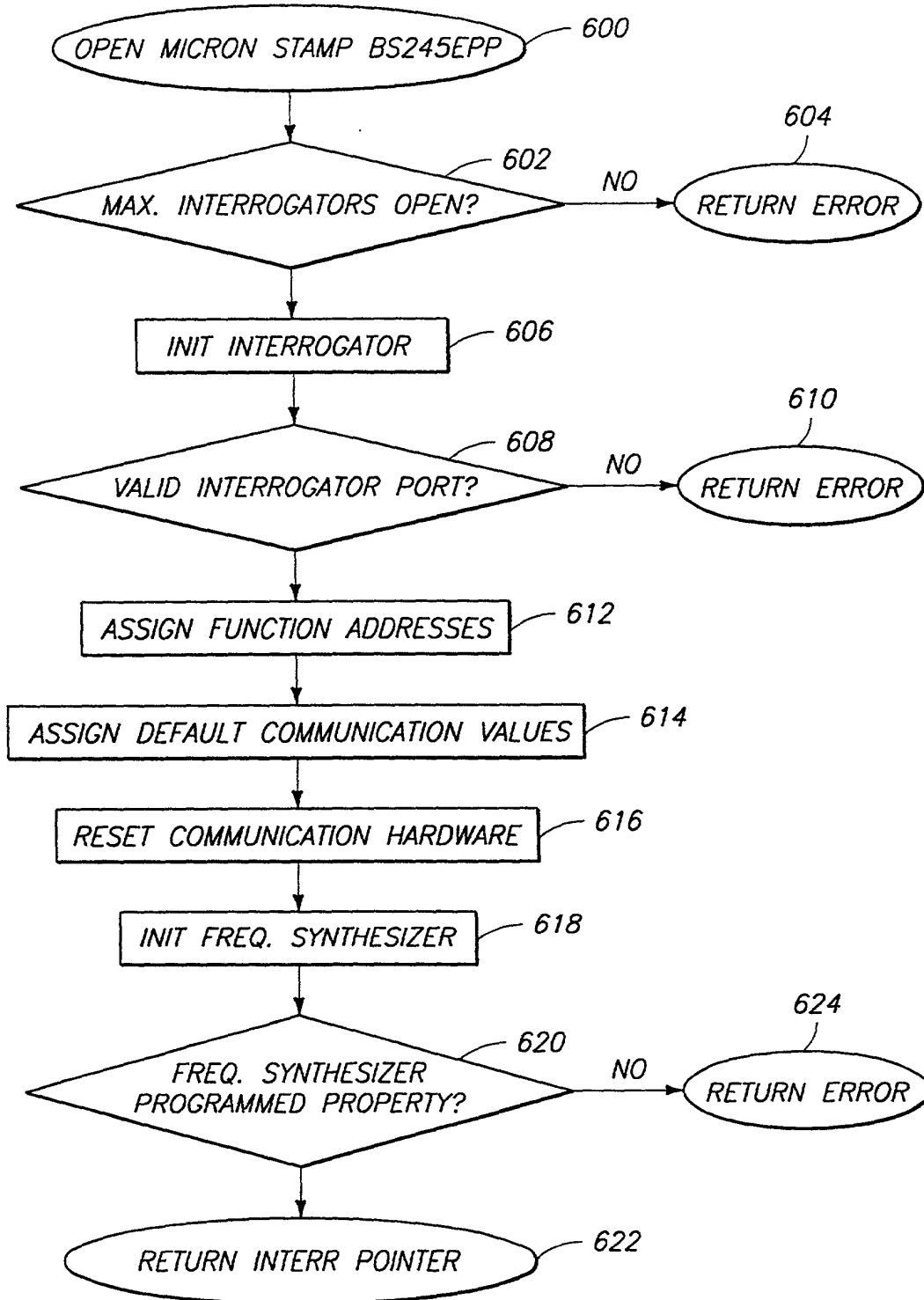


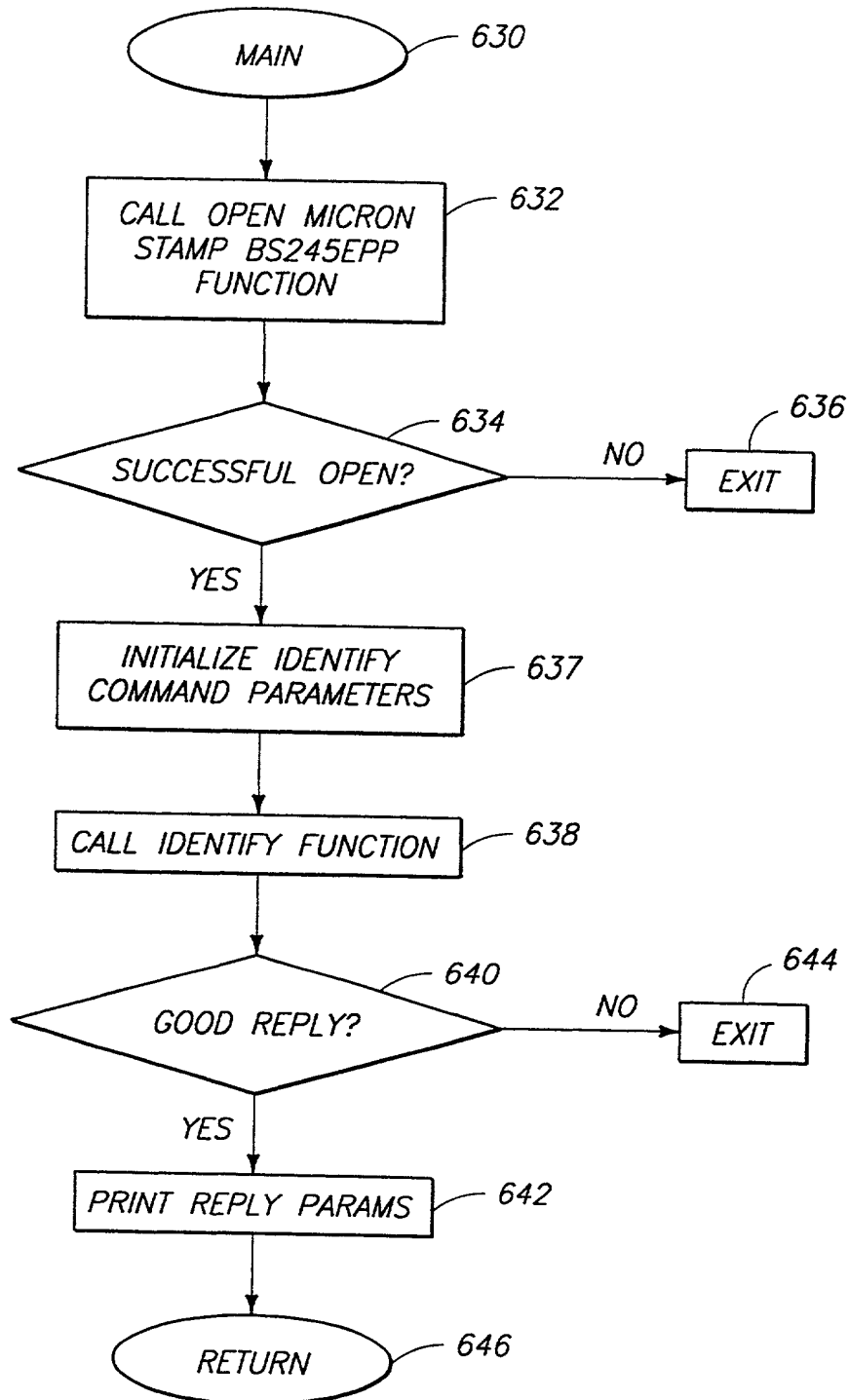


IEEE 561B

IEEE 57A







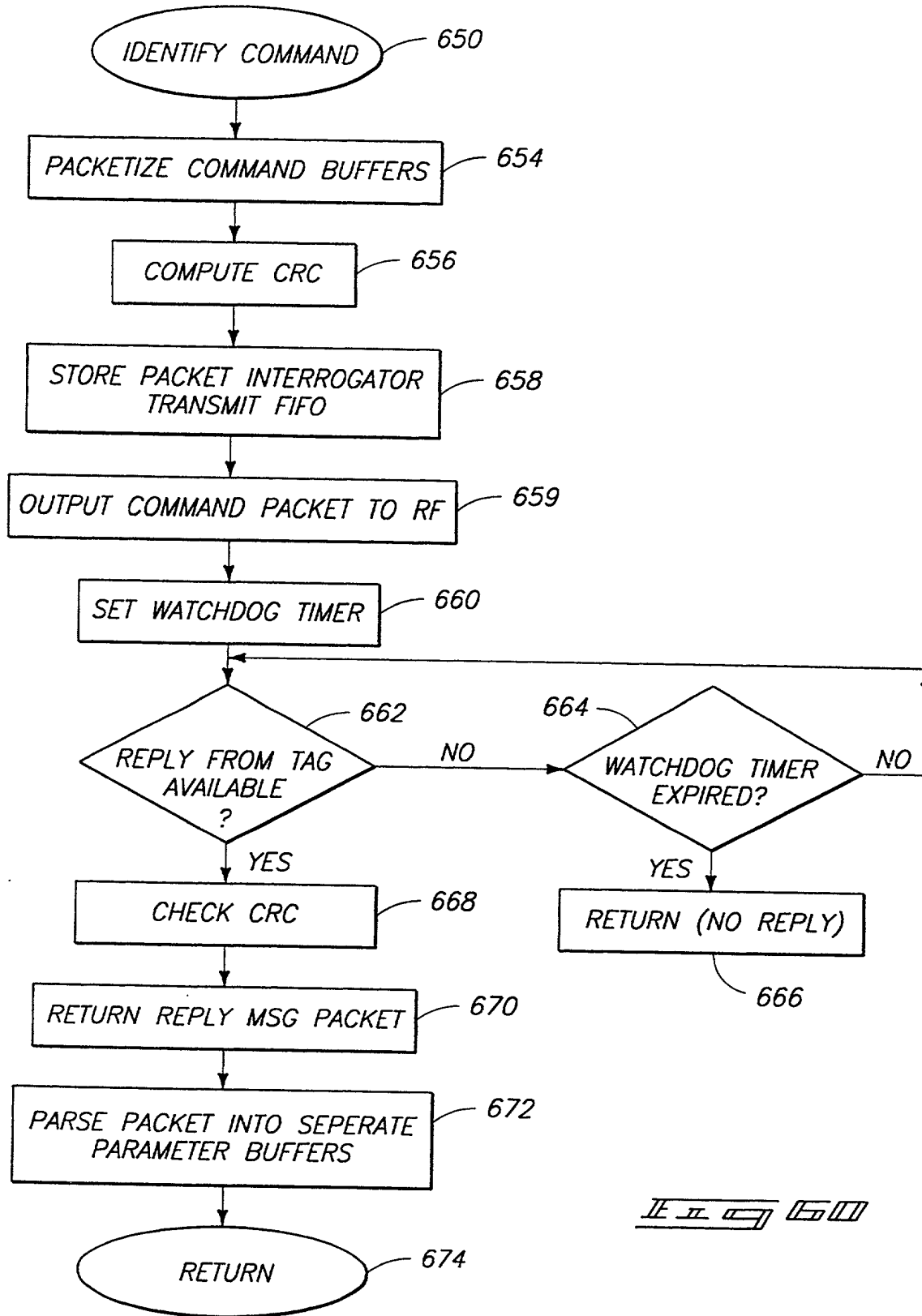
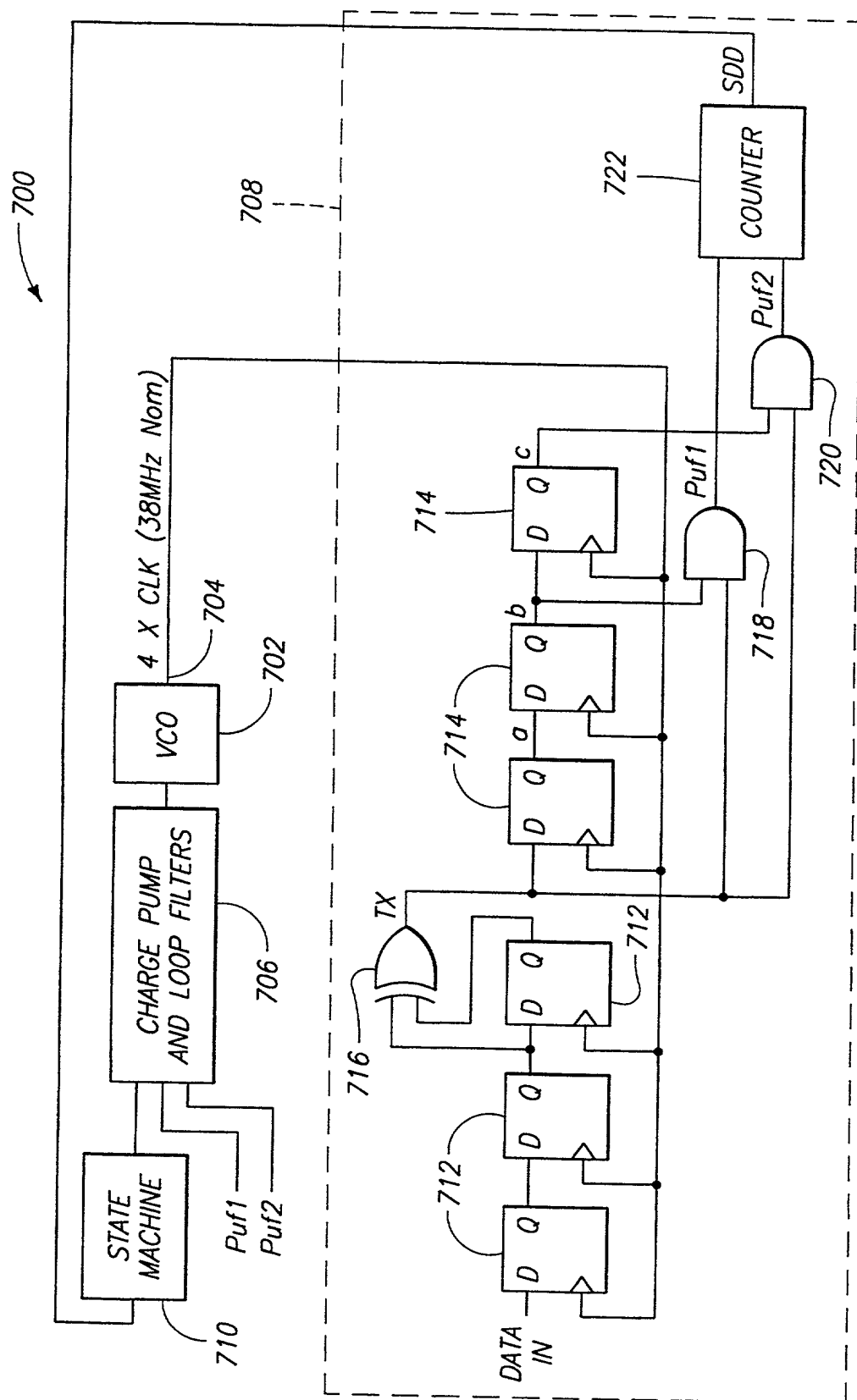
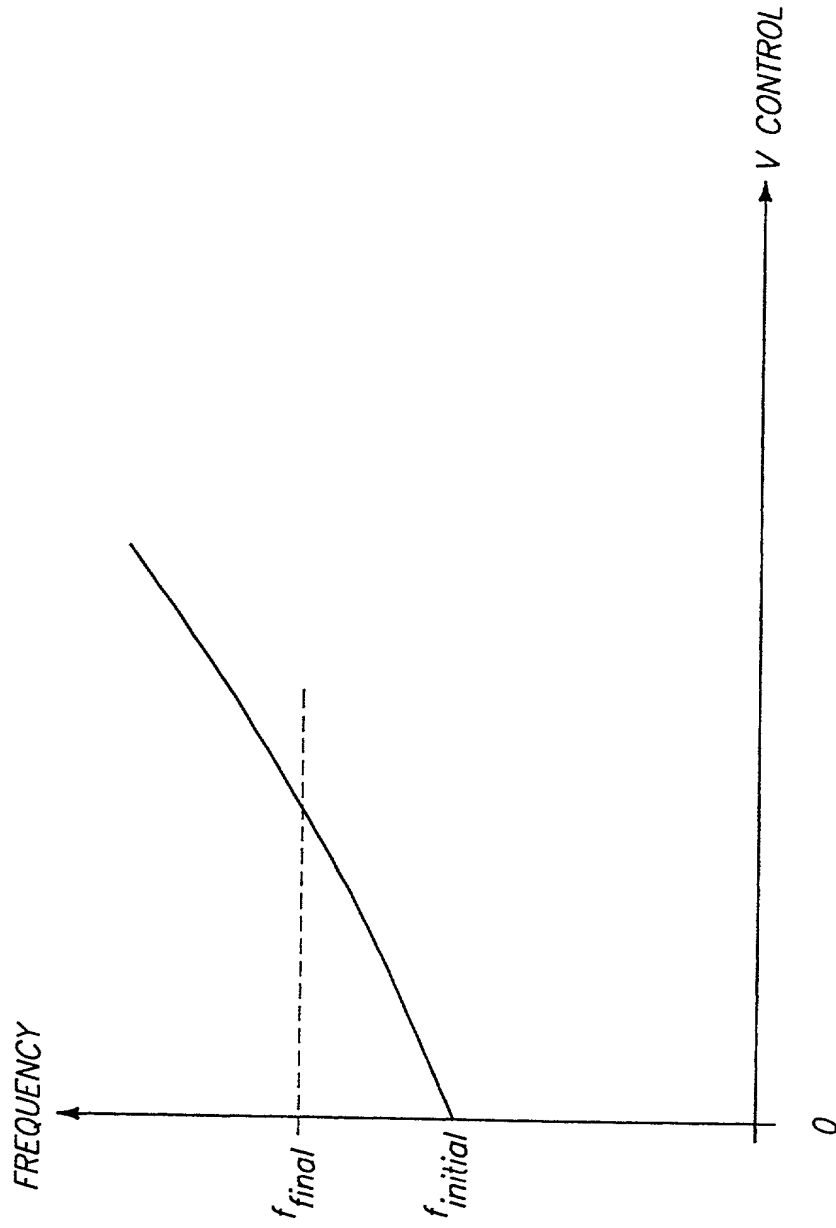


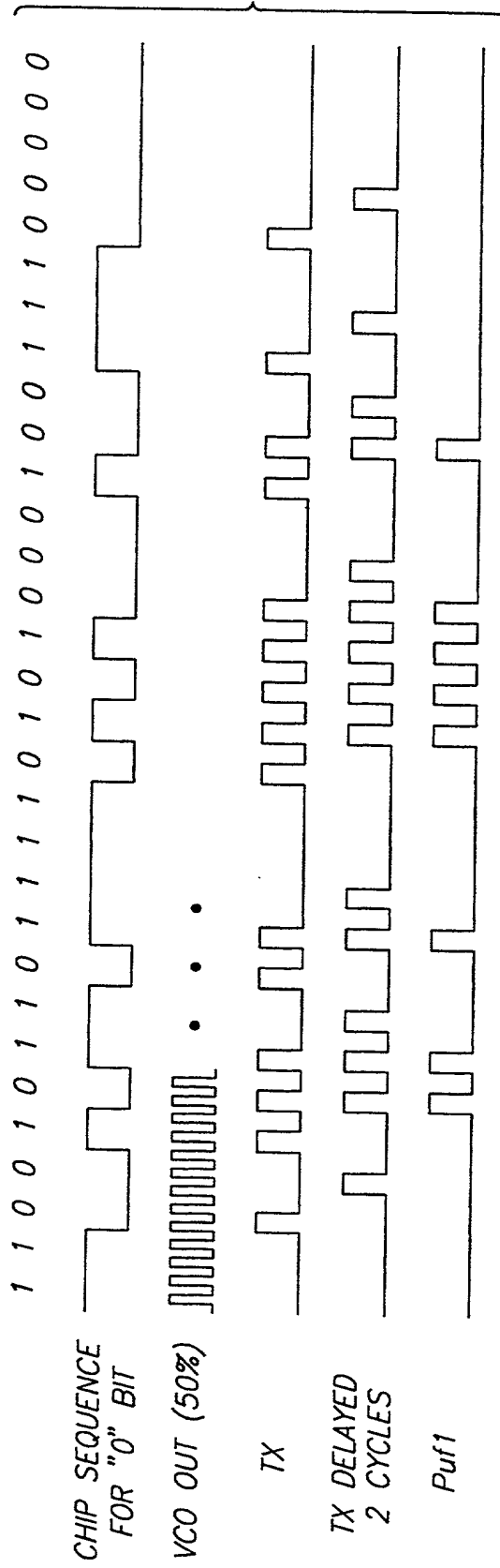
FIG. 60



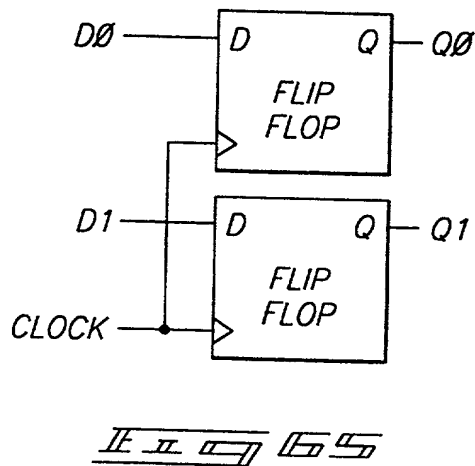
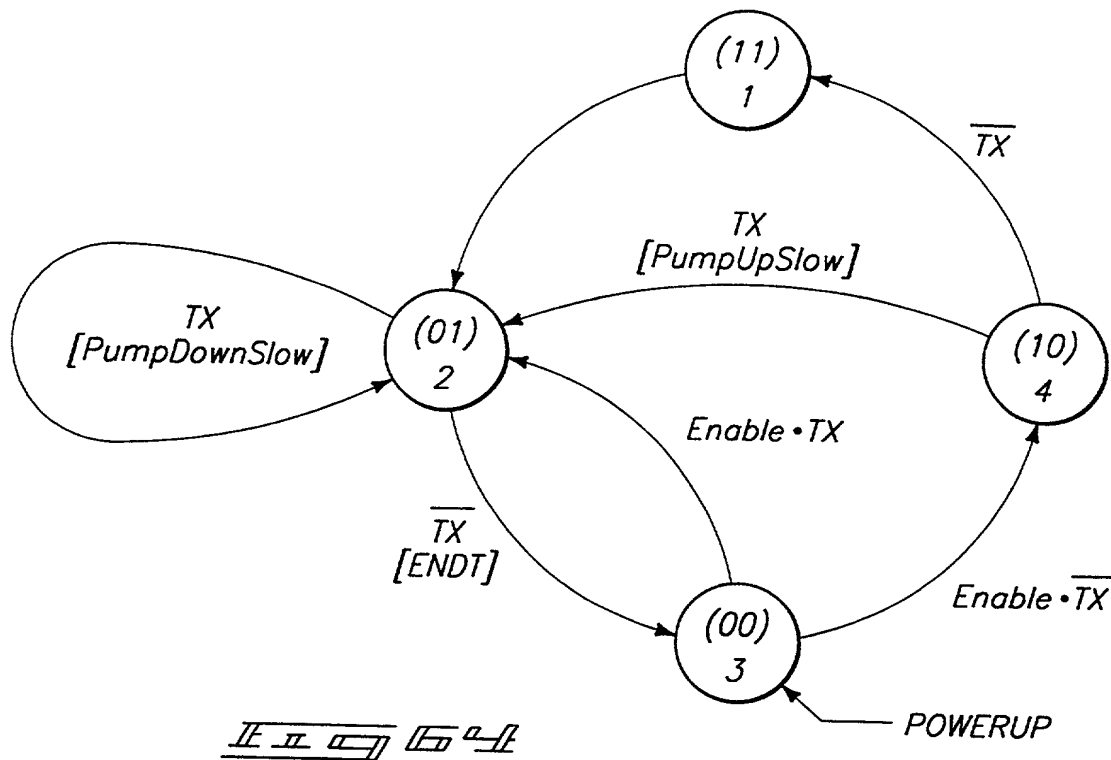




EXERCISE



II II



PRESENT STATE				NEXT STATE		
ENABLE	TX	Q1	Q0	D1	D0	
0	0	0	0	0	0	
0	1	0	0	0	0	
1	0	0	0	1	0	
1	1	0	0	0	1	
X	0	0	1	0	0	
X	1	0	1	0	1	
X	X	1	1	0	1	
X	0	1	0	1	1	
X	1	1	0	0	1	

11 01 66

*En TX*  $Q1\ Q0$

	00	01	11	10
00	0	0	1	1
01	0	1	1	1
11	1	1	1	1
10	0	0	1	1

*D0:*

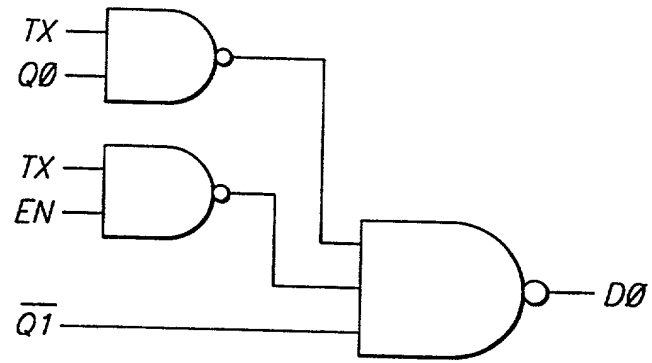
IE II III IV V

*En TX*  $Q1\ Q0$

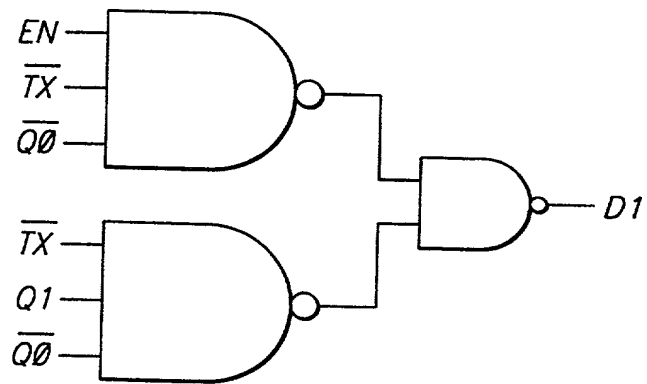
	00	01	11	10
00	0	0	0	1
01	0	0	0	0
11	0	0	0	0
10	1	0	0	1

*D1:*

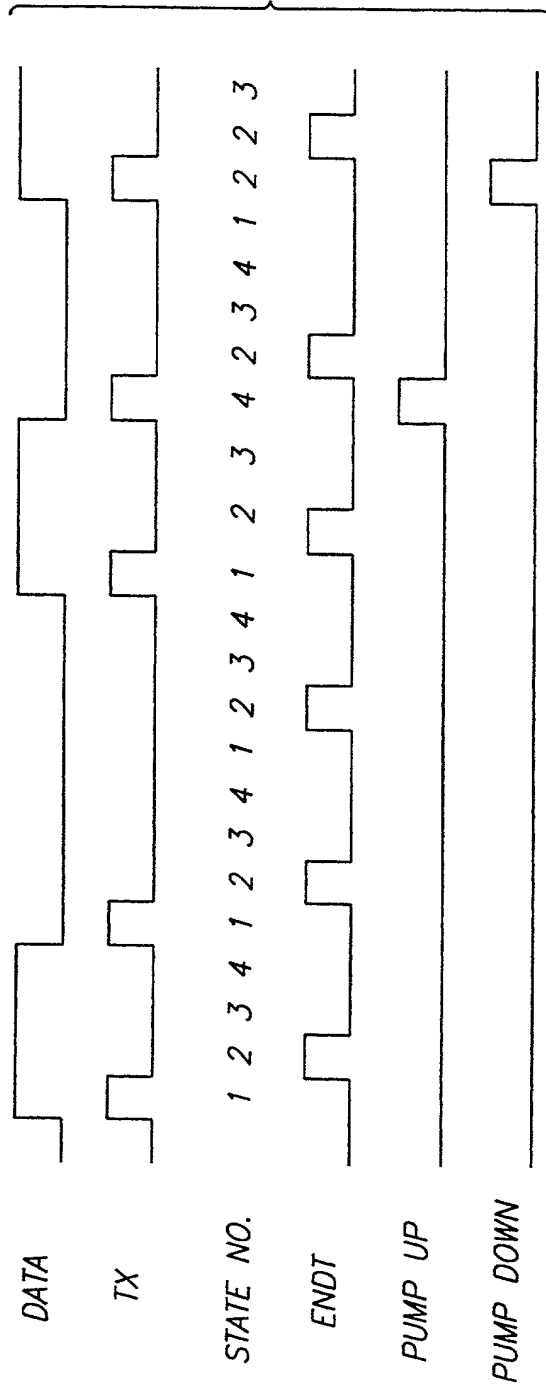
IE II III IV V



II II II II II



II II II II II



11 11 11



TABLE 1

NAME	CURRENT ( $\mu A$ )	$\Delta V$ (mV)	$\Delta V/V$ CONTROL(NOM) X 100
COARSE	40	160	13.3%
MEDIUM	10	40	3.3
MEDIUM FINE	1	2.6	0.22
FINE	0.1	0.26	0.022

TABLE 1